The McGraw-Hill Computer Handbook

Editor In Chief
Harry Helms

The McGraw-Hill Computer Handbook

Editor in Chief
Harry Helms

Overview by Adam Osborne

Thomas C. Bartee

McGraw-Hill Book Company

New York St. Louis San Francisco Auckland Bogotá Hamburg Johannesburg London Madrid Mexico Montreal New Delhi Panama Paris São Paulo Singapore Sydney Tokyo Toronto

Library of Congress Cataloging in Publication Data

Main entry under title:

The McGraw-Hill computer handbook.

Includes index.

- 1. Computers Handbooks, manuals, etc.
- 2. Programming (Electronic computers) Handbooks, manuals, etc. 3. Programming languages (Electronic computers) Handbooks, manuals, etc. I. Helms, Harry L. II. McGraw-Hill Book Company.

 QA76.M37 1983 001.64 83-1044

 ISBN 0-07-027972-1

Copyright © 1983 McGraw-Hill, Inc. All rights reserved. Printed in the United States of America. Except as permitted under the United States Copyright Act of 1976, no part of this publication may be reproduced or distributed in any form or by any means, or stored in a data base or retrieval system, without the prior written permission of the publisher.

1234567890 KGP/KGP 89876543

I-579750-70-0 NBZI

The editors for this book were Patricia Allen-Browne and Margaret Lamb, the production supervisor was Teresa F. Leaden, and the designer was Mark E. Safran. It was set in Times Roman by University Graphics, Inc.

Printed and bound by The Kingsport Press.

Contributors

Bartee, Thomas C. Harvard University

Conroy, Thomas F. International Business Machines Corp.

Erickson, Jonathan Radio Shack Technical Publications

Gault, James W. North Carolina State University

Gear, C. William University of Illinois

Givone, Donald D. State University of New York at Buffalo

Hamacher, V. Carl University of Toronto

Hellerman, Herbert State University of New York at Binghamton

Helms, Harry L. Technical Writer and Consultant

Hohenstein, C. Louis Hohenstein and Associates

House, Charles H. Hewlett-Packard

Kohavi, Zvi University of Utah

Koperda, Frank International Business Machines Corp.

Miastkowski, Stan Rising Star Industries, Inc.

Newman, William M. Queen Mary College, London

Pimmel, Russell L. University of Missouri

Roesser, Robert P. University of Detroit

Sproull, Robert F. Sutherland, Sproull and Associates

Stout, David F. Dataface

Tucker, Allen, Jr. Georgetown University

Vranesic, Zvonko G. University of Toronto

Wiatrowski, Claude A. Mountain Automation Corp.

Wiederhold, Gio M. Stanford University

Zaky, Safwat G. University of Toronto

Overview

TRENDS IN THE MICROCOMPUTER INDUSTRY

Without a doubt, IBM's Personal Computer strategy cast the shape of the microcomputer industry in 1982— and probably for the rest of the decade. It was not simply sales volume or market share that made IBM's Personal Computer such a formidable factor in 1982. Rather, it was the marketing strategy IBM adopted. It is a strategy all other microcomputer industry participants who wish to survive will have to adopt.

Prior to the IBM Personal Computer, industry leaders (such as Apple, Commodore, and Radio Shack) all strove to build unique hardware which, whenever possible, would run programs that could not be run on any competing machine. Furthermore, these companies vigorously fought competitors attempting to build look-alike microcomputers.

That was the old minicomputer industry philosophy, adopted by too many microcomputer manufacturers. Well in advance of IBM's entry, the ultimate fallacy of this philosophy was evident. The reason was CP/M, an operating system standard on none of the three leading personal computers (Apple, Commodore, and Radio Shack). Nevertheless, CP/M not only survived but thrived. CP/M was kept alive by more than the flock of secondary microcomputer manufacturers. A large number of Radio Shack and Apple microcomputers were also running CP/M, even though it required additional expense for unauthorized software and additional hardware for the Apple.

Was there a message in the extremes to which customers would go to thwart the intent of microcomputer manufacturers and run CP/M? Indeed there was, and it was that de facto industry standards are overwhelmingly desirable to most customers.

That message was not lost on IBM. Few messages of validity are; that is why IBM has grown to be so successful.

And so when IBM introduced its Personal Computer, it went about making its entry a new de facto standard. Any hardware manufacturer who wanted to could build a product compatible with the IBM Personal Computer. Software vendors were given every encouragement to adopt the IBM standard. The first

independent microcomputer magazine dedicated to the IBM Personal Computer grew to be one of the most successful magazines in the business within six months of its first publication. People bought the magazine and advertised in it in unprecedented volumes.

There are already several microcomputers built by companies other than IBM that are compatible with the IBM Personal Computer. Already there are probably more software companies devoting themselves to the IBM standard than any other with the possible exception of CP/M.

Within a short time, I predict there will be two de facto industry standards for microcomputers: CP/M running on the Z80 for 8-bit systems and IBM (MDOS and CP/M 86) running on the 8086 or 8088 for 16-bit systems. Companies not supporting one or both of these standards have a tortuous, uphill fight ahead of them.

One can well argue that 8-bit microprocessors are generally obsolete and that the 8086 and 8088 are among the least powerful 16-bit microprocessors. But what has that to do with anything? If these microprocessors are adequate for the tasks they are being asked to perform, then any theoretical inadequacies will not be perceived by the end user. And even if the de facto standards are not the best in whatever area they have become standard, what does it matter providing their shortcomings are not apparent to the user?

The difference between the microcomputer and minicomputer industries is that microcomputers are rapidly becoming consumer products. It will be far more difficult for microcomputer industry managers to impose their will on a mass market of consumer buyers than it was for minicomputer manufacturers to manipulate a relatively small customer base (which was commonly done in the early 1970s).

This message has not been learned by many present participants in the microcomputer industry. But this message will determine more than anything else who will be the survivors when the inevitable industry shakeout occurs.

Adam Osborne

President
Osborne Computer Corporation
1983

Foreword

Since the 1950s the digital computer has progressed from a "miraculous" but expensive, rarely seen, and overheated mass of vacuum tubes, wires, and magnetic cores to a familiar, generally compact machine built from hundreds of thousands of minuscule semiconductor devices packaged in small plastic containers.

As a result, computers are everywhere. They run our cash registers, check out our groceries, ignite our spark plugs, and manage the family bank account. Moreover, because of the amazing progress in the semiconductor devices which form the basis for computers, both computers and the applications now being developed are only fragments of what will soon be in existence.

The Computer Handbook which follows presents material from the basic areas of technology for digital computers. The Handbook progresses from hardware through software to such diverse topics as artificial intelligence, robotics, and voice recognition. Microprocessors, the newest addition to computer hardware, are also discussed in some detail.

Computers are beginning to touch the lives of everyone. If you are ill, the hospital will be full of computers (there could be more computers than patients in a modern hospital; medical instrument designers make considerable use of microprocessors). Schools have been using computers for bookkeeping for years and now use computers in many courses outside computer science. Even elementary schools are beginning to use computers in basic math and science courses. Games designed around microprocessors are sold everywhere. New ovens, dishwashers, and temperature control systems all use microprocessors.

The wide range of applications brings up some basic philosophical questions about what the future will bring. System developers can now produce computers which speak simple phrases and sentences reasonably well—bank computers probably give the teller your balance over the telephone in spoken form and some of the newer cars produce spoken English phrases concerning car operation. Computers also listen well but have to work hard to unscramble what is said unless the form of communication is carefully specified. This is, however, a hot research area and some details are in this book.

The speech recognition problem previously described is a part of a larger problem called *pattern recognition*. If computers can become good at finding patterns, they can scan x-rays, check fingerprints, and perform many other use-

ful functions (they already sort mail). The human brain is, however, very good at finding patterns even in the presence of irrelevant data (noise), and research in this area faces many challenges if computers are to become competitive. If, however, computers can become good at recognizing speech and returning answers verbally, it might be possible even to enter programs for computers and data verbally. This would make it possible literally to tell the computer what to do and have the computer respond, provided the directions were clear, contained no contradictions, etc.

While verbal communication might facilitate the programming of a computer, there would still be the problem of what language to use. There are many proponents of English, but English need not be precise and lacks the rigidity now required by the computer and its translators. Certainly much has been done in this area and the steady march from machine-like assemblers to today's high-level programming languages testifies to the need for and emphasis on this area. Much more is needed, however, and the sections of this Handbook fairly represent the state of this art and point to the direction of future work.

Robotics also presents an outstanding area for future development. Factories now use many robotic devices and research labs are beginning to fill with robots in many strange and wonderful forms. Waving aside the possibility and desirability of robots for maids, waitresses, waiters, ticket sales persons, and other functions already exploited by television and movies, there are many medical operations and precision production operations which can and will be performed by computer-guided robots (often because they are better than humans).

We often complain that others do not understand us, and at present computers do not understand us; for a while we will have to be content with computers which will simply follow our directions. Computer memories are making gains on human memories, however. Largely due to the ingenuity of memory device designers, the memory capacity of large machines now competes with our own but the different organization of the brain seems to give it substantial advantages for creative thought. Artificial intelligence delves into this area. In some areas formerly relegated to "human" thought, computers do quite well, however. For example, in such straightforward mathematical systems as Euclid's geometry, computers already perform better than might be expected; in a recent test a computer proved all the theorems in a high school test in minutes.

I think that to be really comfortable with computers it is necessary to have some knowledge of both hardware and software. In order to make computers more widely used, there is a tendency to make consumer-oriented personal computers appear to be "friendlier" than they really are. This limits their flexibility and presents users with a mystery element which needs to be and can be dissolved by a little knowledge of actual computer principles. A handbook such as this can be very helpful to users in dissolving some of the mystery. At the same time, such a handbook can open new doors in exploration and serve as a continuing reference.

Contents

Contributors xi
Overview xiii
Toreword xv

1. Computer History and Concepts 1-1

- 1-1 Introduction 1-1
- 1-2 Historical Perspective 1-2
- 1-3 A Classification of AutomaticComputers 1-5
- 1-4 The Nature f a Computer System 1-6
- 1-5 Principles of Hardware Organization 1-7
- 1-6 Conventions on Use of Storage 1-10
- 1-7 Elements of Programming 1-11
- 1-8 Principles of the Space-Time Relationship 1-13

2. Computer Structures 2-1

- 2-1 Introduction 2-1
- 2-2 Functional Units 2-2
- 2-3 Input Unit 2-5
- 2-4 Memory Unit 2-5
- 2-5 Arithmetic and Logic Unit 2-7
- 2-6 Output Unit 2-8
- 2-7 Control Unit 2-9
- 2-8 Basic Operational Concepts 2-10
- 2-9 Bus Structures 2-12

3. Number Systems and Codes 3-1

- 3-1 Number Systems 3-1
- 3-2 Binary Codes 3-5
- 3-3 Error Detection and Correction 3-8

4. Boolean Algebra and Logic Networks 4-1

- 4-1 Introduction 4-1
- 4-2 Boolean Algebra 4-2
- 4-3 Truth Tables and Boolean Expressions 4-3
- 4-4 Boolean Algebra Theorems 4-7
- 4-5 Using the Boolean Algebra Theorems 4-9
- 4-6 The Karnaugh Map Method of Boolean Simplification 4-13
- 4-7 Logic Networks 4-20
- 4-8 Additional Logic Gates 4-21

5. Sequential Networks 5-1

- 5-1 Introduction 5-1
- 5-2 The Flip-Flop Element 5-1
- 5-3 State Tables and State Diagrams 5-6
- 5-4 Converting a State Table into a LogicDiagram 5-10
- 5-5 Converting a Logic Diagram into a State Table 5-14
- 5-6 Design Examples 5-18
- 5-7 Important Sequential Networks 5-26

6. The Arithmetic-Logic Unit 6-1

- 6-1 Introduction 6-1
- 6-2 Construction of the ALU 6-2
- 6-3 Integer Representation 6-3

6-4 The Binary Half Adder 6-4

	•	
	6-5	The Full Adder 6-5
	6-6	A Parallel Binary Adder 6-7
	6-7	Positive and Negative Numbers 6-8
	6-8	Positive and Negative Numbers 6-8 Addition in the 1S Complement
		System 6-9
	6-9	Addition in the 2S Complement
	0,	System 6-10
	6 10	Addition and Subtraction in a Parallel
	0-10	Arithmetic Element 6-12
	<i>c</i> 11	
	0-12	The Binary-Coded-Decimal (BCD)
		Adder 6-16
	6-13	Positive and Negative BCD
		Numbers 6-19
	6-14	Addition and Subtraction in the 9S
	6	Complement System 6-19
	6-15	The Shift Operation 6-24
	6-16	Basic Operations 6-25
		Binary Multiplication 6-27
		Decimal Multiplication 6-31
		Division 6-32
		Logical Operations 6-37
		Floating-Point Number Systems 6-40
		Performing Arithmetic Operations with
	0-22	
		Floating-Point Numbers 6-44
7	The	Memory Flement 7-1
7.	The	Memory Element 7-1
7.	The 7-1	Memory Element 7-1 Introduction 7-2
7.		Introduction 7-2
7.	7-1	Introduction 7-2
7.	7-1 7-2	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory
7.	7-1 7-2	Introduction 7-2 Random-Access Memories 7-3
7.	7-1 7-2 7-3	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9
7.	7-1 7-2 7-3 7-4 7-5	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11
7.	7-1 7-2 7-3	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a
7.	7-1 7-2 7-3 7-4 7-5 7-6	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16
7.	7-1 7-2 7-3 7-4 7-5	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor
7.	7-1 7-2 7-3 7-4 7-5 7-6	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core Memory 7-42 Timing Sequence 7-44
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core Memory 7-42 Timing Sequence 7-44
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core Memory 7-42 Timing Sequence 7-44 Driving the X- and Y-Selection
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13 7-14	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core Memory 7-42 Timing Sequence 7-44 Driving the X- and Y-Selection Lines 7-46
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13 7-14	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-22 Static MOS Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core Memory 7-42 Timing Sequence 7-44 Driving the X- and Y-Selection Lines 7-46 Memory Buffer Register and Associated
7.	7-1 7-2 7-3 7-4 7-5 7-6 7-7 7-8 7-9 7-10 7-11 7-12 7-13 7-14 7-15 7-16	Introduction 7-2 Random-Access Memories 7-3 Linear-Select Memory Organization 7-5 Decoders 7-9 Dimensions of Memory Access 7-11 Connecting Memory Chips to a Computer Bus 7-16 Random-Access Semiconductor Memories 7-21 Bipolar IC Memories 7-22 Static MOS Memories 7-26 Dynamic Memories 7-29 Read-Only Memories 7-31 Magnetic Core Storage 7-37 Storage of Information in Magnetic Cores in a Two-Dimensional Array 7-40 Assembly of Core Planes into a Core Memory 7-42 Timing Sequence 7-44 Driving the X- and Y-Selection Lines 7-46

Schemes 7-49

	7-19 Magnetic Drum Storage 7-51
	7-20 Parallel and Serial Operation of a
	Magnetic Drum 7-53
	7-21 Magnetic Disk Memories 7-55
	7-22 Flexible Disk Storage Systems—the
	Floppy Disk 7-60
	7-23 Magnetic Tape 7-63
	7-24 Tape Cassettes and Cartridges 7-69
	7-25 Magnetic Bubble and CCD
	Memories 7-71
	7-26 Digital Recording Techniques 7-72
	7-27 Return-to-Zero and Return-to-Bias
	Recording Techniques 7-73
	7-28 Non-Return-to-Zero Recording
	Techniques 7-75
	Toomiques
8.	Software 8-1
	8-1 Introduction 8-1
	8-2 Languages and Translators 8-2
	8-3 Loaders 8-4
	8-4 Linkers 8-7
	8-5 Operating Systems 8-10
	or opening systems
_	
9.	Input, Output, and Secondary
	Storage Devices 9-1
	9-1 Introduction 9-1
	9-2 Input-Output Devices 9-2
	9-3 Long-Term Storage and Intermediate
	Input-Output 9-8
	9-4 Medium-Term Storage Devices 9-16
	9-5 Speed and Capacity Comparisons 9-19
	7-5 Speed and Capacity Comparisons 7-17
	•
10.	Timesharing Systems 10-1
	10-1 Introduction 10-1
	10-2 The User Viewpoint and Some
	Consequences 10-3
	10-3 Choice of Time Slice 10-9
	10-4 The MIT CTSS System 10-10
	10-5 The APL System 10-12
	10-6 Performance Measurement 10-16
	10-7 A Timesharing System Simulator
	10-19
	10-8 IBM Timesharing Option (TSO) For
	System/360/370 10-28
	10-9 The G.E. Information Service
	Network 10-35
	TOURDIN 10-55

11. Assembly and System Level
Programming 11-1

11-1 Introduction 11-1

Load 11-2

11-2 The Raw Machine: Initial Program

	11-3 The Assembler 11-4	15.	FORTRAN 15-1
	11-4 Relocating Loaders and Linkage		15-1 Introduction 15-1
	Editors 11-26		
	11-5 The Library 11-34		15-2 Program Format 15-2 15-3 Key Punch and Terminal Entry 15-3
	11-6 Other Translators 11-35		,,,,,,,,,,,,
	11-7 Running the Program,		15-4 Constants and Variables 15-3
	Monitoring 11-35		15-5 Type Declarations 15-4
	11-8 Task and Job Scheduling,		15-6 Arrays 15-5
	Commands 11-38		15-7 Assignment of Values 15-5
	2		15-8 Arithmetic Operators 15-6
			15-9 Relational Operators 15-6
40			15-10 The Equals Symbol 15-7
	Survey of High-Level Programming		15-11 Control and Transfer Statements
l	Languages 12-1		15-7
	12-1 Introduction 12-1		15-12 Subprograms 15-10
	12-2 Development of High-Level		15-13 Intrinsic Functions 15-13
20			15-14 Parameters 15-15
	123 High-Level Language		15-15 Naming Programs 15-15
	Descriptions 12-3		15-16 Character Manipulation 15-16
	12-4 Summary 12-5		15-17 Equivalence Operators 15-17
	12-4 Summary 12-3		15-18 File Organization 15-17
			15-19 List-Directed (Stream) Input/
			Output 15-18
13. 1	BASIC 13-1		15-20 Formatted Input/Output 15-18
			15-21 Reserved Words 15-21
	13-1 Introduction 13-1		15 21
	13-2 System Commands 13-2		
	13-3 Program Structure 13-5		· ·
	3-4 Variables and Constants 13-5	16.	Pascal 16-1
. 1	3-5 Arithmetic Operators 13-7		
	3-6 Relational Operators 13-8		16-1 Introduction 16-1
	3-7 Logical Operators 13-8		16-2 Program Structure 16-2
1	3-8 Order of Operations 13-8		16-3 Identifiers 16-3
1	3-9 Program Logic and Control 13-9		16-4 Data Types 16-3
1	3-10 Subroutines 13-11		16-5 Definitions and Declarations 16-3
1	3-11 Numeric Functions 13-12		16-6 Arrays 16-4
1	3-12 String Functions 13-13		16-7 Assignment Operations 16-5
1	3-13 Assembly Language Statements and		16-8 Relational Operators 16-5
	Routines 13-15		16-9 Control Statements 16-6
1	3-14 Graphics Statements 13-16		16-10 Functions and Procedures 16-8
	3-15 Input and Output Statements 13-18		16-11 Predefined Functions 16-9
1	3-16 Specialized Input and Output		16-12 Input and Output 16-9
	Statements 13-20		16-13 Packed Arrays 16-10
1	3-17 Reserved Words 13-21		16-14 Sets 16-10
	15 21		16-15 Files and Records 16-11
			16-16 Reserved Words 16-12
14. C	COBOL 14-1		
1	4-1 Introduction 14-1		
	4-2 Writing COBOL Programs 14-3	17.	PL/I 17-1
1	4-3 COBOL Statements 14-16		17.1 Introduction 17.1
	4-4 Input-Output Conventions 14-37		17-1 Introduction 17-1
			17-2 Writing PL/I Programs 17-3
	4-5 Subprograms 14-43		17-3 Basic Statements 17-14
	4-6 Complete Programs 14-47		17-4 Input-Output Conventions 17-32
	4-7 Additional Features 14-48		17-5 Subprograms 17-48
1;	4-8 Comparison of 1968 and 1974		17-6 Complete Programs 17-62
	Standards 14-54		17-7 Additional Features 17-63

18.	Hardware and Software Documentation 8-1		20-19 The Storage-Tube Display 20-31 20-20 The Refresh Line-Drawing Display 20-34		
	18-1 Introduction 18-1 18-2 Characteristics of Good Documentation 18-2 18-3 Types of Documentation 18-2	21.	Artificial Intelligence and Robotics 21-1		
	 18-4 Reference Documentation 18-3 18-5 Tutorial Documentation 18-3 18-6 Developing Documentation 18-4 		21-1 Introduction 21-1 21-2 Computers and Intelligence 21-1 21-3 Expert Systems 21-3 21-4 Robotics 21-3 21-5 Tasks in Robotics 21-4		
19.	Databases and File-System Organization 19-1		21-6 Robots and Personal Computers 21-5		
	19-1 Introduction 19-1	22.	Character Printers 22-1		
	19-2 Files 19-2 19-3 Computations on a Database 19-4 19-4 A Hierarchical View of Data 19-6 19-5 Current Practice 19-11 19-6 Descriptions 19-14 19-7 Binding 19-16 19-8 Classification of Operating Systems 19-18 19-9 Applications 19-21 19-10 File-System Organization 19-22 19-11 The Pile 19-27 19-12 The Sequential File 19-33 19-13 The Indexed-Sequential File 19-40 19-14 The Indexed File 19-57 19-15 The Direct File 19-68 19-16 The Multiring File 19-84		 22-1 Introduction 22-1 22-2 Classification of Printers 22-2 22-3 Printer Character Set 22-3 22-4 Character-At-A-Time Impact Printers for Fully Formed Characters 22-5 22-5 Line-At-A-Time Impact Printers for Fully Formed Characters 22-9 22-6 Line-At-A-Time Nonimpact Printers for Fully Formed Characters 22-11 22-7 Dot-Matrix Printers 22-13 22-8 Character-At-A-Time Impact Dot-Matrix Printers 22-16 22-9 Line-At-A-Time Impact Dot-Matrix Printers 22-19 22-10 Nonimpact Dot-Matrix Printers 22-20 22-11 Paper Handling 22-24 		
20.	Computer Graphics 20-1		Overhier Blattern 2014		
	 20-1 Introduction 20-1 20-2 The Origins of Computer Graphics 20-4 20-3 How the Interactive-Graphics Display Works 20-5 20-4 Some Common Questions 20-6 20-5 New Display Devices 20-8 20-6 General-Purpose Graphics Software 20-9 20-7 The User Interface 20-10 20-8 The Display of Solid Objects 20-10 20-9 Point-Plotting Techniques 20-11 20-10 Coordinate Systems 20-12 20-11 Incremental Methods 20-13 20-12 Line-Drawing Algorithms 20-14 20-13 Circle Generators 20-19 20-14 Line-Drawing Displays 20-21 20-15 Display Devices and Controllers 20-22 20-16 Display Devices 20-23 20-17 The CRT 20-23 		Graphics Plotters 23-1 23-1 Introduction 23-1 23-2 Marking and Scanning Methods 23-2 23-3 Paper Motion 23-3 23-4 Tabletop and Free-Standing Plotters 23-6 23-5 Range of Physical Capabilities 23-6 23-6 Dedicated-Use Plotters: Automatic Dafting Systems 23-8 23-7 Plotting Programs 23-9 Survey of Microprocessor Technology 24-1 24-1 The Evolution of the Microprocessor 24-1 24-2 The Elements of a Microprocessor 24-3 24-3 Microprocessor and Microcomputer Software 24-10 24-4 Fundamentals of Microprocessor		
	20-18 Inherent-Memory Devices 20-28		System Design 24-15		

25.	Microcomputers and				
	Programming	2	5-1		

- 25-1 Introduction 25-1
- 25-2 Program Planning 25-4
- 25-3 Data Types 25-8
- 25-4 Data Storage 25-11
- 25-5 Instructions 25-11
- 25-6 8080/8085 Microcomputer Organization 25-12
- 25-7 Assembly Language 25-16
- 25-8 Data Movement Instructions 25-19
- 25-9 Boolean Manipulation Instructions 25-25
- 25-10 Rotate Instructions 25-28
- 25-11 Branching Instructions 25-31
- 25-12 A Logic Controller Example 25-37
- 25-13 Another Approach to the Logic Controller 25-43

26. Subroutines, Interrupts, and Arithmetic 26-1

- 26-1 Subroutines 26-1
- 26-2 Interrupts 26-6
- 26-3 Additional Pseudoinstructions 26-11
- 26-4 Manual-Mode Logic Controller Example 26-12
- 26-5 Arithmetic 26-14
- 26-6 Some Additional Data Movement Instructions 26-24
- 26-7 Programmed Arithmetic Operations 26-25

27. Interfacing Concepts 27-1

- 27-1 Introduction 27-1
- 27-2 Input/Output Ports 27-2
- 27-3 Handshaking 27-6
- 27-4 Program Interrupts 27-11
- 27-5 Main-Memory Interfacing 27-21
- 27-6 Direct Memory Access 27-31
- 27-7 Further Microprocessor Bus Concepts 27-34
- 27-8 Analog Conversion 27-36
- 27-9 Serial I/O 27-9
- 27-10 Bit-Slice Microprocessors 27-55
- 27-11 Microprocessor Clocks 27-61

28. Microcomputer Operating Systems 28-1

- 28-1 Introduction 28-1
- 28-2 Operating System Components 28-1
- 28-3 Specific Problems 28-2
- 28-4 Micros vs. Mainframes 28-2
- 28-5 The Portability/Compatibility Problem 28-3
- 28-6 CP/M 28-4
- 28-7 Advanced 8-Bit Operating Systems 28-7
- 28-8 16-Bit Operating Systems 28-8
- 28-9 Conclusions 28-9

29. Audio Output: Speech and Music 29-1

- 29-1 Introduction 29-1.
- 29-2 Audio-Response Units 29-1
- 29-3 Music and Sound Synthesizer 29-3
- 29-4 Speech Synthesizers 29-3

30. Voice Recognition 30-1

- 30-1 Historical Overview 30-1
- 30-2 System Goals 30-2
- 30-3 System Overview 30-3
- 30-4 Input-Signal Processing 30-4
- 30-5 Data Compression 30-6
- 30-6 Discrete Wood-Boundary Determination 30-8
- 30-7 Definitions of Linguistic Terms 30-11
- 30-8 Pattern Detection within a Word 30-12
- 30-9 Pattern Parameters and Analysis 30-14
- 30-10 Word-Identification Techniques 30-21
- 30-11 Hardware Implementation 30-22
- 30-12 Status 30-23

Glossary G-1 Index follows Glossary



Computer History and Concepts

Herbert Hellerman

- 1-1 INTRODUCTION
- 1-2 HISTORICAL PERSPECTIVE
- 1-3 A CLASSIFICATION OF AUTOMATIC COMPUTERS
- 1-4 THE NATURE OF A COMPUTER SYSTEM
- 1-5 PRINCIPLES OF HARDWARE ORGANIZATION
- 1-6 CONVENTIONS ON USE OF STORAGE
- 1-7 ELEMENTS OF PROGRAMMING
- 1-8 PRINCIPLES OF THE SPACE-TIME RELATIONSHIP

1-1 INTRODUCTION

The modern general-purpose digital computer system, which is the subject of this book, is the most versatile and complex creation of mankind. Its versatility follows from its applicability to a very wide range of problems, limited only by human ability to give definite directions for solving a problem. A **program** gives such directions in the form of a precise, highly stylized sequence of statements detailing a problem-solution procedure. A computer system's job is to reliably and rapidly execute programs. Present speeds are indicated by the rates of arithmetic operations such as addition, subtraction, and comparison, which lie in the range of about 100,000 to 10,000,000 instructions per second, depending on the size and cost of the machine. In only a few hours, a modern large computer can do more information processing than was done by all of mankind before the electronic age, which began about 1950! It is no wonder that this tremendous amplification of human information-processing capability is precipitating a new revolution.

Adapted from Digital Computer Systems Principles, 2d ed., by Herbert Hellerman. Copyright © 1973. Used by permission of McGraw-Hill, Inc. All rights reserved.

To most people, the words "computer" and "computer system" are probably synonymous and refer to the physical equipment, such as the central processing unit, console, tapes, disks, card reader, and printers visible to anyone visiting a computer room. Although these devices are essential, they make up only the visible "tip of the iceberg." As soon as we start to use a modern computer system, we are confronted not by the machine directly but by sets of rules called programming languages in which we must express whatever it is we want to do. The central importance of programming language is indicated by the fact that even the physical computer may be understood as a hardware interpreter of one particular language called the machine language. Machine languages are designed for machine efficiency, which is somewhat dichotomous with human convenience. Most users are shielded from the inconveniences of the machine by one or more languages designed for good man-machine communication. The versatility of the computer is illustrated by the fact that it can execute translator programs (called generically compilers or interpreters) to transform programs from user-oriented languages into machine-language form.

It should be clear from the discussion thus far that a computer system consists of a computer machine, which is a collection of physical equipment, and also programs, including those that translate user programs from any of several languages into machine language. Most of this book is devoted to examining in some detail theories and practices in the two great themes of computer systems: equipment (hardware) and programming (software). It is appropriate to begin, in the next section, by establishing a historical perspective.

1-2 HISTORICAL PERSPECTIVE

Mechanical aids to counting and calculating were known in antiquity. One of many ancient devices, the abacus, survives today as a simple practical tool in many parts of the world, especially the East, for business and even scientific calculations. (A form of the abacus was probably used by the ancient Egyptians, and it was known in China as early as the sixth century B.C.) In the hands of a skilled operator, the abacus can be a powerful adjunct to hand calculations. There are several forms of abacus; they all depend upon a positional notation for representing numbers and an arrangement of movable beads, or similar simple objects, to represent each digit. By moving beads, numbers are entered, added, and subtracted to produce an updated result. Multiplication and division are done by sequences of additions and subtractions.

Although the need to mechanize the arithmetic operations received most of the attention in early devices, storage of intermediate results was at least as important. Most devices, like the abacus, stored only the simple current result. Other storage was usually of the same type as used for any written material, e.g., clay tablets and later paper. As long as the speed of operations was modest and the use of storage also slow, there was little impetus to seek mechanization of the control of sequences of operations. Yet forerunners of such control did appear in somewhat different contexts, e.g., the Jacquard loom exhibited in 1801 used perforated (punched) cards to control patterns for weaving.

Charles Babbage (1792–1871) was probably the first to conceive of the essence of the general-purpose computer. Although he was very versatile, accomplished both as a mathematician and as an engineer, his lifework was his computing machines. It is worth noting that Babbage was first stimulated in this direction because of the unreliability of manual computation, *not* by its slow speed. In particular, he found several errors in certain astronomy tables. In determining the causes, he became convinced that error-free tables could be produced only by a machine that would accept a description of the computation by a human being but, once set up, would compute the tables and print them—all without human intervention. Babbage's culminating idea, which he proposed in great detail, was his Analytic Engine, which would have been the first general-purpose computer. It was not completed because he was unable to obtain sufficient financial support.

As Western industrial civilization developed, the need for mechanized computation grew. As the 1890 census approached in the United States, it became clear that if new processes were not developed, the reduction of the data from one census would not be complete before it was time for the next one. Dr. Herman Hollerith applied punched cards and simple machines for processing them in the 1890 census. Thereafter, punched-card machines gained wide acceptance in business and government.

The first third of the twentieth century saw the gradual development and use of many calculating devices. A highly significant contribution was made by the mathematician Alan Turing in 1937, when he published a clear and profound theory of the nature of a general-purpose computing scheme. His results were expressed in terms of a hypothetical "machine" of remarkable simplicity, which he indicated had all the necessary attributes of a general-purpose computer. Although Turing's machine was only a theoretical construct and was never seriously considered as economically feasible (it would be intolerably slow), it drew the attention of several talented people to the feasibility of a general-purpose computer.

World War II gave great stimulus to improvement and invention of computing devices and the technologies necessary to them. Howard Aiken and an IBM team completed the Harvard Mark I electric computer (using relay logic) in 1944. J. P. Eckert and J. W. Mauchly developed ENIAC, an electronic computer using vacuum tubes in 1946. Both these machines were developed with scientific calculations in mind. The first generation of computer technology began to be mass-produced with the appearance of the UNIVAC I in 1951. The term "first generation" is associated with the use of vacuum tube as the major component of logical circuitry, but it included a large variety of memory devices such as mercury delay lines, storage tubes, drums, and magnetic cores, to name a few.

The second generation of hardware featured the transistor (invented in 1948) in place of the vacuum tube. The solid-state transistor is far more efficient than the vacuum tube partly because it requires no energy for heating a source of electrons. Just as important, the transistor, unlike the vacuum tube, has almost unlimited life and reliability and can be manufactured at much lower cost. Second-generation equipment, which appeared about 1960, saw the widespread installation and use of general-purpose computers. The third and fourth gen-

erations of computer technology (about 1964 and 1970) mark the increasing use of integrated fabrication techniques, moving to the goal of manufacturing most of a computer in one automatic continuous process without manual intervention.

Hardware developments were roughly paralleled by progress in programming, which is, however, more difficult to document. An early important development, usually credited to Grace Hopper, is the symbolic machine language which relieves the programmer from many exceedingly tedious and error-prone tasks. Another milestone was FORTRAN (about 1955), the first widely used high-level language, which included many elements of algebraic notation, like indexed variables and mathematical expressions of arbitrary extent. Since FORTRAN was developed by IBM, whose machines were most numerous, FORTRAN quickly became pervasive and, after several versions, remains today a very widely used language.

Other languages were invented to satisfy the needs of different classes of computer use. Among the most important are COBOL, for business-oriented data processing; ALGOL, the first widely accepted language in the international community, particularly among mathematicians and scientists; and PL/I developed by IBM and introduced in 1965 as a single language capable of satisfying the needs of scientific, commercial, and system programming.

Along with the introduction and improvements of computer languages, there was a corresponding development of programming technology, i.e., the methods of producing the compiler and interpreter translators and other aids for the programmer. A very significant idea that has undergone intensive development is the **operating system**, which is a collection of programs responsible for monitoring and allocating all systems resources in response to user requests in a way that reflects certain efficiency objectives. By 1966 or so, almost all medium to large computers ran under an operating system. Jobs were typically submitted by users as decks of punched cards, either to the computer room or by **remote-job-entry** (RJE) terminals, i.e., card reader and printer equipment connected by telephone lines to the computer. In either case, once a job was received by the computer, the operating system made almost all the scheduling decisions. A large computer could run several hundred or even thousands of jobs per 24-hour day with only one or two professional operators in the machine room.

The 1960s saw a great intensification of the symbiosis of the computer and the telephone system (teleprocessing). Much of this was RJE and routine non-general-purpose use, such as airline reservation systems. Considerable success was also achieved in bringing the generality and excitement of a general-purpose computer system to individual people through the use of timesharing systems. Here, an appropriate operating-system program interleaves the requests of several human users who may be remotely located and communicating over telephone lines using such devices as a teletype or typewriter terminal. Because of high computer speed relative to human "think" time, a single system could comfortably service 50 to 100 (or more) users, with each having the "feel" of his own private computer. The timesharing system, by bringing people closest to the computer, seems to have very great potential for amplifying human creativity.