# INEAR CIRCUIT ANALYSIS

TIME DOMAIN, PHASOR, AND LAPLACE TRANSFORM APPROACHES

RAYMOND A. DeCARLO / PEN-MIN LIN



# LINEAR CIRCUIT ANALYSIS

TIME DOMAIN, PHASOR, AND LAPLACE TRANSFORM APPROACHES

RAYMOND A. DECARLO Purdue University

PEN-MIN LIN Purdue University



PRENTICE HALL, Englewood Cliffs, New Jersey 07632

#### Library of Congress Cataloging-In-Publication Data

DeCarlo, Raymond A.,

Linear circuit analysis: time domain, phasor, and Laplace transform approaches / Raymond A. DeCarlo, Pen-Min Lin.

p. cm. Includes index. ISBN 0-13-473869-1

1. Electric circuit analysis. 2. Electric circuits, Linear.

3. Laplace transformation. 4. Time-domain analysis. I. Lin, Pen-Min, II. Title.

TK454.D44 1995 621.319'2—dc20

94-26845 CIP

Publisher: Alan Apt

Development Editor: Sondra Chavez Editor-in-Chief: Marcia Horton Production Editor: Jennifer Wenzel

Marketing Manager: Gary June

Designers: Kenny Beck and Joanne Kaiser

Cover Designer: Jeannette Jacobs

Buyer: Lori Bulwin

Photo Editor: Lorinda Morris-Nantz

Photo Researchers: Chris Pullo and Melinda Reo

Supplements Editor: Alice Dworkin Editorial Assistant: Shirley McGuire

The authors and publisher of this book have used their best efforts in preparing this book. These efforts include the development, research, and testing of the theories and programs to determine their effectiveness. The authors and publisher make no warranty of any kind, expressed or implied, with regard to these programs or the documentation contained in this book. The authors and publisher shall not be liable in any event for incidental or consequential damages in connection with, or arising out of, the furnishing, performance, or use of these programs.



©1995 by Prentice-Hall, Inc. A Simon & Schuster Company Englewood Cliffs, NJ 07632

### TRADEMARK INFORMATION

Mathematica is a registered trademark of Wolfram Research, Inc.
MATLAB is a registered trademark of the MathWorks, Inc.
Theorist is a registered trademark of Allan Bandio Association
TTC is a registered trademark of Telecommunication Technology Corp.
PSpice is a registered trademark of MicroSim.

All rights reserved. No part of this book may be reproduced, in any form or by any means, without permission in writing from the publisher.

Printed in the United States of America

10 9 8 7 6 5 4 3 2 1

1-PART4-E1-0 NBZI

Prentice-Hall International (UK) Limited, London
Prentice-Hall of Australia Pty. Limited, Sydney
Prentice-Hall Canada Inc., Toronto
Prentice-Hall Hispanoamericana, S.A., Mexico
Prentice-Hall of India Private Limited, New Delhi
Prentice-Hall of Japan, Inc., Tokyo
Simon & Schuster Asia Pte. Ltd., Singapore
Editora Prentice-Hall do Brasil, Ltda., Rio de Janeiro

## PRESENT AND PAST

For several decades in the United States, most electrical engineering undergraduates have taken two semesters of linear circuit analysis in their sophomore year. Those in other engineering disciplines often have taken only one semester. Standard topics include laws and techniques of resistive circuit analysis, time domain transient analysis of first and second order linear circuits, a phasor approach to sinusoidal steady state analysis, resonant and magnetically coupled circuits, transformers, elementary 3-phase circuits and some Fourier series analysis.

The late 70's and the decade of the 80's marked a time of revolutionary growth in undergraduate electrical engineering education. Discrete time system and computer engineering concepts grew into integral parts of the engineer's required graduation toolbox. Software programs such as PSpice®, MATLAB® and its toolboxes, Theorist®, Mathematica®, and a host of others appeared to streamline the computational drudgery of engineering analysis and design. Paralleling this were advances in research and technology. IC implementations of active filters and especially switched capacitor filters have become shelf items in supply houses. Matrix-based numerical algorithms for large scale circuit simulation, parameter optimization, layout, and design are widely available. Our research colleagues have dramatically deepened our ability to analyze, design, and control nonlinear circuits and systems.

# **APPROACHING THE YEAR 2000**

In order to meet the needs of the next decade, our approach was to more efficiently package the traditional circuits courses in an up-to-date framework without sacrificing rigor. For example, we have introduced the matrix formulation of node and mesh equations for solution with software programs such as Matlab® or its equivalent, and have added a special section on the Modified Nodal Method of circuit analysis. The ubiquitous presence of active circuits built around the op amp, prompted us to integrate op amps throughout the text. In support of this we have a more careful treatment of circuits containing controlled sources and op amps; in particular a careful restatement of the traditional Thévenin and Norton Theorems usually stated (rigorously) in many texts only for passive circuits. We have unified the treatment of RL and RC first order circuits and emphasized the use of the characteristic equation for the solution of second order circuits in contrast to the usual formuladevelopment for each of the parallel and series cases. Classical phasor analysis is introduced in step-by-step fashion beginning with a review of its complex variable foundation. These ideas underlie our treatment of ac steady state power considerations and applications to power systems which we treat in the text.

Many texts introduce a notion of generalized phasor analysis. Our imperative was to replace this often ill-used approach with an early introduction to Laplace transforms without first covering the Fourier series and Fourier transform. This allows us to recast the usual second semester circuits topics (resonant and magnetic circuits, two ports, filters, etc.) with a systems flavoring underpinned on the foundation of the Laplace transform. For example, with resonant circuits we lead the student quickly through the ordinary material spiced with an application or two and then unify the ideas under the umbrella of a bandpass transfer function. In fact, when teaching this material we ordinarily begin with the bandpass transfer function so that the student sees the unity from the onset.

This menu allows us to expand the boundaries of traditional coverage without additional investment in time while simultaneously developing advanced skills: students are able to tackle both transient and steady state circuit analysis for any excitation with a rational Laplace transform besides the usual steps, ramps, exponentials, sinusoids and sinusoidally modulated exponentials for circuits of small to moderate order. This framework offers students an entire semester of problem-solving practice in the Laplace transform context, thereby significantly honing their analytical skills. This is not achievable in the more traditional setting for teaching the course. The approach also enables

us to unify the frequency and time-domain approaches to circuit analysis in the systems context by covering continuous-time convolution. Here we introduce the basic definitions, the integration and graphical approaches, and show where it might be useful for circuit analysis.

With the Laplace transform, the concepts of impedance and transfer function are cleanly and rigorously introduced. Further, there is the potential for the incorporation of synthesis techniques into homework exercises otherwise precluded in the traditional treatment. In sum, this approach better prepares students for the rigors of the signals and systems course of the junior year and frees up time in the junior year for the exploration of discrete time systems concepts.

To better implement our approach we developed a software program entitled tfc which automates the entire approach to Laplace transform analysis. This, of course, can be complemented by Matlab® commands which also accommodate Laplace transform analysis and convolution.

To allow flexibility in curriculum development and to introduce (nonlinear) electronic circuits at a very elementary level, we provide two optional chapters on piecewise linear analysis of diode and transistor amplifier circuits. The piecewise linear approach enables the analysis of many simple and useful electronic circuits via the techniques of linear resistive circuits. It permits us to address the growing need to expose students to nonlinear behavior in a simple understandable way.

#### Personal Reasons

All of the above reasons not withstanding, starting in 1986 and continuing to this day, we have had a genuine desire to significantly improve the content and complexion of the basic circuits courses. We wanted to tie circuits concepts to real world devices such as microwave ovens, stereo amplifiers, adapters for portable computers, etc. With the suggestions and help of our editor, we did this with chapter openers. Further, we wanted to present challenging homework exercises and to present the material in a way consistent with our perception of research and development over the last decade.

# I. KEY CONCEPTS

# 1. Balanced Emphasis on Concepts and Calculation

Quality software programs numerically automate important aspects of circuit analysis and design, relieving engineers of tedious and often impossible hand calculations. However, numerical algorithms implemented as canned programs are no substitute for an understanding of the basic circuit principles and properties which govern circuit behavior nor for a firm understanding of the steps necessary for the solution of a problem, because from our perspective, there are five educational tasks of a circuits course:

- Create an environment where the student has the opportunity to learn the basic vocabulary, principles, analysis methods and design techniques of circuit theory as gleaned from the accumulated experience of past engineers and physicists.
- Create an environment for applying the principles of mathematics and physics to engineering problems.
- 3. Develop a qualitative understanding of circuit behavior.
- 4. Foster the development of rational thinking patterns in the context of circuit analysis and design in order to prepare students for solving a broad variety of engineering problems.
- Help develop the student's ability to critically evaluate their chosen problem-solving technique and the accuracy of their answer.

In order to meet these needs we have included some advanced sections marked with an asterisk (\*) and a variety of problems and exercises from the simple to the difficult. (The more difficult exercises are also flagged with an asterisk.) Some problems apply the ideas of the text while a few enhance or extend the ideas described in the text. Although numerical approaches and problems are included throughout the text, we have included two appendices, one for each of the traditional semesters of circuits, on numerical problems for exploration by the student. These use software packages such as Matlab®, tfc (a supplement to this text and described earlier), and SPICE. Of course, there are many other programs that can be substituted for these.

We believe that using such programs not only allows the student to more easily calculate numbers but reinforces the delineated properties of circuit behavior covered in the text. On the other hand, the principles and properties developed throughout the text allow the students to assess the reasonableness and accuracy of answers computed using a software program. Software programs are

not infallible and it is always possible to construct an example which will cause a program to fail to produce meaningful numbers.

# 2. Text Exercises and Homework Problems

The text contains a wide variety of analysis, design, and computer-oriented problems. We have tried to provide a balance between problems emphasizing concepts and those emphasizing the mechanics of computing solutions. Design problems and software assisted problems are marked in the text with & and & respectively.

# 3. Chapter Openers/Real World Applications

The book includes a variety of real world applications that introduce and motivate almost every chapter in the book. Students immediately preview how circuits concepts underlie a wide variety of applications. Some of these are:

- (a) Car Heater Fan Speed Control (Chapter 2)
- (b) Digital-to-Analog Converter (Chapter 3)
- (c) Protection Circuits Against Overvoltage and Polarity Reversal (Chapter 6)
- (d) Capacitive Voltage Regulator (Chapter 7)
- (e) Sawtooth Waveform Generation (Chapter 8)
- (f) Microwave Oven (Chapter 9)
- (g) Capacitive-bridge Pressure Sensor (Chapter 10)
- (h) Fluorescent Light (Chapter 14)
- (i) DC Motor (Chapter 15)(j) Averaging by a Finite Time Integrator Circuit (Chapter 16)
- (k) How a Touch Tone Phone Signals the Numbers Dialed (Chapter 17)
- (1) Rectifier Circuit (Chapter 22)

Each of these openers is discussed in a simplified form either within a section of the associated chapter or as a chapter problem. In addition, we have included career boxes as an added feature so students will be better informed about career opportunities in the field.

# 4. In-depth Coverage

The traditional topics are covered in a more modern format, utilizing available software for problem solving where necessary. As mentioned in the Preface, we introduce a matrix-based approach to nodal and mesh analysis and include a special section on the Modified Nodal approach to circuit analysis. Teamed up with MATLAB® or its equivalent, the student has a powerful analysis tool not directly available to engineering students of past decades. (A MATIAB® supplement which expands upon the numerical implementation of the course material is available through Prentice Hall.) Op amp coverage is integrated throughout the text. Rigorous treatments of Thévenin and Norton theorems for active circuits are given. The Laplace transform is introduced after phasor analysis and utilized through the remainder of the text. This allows a unifying systems approach to impedance concepts, circuit transfer functions, convolutional approaches to circuit analysis, resonance, magnetic circuits, two ports and filtering. The software program, tfc, for use on a PC is available from Prentice Hall to enhance the Laplace transform analysis required in the text. The program computes roots, Laplace transforms, partial fraction expansions, and inverse Laplace transforms; it also has a provision for the manipulation of two-port parameters. Fourier series with applications to power supplies and distortion in an amplifier ends our treatment.

# 5. Review and Development of Text

Extensive review of both volumes and extensive class testing of Volume II.

# 6. Chapter Pedagogy

- (a) Each chapter begins with an overview of chapter contents and ends with a summary and a glossary of terms and concepts used throughout the chapter.
- (b) Throughout each chapter "key concept boxes" highlight important definitions, laws, and properties of circuit analysis and design.
- (c) Many examples illustrate concepts and techniques. Each example is set off with its conclusion clearly marked.
- (d) More advanced sections and problems are marked with an asterisk for students desiring more challenging material.

# 7. Presentation of Piecewise Linear Analysis

Presentation of piecewise linear analysis of diode and transistor amplifier circuits as a first step in approaching nonlinear circuits: This material is optional and may be omitted without any loss of continuity.

# 8. Software for Laplace Transform Analysis

Free software for Laplace transform and two-port analysis for IBM PC or equivalent is available from the publisher.

# 9. Instructor's Manual

An Instructor's Manual is available from the publisher.

# 10. Matlab® Supplement

Jim Gottling of the Department of Electrical Engineering of The Ohio State University has put together a MATLAB® supplement that can be used in conjunction with the text.

# II. HOW TO USE THIS BOOK

This is the only circuits book available in one comprehensive book and two separate volumes. The purpose is to give instructors and students some cost effective options.

# 1. Volume Selection

Volume I best fits a one semester course primarily for those not taking a second course with a price tag much lower in cost than competing two semester texts. Volume II is for those taking only the second course or for those using a different text for the first course. Alternately, one can choose a combined volume for those taking a two semester sequence or equivalent.

As mentioned earlier, most electrical engineering sophomores take two semesters of linear circuit analysis. Those in other engineering disciplines often take only one semester. The two-volume book contains an abundance of material that can be used in a variety of ways. An instructor can choose the proper ingredients for the intended course. Some possible plans are given in the following table:

	Course length	Intended Course Structure	Chapters used
1.	Two semesters [3 credit hours each]	Traditional linear circuit analysis	Semester 1 vol. 1, with ch. 6 and 12 omitted Semester 2 vol. 2, ch. 13 to 20, plus ch. 21 or ch. 22
2.	Three quarters (2 credit hours each)	Traditional linear circuit analysis	quarter 1 vol. 1, ch. 1, 2, 3, 4, 5, 7, 8 quarter 2 vol. 1, ch. 9, 10, 11, and vol. 2, ch. 13, 14, 16, 17 quarter 3 vol. 2, ch. 18 to 21
3.	One-semester (4 credit hours)	Introductory linear circuit analysis and Elementary electronic circuits	vol. 1, ch. 1 through 12
4.	One-semester (3 credit hours)	Introductory linear circuit analysis	vol. 1, with ch. 6 and 12 omitted
5.	One-semester (3 credit hours)	Laplace transform analysis of linear circuits	vol. 2, ch. 13 through 21

For the first semester of option 1 or for option 4, the lecture by lecture topics can be chosen as:

1 2 3	General circuit element, charge, current Voltage, sources, power Resistance, Ohm's Law, power reprise
4 5 6	Kirchhoff's Laws, single loop, node circuits R combinations, v & i division Op amp basics
7 8 9	Nodal analysis Nodal analysis (cont.) Mesh analysis
10 11 12	Superposition and linearity Linearity (cont.) & source transformations Review
13 14 15	TEST #1 Thévenin's and Norton's Theorems Thévenin's and Norton's Theorems (cont.)
16 17 18	Inductance Capacitance L and C combinations, duality
19 20 21	First order undriven circuits: RL case and RC case; use of $R_{\rm th}$ ; initial conditions from past dc excitations; sequential switching First order undriven circuits (continued); recovery of stored energy; differential equation, and mathematical solution for first order circuits with constant inputs RC emphasis; examples/applications
22 23 24	Unit step function; DC response of first order circuits; the 3-parameter formula; exponential decay and growth curves; inductive and capacitive circuit examples; piecewise constant inputs  Transient analysis of first order circuits (cont.); unit step, natural and forced response; solution to real exponential input; integral solution for arbitrary inputs Switching & elapsed time calculation; negative time constant and stability
25 26 27	RC op-amp circuits Differential equation models of parallel and series RLC circuits; derivation of solution for the undriven case Review
28 29 30	TEST #2 Solution of 2nd order differential; equation; models for undriven circuits continued: overdamped, critically damped, and underdamped cases Solution of 2nd order differential; equation; models for circuits driven by constant input excitations
31 32 33	Sinusoidal forcing function Complex forcing function Phasors: KVL & KCL with phasors
34 35 36	Impedance & admittance Sinusoidal steady state (SSS) analysis using phasors; general case requiring (matrix) mesh and node equations SSS phasor diagrams; examples of applications
37 38 39	Frequency response: magnitude and phase plots; lowpass and highpass examples Instantaneous power absorbed by a general 2-terminal element and special cases of R, L, and C; average power over a specified time interval; average power when $p(t)$ is periodic; sinusoidal steady state average power Maximum power transfer in SSS; adjustable impedance/resistive load; application in communication circuits

40 <b>41</b> 42	Review TEST #3  Effective (rms) value; definition and examples of sine and triangular waves; counter-example to superposition of power; RMS value of response when all inputs are at different frequencies
43 44 <b>45</b>	Complex power, VA; reactive power, var conservation of Power Balanced 3-phase circuits; economic consideration in power transmission FINAL EXAM

For the second semester of option 1 or for option 5, the lecture-by-lecture topics can be chosen

1 2 3	Motivation for studying Laplace transform Laplace transform, transforms of basic signals Inverse transform, partial fraction expansion
4 5 6	Basic properties Solution of linear DE's with initial conditions Z(s),Y(s), series-parallel & other manipulations
7 8 9	Transfer function Equivalent circuits for L and C with I.C. Nodal and mesh analyses in s-domain
10 11 12	Switching in linear circuits Switched capacitor circuits H(s),poles, zeros, s-plane plot and stability
13 14 15	Test #1 Decomposition of the complete response Sinusoidal steady state analysis
16 17 18	Frequency response from pole-zero plot Impulse response, h(t); relation to H(s); additional Laplace properties and review Convolution, definition, and integral evaluation; relationship to L.T.; time domain derivation
19 20 21	Circuit response calculation using convolution; graphical convolution; additional properties Additional properties; convolution algebra; SPICE, and tfc
22 23 24	Parallel resonance, calculation of $\Omega r$ Parallel resonance, exact analysis Other resonance forms; approximate analysis
25 26 27	Pole-zero approach to resonant circuit analysis Frequency and amplitude scaling Mutual inductance, the dot convention
28 <b>29</b> 30	Mutual inductance: s-domain and phasor equations  Test #2  Coefficient of coupling stored energy
31 32 33	Ideal transformer Coupled inductors modeled with ideal transformer One-port networks
34 35 36	y parameters Equivalent circuit & analysis of terminated 2-port z parameters
37 38 39	h parameters t parameters; reciprocity; conversion table 2-port interconnections

viii Preface

40	Test #3 Indefinite admittance parameters
42	Lowpass Butterworth response; loss in dB; from  H(j\omega)  to H(s)
43	Calculation of filter order and cut-off frequency; basic passive realization
44	2nd order filters  Rasic active realization of 2nd order filters

For non-electrical engineering majors, spending three semester credit hours in linear circuit analysis, use Volume I with Chapters 6 and 12 omitted. For non-electrical engineering majors, spending four semester credit hours in linear circuit analysis and basic electronic circuits, use Volume I in its entirety.

Of course, many other arrangements are possible. The ingredients are there. An instructor can choose a proper combination for the intended course. For example, if Fourier Series analysis needs to be covered, then other topics (such as two-port interconnections) may be omitted.

# 2. Special Notes on Terminology

Although the Institute of Electrical and Electronic Engineers (IEEE) has a standard dictionary for electrical terms, the engineering literature does not always adhere to the standards. In one sense, students need to know the standard terminology and also the variations which is what they will encounter when they work in the real world. This variation also shows up in the book in some places. For example, we have used the following terms synonymously:

node terminal circuit network

dependent source controlled source

Although the Standard International unit for conductance is Siemens, the term "mho" (ohm spelled backward) is still widely used in engineering literature and manufacturers' data sheets. This book used the term mho and the symbol inverted  $\Omega$  as the unit for conductance.

# **ACKNOWLEDGMENTS**

A work of this magnitude has input from many people over many years. We are particularly indebted to our colleagues Mark Wicks, Gerry Heydt, LeRoy Silva, Chin-Lin Chen, Jim Krogmeier, George Wodicka, Barrett Robinson, Phil Peleties, and Tan-Li Chou who have added in various ways to the quality of the text, but in no way are responsible for any of its shortcomings. These, of course, rest solely with the authors.

We would also like to acknowledge the many teaching assistants of EE-201 and EE-202 at Purdue University who have contributed corrections and suggestions to the many chapter exercises and homework problems throughout. Our secretaries, Cathy Tanner and Nicky Danaher, who have helped with the mechanics and proofing of the text deserve a special note of thanks. They freed us from the many mailings and correspondences that allowed us to focus on the writing. Also Jean Jackson, who helped set up guidelines for color usage in the figures.

We would like to thank all of the reviewers listed below, especially Irwin Sandberg and Peter Aronhime, whose critiques have helped us improve the text in many different areas.

Peter Aronhime - University of Louisville John A. Fleming - Texas A & M University Edwin W. Greeneich - Arizona State University James V. Krogmeier - Purdue University Derek Lile - Colorado State University Charles P. Neuman - Carnegie Mellon University

Burks Oakley II - University of Illinois at Urbana-Champaign

Edgar A. O'Hair - Texas Tech University

Irwin W. Sandberg - University of Texas at Austin

We also must acknowledge our editor Alan Apt for his many ideas and suggestions, our development editor Sondra Chavez for her coaxing and coaching, our production editor Jennifer Wenzel for her patience and skill in dealing with the many tribulations of production, and the other PH staff who have either quietly or indirectly helped to shape the face (quite literally) of this text.

Our friends from industry who have supplied technical information on products and photos for use as chapter openers, Bill Lorenz of AlliedSignal and Howard Babbitt III of The Mitre Corporation

Preface ix

must also be mentioned. And Mr. Thackery, W3IU, who showed one of the authors the magic of ham radio.

We would also like to thank all those of the School of Electrical Engineering at Purdue University who encouraged us to revise and update the content of our basic circuits courses. The update led to a set of notes and homework exercises that in turn led to the volumes.

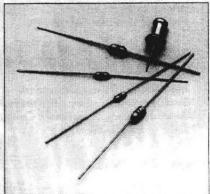
More importantly, we must thank our wives, Chris and Louise, who have supported us throughout this eight-year project. Last but not least we thank our attorney, Scott Sullivan, who has guided us through several skirmishes and one of the authors apologizes for all the lawyer jokes he has told over the last eight years.

Dedication

To our wives
Chris and Louise,
and to
Frank and Adele DeCarlo

# CHAPTER 1







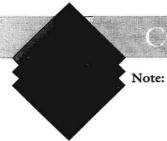
# CHAPTER OUTLINE

- 1. Introduction and Basic Concepts
- 2. Fields, Charge, and Current
- 3. Voltage
- 4. Energy Conversion in an Electric Circuit
- 5. Relationships among Voltage, Current, Power, and Energy
- 6. Ideal Voltage and Current Sources
- 7. Resistance
- 8. Nonideal Sources
- 9. Additional Remarks

Summary

Terms and Concepts

**Problems** 



# CONTENTS

This book is also available in individual volumes entitled, Linear Circuit Analysis: Time Domain and Phasor Approach Volume One (Chapter 1-12 and Appendices A1-A3) and Linear Circuit Analysis: A Laplace Transform Approach Volume Two (Chapters 13-22 and Appendices B1-B3)

1	hapter 1 INTRODUCTION AND B.	ASIC	Co	NCEPTS	1
<ul><li>3.</li><li>4.</li><li>5.</li></ul>	Introduction and Basic Concepts Fields, Charge, and Current Fields, 3 Charge, 3 Current, 3 Voltage Energy Conservation in an Electric Circuit Relationships Among Voltage, Current, Power, and Energy Ideal Voltage and Current Sources Resistance Ohm's Law, 21	1 2 7 11 13 18 21		Non-Ideal Sources Additional Remarks The Notion of a Device, 27 The Notion of a Model, 28 Frequency, Wavelength, and to Notion of a Lumped Circuit Element, 28 Skin Effect, 29 Power Reprise and Efficiency Summary Terms and Concepts Problems	he The
Ck	bapter 2 KIRCHHOFF'S CURREN				36
	AND SERIES-PARALLEL	RES	IST	IVE CIRCUITS	
1.	Introduction	38	6.	Series-Parallel Interconnections	49
	Kirchhoff's Current Law	39	7.	Circuit Applications: Design of Analog	
	Kirchhoff's Voltage Law	42		Multimeters	52
4.	Applications of KVL: Voltage Division and	Talle:		Voltmeters, 53 Ammeters, 55 Ohmmeters,	
-	Series Resistance	44		Summary	58
Э.	Application of KCL: Current Division and	47		Terms and Concepts	59
	Parallel Résistance	46		Problems	59
Ch	hapter 3 DEPENDENT SOURCES	AND T	ГНЕ	E OPERATIONAL AMPLIFIER	66
	Introduction	68	5	An Application to Analog-to-Digital	
1.	Introduction	00	J.		
2.	Dependent Voltage and Current Sources	68	٥.	Converters	79
2.	Dependent Voltage and Current Sources The Operational Amplifier		٥.	Converters  Elements of A/D and D/A Conversion, 79	79 A
2.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72	68	5.	Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80	
2. 3.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74	68	5.	Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary	A 82
2. 3.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by	68 71	3.	Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts	A 82 83
2. 3.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74	68	3.	Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary	A 82
<ol> <li>3.</li> <li>4.</li> </ol>	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by	68 71 74	3.	Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts	A 82 83
2. 3. 4.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit	68 71 74	J.	Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems	82 83 84
2. 3. 4. <b>Ck</b> 1. 2.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources	68 71 74 XSIS		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts	82 83 84
2. 3. 4. Ch. 1. 2. 3.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources	68 71 74 XSIS 90 92 95		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary	82 83 84
2. 3. 4. <i>Ch</i> . 1. 2. 3. 4.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis	68 71 74 XSIS 90 92 95 98		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary Terms and Concepts	82 83 84 88
2. 3. 4. Ch. 1. 2. 3. 4. 5.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis Modified Nodal Analysis	68 71 74 XSIS 90 92 95 98 102		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary	82 83 84 <b>88</b>
2. 3. 4. Ch. 1. 2. 3. 4. 5.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis	68 71 74 XSIS 90 92 95 98		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary Terms and Concepts	82 83 84 88
2. 3. 4. 2. 3. 4. 5. 6.	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis Modified Nodal Analysis	68 71 74 XSIS 90 92 95 98 102		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary Terms and Concepts Problems	82 83 84 88
2. 3. 4. 1. 2. 3. 4. 5. 6. Ch	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis Modified Nodal Analysis Theoretical Considerations  Mapter 5 NETWORK THEOREMS Introduction	68 71 74 XSIS 90 92 95 98 102 106		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary Terms and Concepts Problems	82 83 84 88 110 111 112
2. 3. 4. 1. 2. 3. 4. 5. 6. Ch	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis Modified Nodal Analysis Theoretical Considerations  Mapter 5 NETWORK THEOREMS Introduction Proportionality, Superposition, and Linearity	68 71 74 XSIS 90 92 95 98 102 106		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary Terms and Concepts Problems	82 83 84 88 110 111 112
2. 3. 4. 1. 2. 3. 4. 5. 6. Ch	Dependent Voltage and Current Sources The Operational Amplifier Introduction, 71 Operation of Op Amp, 72 Model of the Op Amp, 74 Analysis of Basic Op Amp Circuits by Virtual Short Circuit  Mapter 4 NODE AND LOOP ANAL Introduction Nodal Analysis I: Grounded Voltage Sources Nodal Analysis II: Floating Voltage Sources Loop Analysis Modified Nodal Analysis Theoretical Considerations  Mapter 5 NETWORK THEOREMS Introduction	68 71 74 XSIS 90 92 95 98 102 106		Converters Elements of A/D and D/A Conversion, 79 Binary-Weighted Summing Circuit, 80 Summary Terms and Concepts Problems  Selection of Loop Currents for a General Network Configuration, 106 Summary Terms and Concepts Problems  Proportionality and Superposition Properties,	82 83 84 88 110 111 112

-	Source Transformations Thévenin's and Norton's Theorem	131 134		Active Circuits, 139 Computation of Their and Norton Equivalent Circuits, 140	enin
	Introduction, 134 Thévenin's Theorem for		5.	Maximum Power Transfer Theorem	148
	Passive Networks, 135 A Proof of Thévenin			Summary	156
	Theorem for Passive Circuits, 136 The Nort			Terms and Concepts	157
	Equivalent Circuit, 137 Thévenin's Theorem	n for		Problems	157
C	bapter 6 PIECEWISE LINEAR AN	ALYS	IS O	F DIODE CIRCUITS	166
1.	Introduction	168		Examples and Applications, 174	
2.	The Ideal Diode: A Basic Circuit Element	168	5.	Further Application of Ideal Diodes in	-
	Simple Circuits Containing Ideal Diodes	170		Circuit Models	176
4.	The Zener Diode: Circuit Models and			Summary	178
	Applications	172		Terms and Concepts	178
	v-i Characteristics of a Zener Diode, 172			Problems	179
	Circuit Model for a Zener Diode, 173				
Ci	hapter 7 INDUCTORS, CAPACITO	ORS, A	AND	DUALITY	182
1.	Introduction	183		Series-Parallel Combinations, 199 Capaci	tors
2.	The Inductor	183		in Series, 200 Capacitors in Parallel, 20:	I
	Some Physics, 184 Basic Definition and			Series-Parallel Combinations, 201	
	Examples, 186 Power and Energy, 188		5.	Smoothing Property of a Capacitor in a Po	ower
3.	The Capacitor	191		Supply	202
	Definitions and Properties, 191 Relationshi		6.	The Duality Principle	203
	Charge to Capacitor Voltage and Current, 1			Definition and Basic Relationship of Dual	
	The Principle of Conservation of Charge, 19-			Circuits, 203 Constructing the Dual N*	
	Energy Storage in a Capacitor, 195 Capaci	tors		of a Planar Circuit N, 205	200
4	and Charge, 197	107		Summary	209
4.	Series and Parallel L's and C's	197		Terms and Concepts Problems	210 211
	Series Inductors, 197 Inductors in Parallel,	190		Problems	211
Ci	hapter 8 FIRST ORDER RL AND	RC (	CIRC	CUITS	216
	Introduction	218		Classification of Responses	233
	Some Mathematical Preliminaries	219		Further Points of Analysis and Theory	233
	The Source-Free or Zero-Input Response	221	8.	First-Order RC Op Amp Circuits	236
4.	The DC or Step Response of First-Order	225		Summary	239
-	Circuits	225		Terms and Concepts	240 240
Э.	Superposition and Linearity	230		Problems	240
Ci	bapter 9 SECOND ORDER LINEA	AR CI	RCU	JITS	250
-	Introduction	250	*5.	Formulation of a Single Second-Order	
2.	Discharging a Capacitor through			Differential Equation	265
	an Inductor	251		Writing the State Equations, 265 Reduct	
3.	Source-Free Second-Order Linear Network	254		of State Equations to a Single Second-Orde	er
	Development of the Differential Equation		.5	Differential Equation, 266	
	Model, 254 Solution of the Second-Order		6.	Further Examples and Applications	269
	Differential Equation Model, 255			Summary	272
4.	Second-Order Linear Networks	2/1		Terms and Concepts	272
	with Constant Inputs	261		Problems	273
C	bapter 10 SINUSOIDAL STEADY-S	TATE	AN	JALYSIS	278
	BY PHASOR METHODS				
· .	Introduction	280		Steady-State	287
	A Brief Review of Complex Numbers	281	4	Complex Forcing Functions in Sinusoidal	
	A Native Technique for Computing the Sinus			Steady-State Computation	289

5.	Phasor Representatives of Sinusoidal		10.	Introduction to the Notion of Frequency	
	Signals	292		Response	310
6.	Elementary Impedance and Concepts:		11.	Nodal Analysis of a Pressure-Sensing	
	Phasor Relationships for Resistors, Inductors,			Device	314
	and Capacitors	294		Summary	317
7.	Phasor Impedance and Admittance	298		Terms and Concepts	317
	Steady-State Circuit Analysis using Phasors	301		Problems	318
	The Phasor Diagram	304			
F ( 15.)	8				
Ch	apter 11 SINUSOIDAL STEADY-S	TATE	Po	WER CALCULATIONS	326
1.	Introduction	328	7.	Analysis of Balanced Three-Phase Circuits	352
	Instantaneous and Average Power	329		Circuit Model for Practical Three-Phase Soi	irces,
	Apparent Power and Power Factor			354 Wye-Delta Transformation, 355	,
	Improvement	336		Analysis of Balanced Three-Phase Circuits, 3	356
4.	Reactive Power and Conservation of Power	339		Summary	360
	The Maximum Power Transfer in			Terms and Concepts	361
٠.	Sinusoidal Steady State	346		Problems	362
6	Some Economical Aspects of Transmitting	0.10			002
0.	Electric Power	349			
	Dicelle Tower	017			
~	Deportured I when An	4 × ××××		Thanksomen	2/0
Ch	papter 12 PIECEWISE LINEAR AN	ALYSI	SO	FIRANSISTOR	368
	Amplifier Circuits				
1	Introduction	370		Stabilization of the Operating Point 205	
	Introduction Characterization and Models for 3-terminal	3/0		Stabilization of the Operating Point, 385	
2.		370		PWL Model for Determining Amplifier Gar	
2				and Maximum Output Swing, 387 Signij	icunce
Э.	Piecewise Linear Models for Bipolar	275	_	of AC Equivalent Circuits, 391	
1	Junctions Transistors	375	5.	Piecewise Linear Analysis of a Practical	202
4.	Piecewise Linear Analysis of Bipolar	201		Amplifier Stage	392
	Transistor Circuits	381		Summary	395
	Three Regions of BJT Operation, 382 Graph	ncal		Terms and Concepts	395
	Method for a Simple Configuration, 384			Problems	396
Ch	papter 13 LAPLACE TRANSFORM	ANAL	YSI	s, 1: Basics	<b>400</b>
	Introduction	401		Partial Fraction Expansions: Distinct Com	plex
2.	Review and Deficiencies of "Second-Order"			Poles, 418	
	Time Domain Methods	402	7.	Elementary Properties and Examples	420
3.	Overview of Laplace Transform Analysis	405	8.	Solution of Integro-differential Equations	
	Basic Signals	406		by the Laplace Transform	429
5.	The One-Sided Laplace Transform	409		Summary	433
6.	The Inverse Laplace Transform	414		Terms and Concepts	433
	Partial Fraction Expansions: Distinct Poles, 4	15		Problems	434
	Partial Fraction Expansions: Repeated Poles,				
Ch	papter 14 LAPLACE TRANSFORM	ANAL	YSI	s. 2:	442
···	CIRCUIT APPLICATION				112
	CIRCUIT APPLICATION	15			
1.	Introduction	444	7.	Switching in RLC Circuits	466
	Notions of Impedance and Admittance	445		Switched Capacitor Circuits and	200
	Manipulation of Impedance and Admittance			Conservation of Charge	470
	Notion of Transfer Function	452		Summary	476
	Equivalent Circuits for L's and C's	455		Terms and Concepts	476
	Nodal and Loop Analysis in the s-Domain	460		Problems	476
	, , , , , , , , , , , , , , , , , , , ,				2, 0

CONTENTS

Ch	TRANSFER FUNCTION				488
<ol> <li>3.</li> <li>4.</li> <li>5.</li> </ol>	Introduction Poles, Zeros, and the s-Plane Classification of Responses Computation of the Sinusoidal Steady-State Response for Stable Networks and Systems Frequency Response Impulse and Step Responses	490 491 496 503 507 510	8.	Initial- and Final-Value Theorems Bode Plots Transfer Function Analysis of a DC Motor Summary Terms and Concepts Problems	512 515 520 522 522 523
Ck	napter 16 TIME DOMAIN CIRCUIT	T RES	PO	NSE COMPUTATIONS:	534
	THE CONVOLUTION M	<b>I</b> ETHO	DD		
<ol> <li>3.</li> </ol>	Introduction Definition, Basic Properties, and Simple Examples Convolution and Laplace Transforms Time Domain Derivation of the Convolution Integral for Linear Time-Invariant Circuits Rectangular Approximation to Signals, 542 Computation of Response for Linear Time-Invariant Systems, 543	536 537 541 n 542	6. 7.	Circuit Response Computations Using Convolution Convolution Properties Revisited Graphical Convolution and Circuit Response Computation Convolution Algebra Summary Terms and Concepts Problems	548 548 548 558 558 558
Ck	papter 17 RESONANT AND BANDI	PASS C	CIR	CUITS	566
<ol> <li>3.</li> <li>4.</li> <li>5.</li> </ol>	Introduction Resonant Frequency of Simple Circuits with Applications Frequency Response of a Parallel RLC Circuit Frequency Response of a Series RLC Circuit: Application of Duality Quality Factor of Components and Approximate Analysis of High-Q Circuits Bandpass Transfer Functions with One Pair of Complex Poles The Structure of a Bandpass Transfer Function	<ul><li>569</li><li>574</li><li>582</li><li>585</li><li>591</li></ul>		591 Case 1: H(s) Has a Single Zero at the Origin, 592 Case 2: H(s) Has a Single Zero off the Origin, 594 Case 3: H(s) Has No Finite Zero, 595 Case 4: H(s) Has Two Representation of Approximate Solutions, 596 An Active Bandpass Circuit Magnitude Scaling and Frequency Scaling Improving Bandpass Characteristics with Stagger-Tuned Circuits Summary Terms and Concepts Problems	ero eal
Ck	papter 18 MAGNETICALLY COUP	LED C	IRO	CUITS AND TRANSFORMERS	620
<ol> <li>3.</li> <li>4.</li> </ol>	Introduction Mutual Inductance and the Dot Convention Differential Equation, Laplace Transform, an Phasor Models of Coupled Inductors Applications: Automobile Ignition and RF Amplifier Coefficient of Coupling and Energy Calculation  Justification that $M_{12} = M_{21} = M$ , 635  Calculation of Stored Energy, 636 Upper Bo	d 626 631 635	7.	Ideal Transformer as a Circuit Element and Applications Coupled Inductors Modeled with an Ideal Transformer Models for Practical Transformers Coupled Coils with a Magnetic Core, 649 Electric Circuit Model for a Practical Transformer, 653 Summary Terms and Concepts	638 648 649 655 656
	for M and the Coefficient of Coupling, 636	.mm(2:5-3.2)		Problems	656
Ch	papter 19 TWO-PORTS				666
1.	Introduction	668		and Norton Equivalent Circuits, 670 Gen	neral
2.	One-Port Networks Basic Impedance Calculations, 669 Thévenin	669 n	3.	One-Port Analysis, 673 Two-Port Admittance Parameters	674

5.	Two-Dependent Source Equivalent Circuit, 677 Y-Parameter Analysis of Terminated Two-Ports 677 Input and Output Admittance Calculations, 677 Gain Calculations, 678 Impedance Parameter Analysis of Two-Ports 679 Definition and Example, 679 Relationship to y-Parameters, 681 The Two-Dependent Source Equivalent Circuit, 682 Impedance and Gain Calculations of Terminated Two-Ports Modeled by Z-Parameters 682 Input and Output Impedance Computations, 682 Gain Calculations, 683	7. Hybrid Parameters 68  Basic Definitions and Equivalences, 685  Computation of h-Parameters, 686 General Relations to z- and y-Parameters, 688  Impedance and Gain Calculations of Terminate Two-Ports, 689  8. Generalized Two-Port Parameters 69 9. Transmission Parameters 69 10. Reciprocity 69  Summary 69  Terms and Concepts 69  Problems 69	ed 00 1 3 07 08
1. 2.	Introduction 710 Parallel, Series, and Cascaded Connection of Two-Ports 710 Indefinite Admittance Matrix of a Three-		20 26 26
	napter 21 PRINCIPLES OF BASIC FILTE		
1. 2.	Introduction and Basic Terminology 736 Types of Filtering, 736 Basic Terminology, 737 Low-Pass Filter Basics 738 The Butterworth Low-Pass Transfer	6. Basic Active Realization of Butterworth Transfer Functions  Sallen and Key Active Low-Pass Filter, 751  7. Input Attenuation and Gain Enhancement	51
4.	Characteristic 741 Introduction, 741 Phase 1: Development of the Butterworth Magnitude Response, 741 Phase 2: Development of the Butterworth Transfer Function, 743 Properties of the Butterworth Loss Function, 745 Computation of Butterworth Loss Functions From Brickwall Specifications 746 Basic Passive Realization of Butterworth Transfer Functions 749	for Active Circuit Design 75  Input Attenuation, 754 Gain Enhancement, 755  8. Basic High-Pass Filter Design with Passive Realization 75  9. Pole-Zero Movement Under the LP to HP Transformation 75	56 59 51 52
Cł.	rapter 22 FOURIER SERIES WITH APPL TO ELECTRONIC CIRCUITS		6
<ol> <li>3.</li> </ol>	Introduction 768 The Fourier Series: Trigonometric and Exponential Forms 770 Basics, 770 Properties of the Fourier Series, 773 Harmonic Distortion in an Amplifier 779 *Ripple Factor in DC Power Supplies 784	5. Additional Properties of and Computational Shortcuts to Fourier Series 79 Summary 79 Terms and Concepts 79 Problems 79	98 98
Ap Ap	Pendix A1 Matrices A1-1 pendix A2 Software Assisted Problems A2-1 pendix A3/B1 Use of SPICE in Linear Circuits A3/B1-1 pendix B2 Software Assisted Problems B2-1	Appendix B3 Introduction to Magnetic Circuit Analysis B3- Photo Credits Index I-	-1

此为试读,需要完整PDF请访问: www.ertongbook.com

CONTENTS