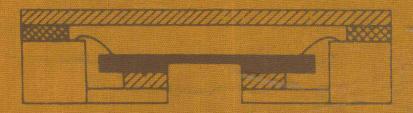
Physics of Semiconductor Devices

PROCEEDINGS OF THE INTERNATIONAL WORKSHOP NOVEMBER, 1981 NEW DELHI, INDIA



S C JAIN
S RADHAKRISHNA

Physics of Semiconductor Devices

PROCEEDINGS OF THE INTERNATIONAL WORKSHOP NOVEMBER, 1981 NEW DELHI, INDIA

> S C JAIN S RADHAKRISHNA

A HALSTED PRESS BOOK



JOHN WILEY & SONS

New York Chichester Brisbane Ontario Singapore

Copyright © 1982, Organising Committee of the International Workshop on the Physics of Semiconductor Devices, Delhi, India

Published in the Western Hemisphere by Halsted Press, a Division of John Wiley & Sons, Inc., New York

Library of Congress Cataloging in Publication Data

Main entry under title:

Physics of semiconductor devices.

Proceedings of the International Workshop on the

Physics of Semiconductor Devices sponsored by COSTED

(UNESCO)

1. Semiconductors--Congresses. I. Jain, S. C. (Suresh C.), 1926- II. Radhakrishna, S. III. International Workshop on the Physics of Semiconductor Devices (1981: New Delhi, India) IV. International Council of Scientific Unions. Committee on Science and Technology in Developing Countries. QC6109.P48 1982 537.6'22 82-9163 ISBN 0-470-27512-X (Halsted Press) AACR2

Printed in India at Pearl Offset Press, New Delhi.

PREFACE

The activity in the field of science and technology of silicon and other semiconductors started late in India. However, it grew rapidly in the last decade. Considerable work has been done in this field at the Solid State Physics Laboratory of the Ministry of Defence, Delhi. Technologies of Space Quality Solar Cells, Gunn Diodes, Semiconductor Laser Diodes and Integrated Circuit Chips up to the level of MSI have been developed at the Solid State Physics Laboratory. Notable work has also been done at IIT Delhi, IIT Kanpur, Tata Institute of Fundamental Research, Bhabha Atomic Central Electronics Engineering Research Research Centre, Institute, and other institutions in India. Apart from the technological achievements, important contributions have been made by the scientists of the Solid State Physics Laboratory and by other scientists in India in the understanding of the physics of semiconductor materials and devices. The year 1981 was, therefore, an appropriate time to hold an International Workshop on the Physics of Semiconductor Devices in India.

International Conferences on semiconductor devices are held every year in developed countries. In fact, three such conferences are held every year in the USA. This is the first time that International Workshop on the Physics of Semiconductor Devices was held in India. The workshop was sponsored by COSTED (Committee on Science and Technology in Developing Countries). Among those Indian scientists who supported the idea and provided encouragement are Dr. P.P. Gupta, Prof. M.G.K. Menon, Prof. R. Ramanna, Prof. S. Sampath and Dr. G.S. Sidhu. Several distinguished scientists from other countries were members of the International Advisory Committee and helped in evolving a good technical programme for the Workshop. Names of some of them are given on page xii, and the names of others are H. Beneking, R.W. Brander, D.L. Feucht, H.L. Hartnagel, H.J. Hovel, W.G. Oldham, Z.J. Staszak, T. Sugano, W. Winstel and A.E. Yunovich.

A total of 365 scientists and technologists participated in the Workshop. These included 66 scientists from Australia, Belgium, Canada, France, Italy, Japan, Netherlands, Poland, Romania, Sweden, Spain, UK, USA, USSR and West Germany. Several scientists from developing countries like Bangladesh, Malayasia, Singapore and Vietnam also participated.

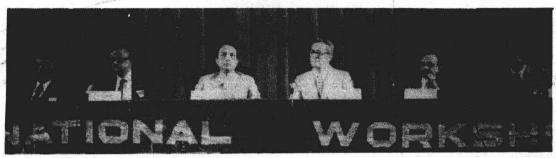
A questionnaire was circulated to the participants and they were asked to send replies to assess the success of the Workshop. All replies were unanimous in that the Workshop was well organised and was very useful to the scientists from both the developing as well as the developed countries. One of the delegates said 'I consider the Workshop a rare event in the scientific activity of this country'. Similar sentiments were expressed in the Press.

A major part of the Workshop was devoted to VLSI, MOS, Bipolar Devices and Photovoltaic Devices. While answering the questionnaire, some delegates said that the Workshop should have been restricted to fewer topics, whereas others said that some topics like Opto-electronics, Amorphous Silicon and Microwave Devices should also have been included. One criticism that has been made of the Workshop is that the programme was too crowded. There should have been more free time for informal discussions. One of the reasons that the programme became crowded was that the response to my invitation was overwhelming, beyond our expectations.

I am glad that the Workshop was successful and that Prof. S. Radhakrishna and I are able to edit and bring out these proceedings. It is hoped that the proceedings will be useful to researchers in the field of semiconductor devices.

Solid State Physics Laboratory Lucknow Road, Delhi-110007 India S.C. JAIN Workshop Chairman











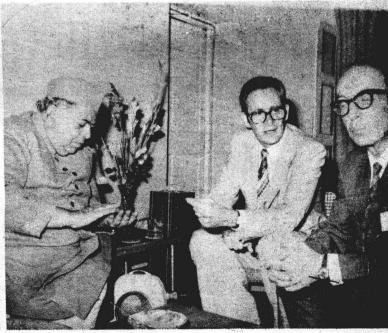




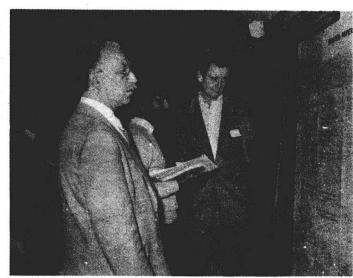


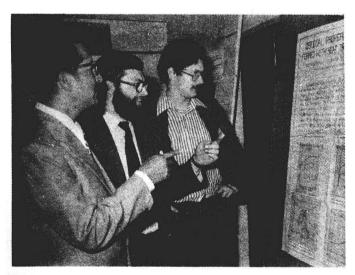
















ACKNOWLEDGEMENTS

The International Workshop has received support from the following organisations and wishes to express its appreciation for their financial assistance.

Committee on Science & Technology in Developing Countries (COSTED) of International Council of Scientific Unions (ICSU) Asian Physical Society Defence Research & Development Organisation Council of Scientific and Industrial Research Department of Electronics University Grants Commission Indian National Science Academy Department of Science and Technology **Bharat Electronics Limited** Bharat Heavy Electricals Limited Central Electronics Engineering Research Institute Indian Institute of Technology Kanpur National Physical Laboratory Semiconductor Complex Limited Solid State Physics Laboratory Central Electronics Limited The Institute of Electrical and Electronics Engineers

COMMITTEES

| PROGRAMME & ORGANISING COMMITTEE | RECEPTION CO | OMMITTEE |
|---------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|----------------------------------------------------------------------------------------------------------|
| Chairman S.C. Jain, Delhi. Co-chairman S. Radhakrishna, Madras. | Chairman Members | M. Krishnamurthi, Delhi. A. Balasubramanian, Delhi. Subhash Chandra, Delhi. A.K. De, Delhi. |
| Secretary T.Rs Reddy, Delhi. | | S.C. Jain, Delhi. P.N. Kapoor, Delhi. I.J. Kumar, Delhi. |
| Joint Secretaries Vinita Aggarwal, Delhi (Technical Programme). G.D. Sharda, Delhi (Book Exhibition). | | N. Lakshmipathi, Delhi. S.A. Mohile, Delhi. S.S. Murthy, Delhi. |
| Members A.B. Bhattacharyya, Delhi. H.L. Chhatwal, Delhi (Souvenir). S.M. Goklaney, Delhi. D.K. Gupta, Delhi (Technical Programme). | | S.G. Payara, Delhi. A.K. Sreedhar, Delhi. G.P. Srivastava, Delhi. D. Swaroop, Delhi. L.K. Wadhwa, Delhi. |
| R.K. Jain, Delhi (Poster Session). K.N. Johry, Delhi. | LOCAL ORGAN (Sub Committee of R | VISING COMMITTEE |
| V.Kodali, Delhi. Pran Kishan, Delhi (Publication). M. Krishnamurthi, Delhi. K.R. Paramesvar, Bangalore. | Chairman Co-chairman Secretaries | M. Gopal Rao Prem Swarup R.K. Jain Pran Kishan |
| K.V. Ramanathan, Bombay.A. Bhaskara Rao, Hyderabad.M. Gopal Rao, Delhi.B. Ramachandra Rao, Delhi. | Conveners Lecture Hall/ Projection Facilities | Vinod K. Jain |
| S. Sampath, Kanpur.B.L. Sharma, Delhi (Technical Programme).Amarjit Singh, Pilani.M.S. Singh, Delhi. | Accommodation Reception, Registration & Transport | K.K. Laroia B.M. Prasad |
| G. Soni, Delhi. G.D. Sootha, Delhi. A.K. Sreedhar, Delhi. | Lunch Arrangements | R.K. Madaria |
| G.P. Srivastava, Delhi. K.B. Subramanian, Delhi. | Cultural Programme | A.V.R. Warrier |
| Prem Swarup, Delhi (Technical Programme). | | RAMME COMMITTEE |
| P.C. Tripathi, Delhi. U. Venkateswarlu, Sahibabad. | Convener | Mrs. Sudha Jain |
| o. Tonkanomaria, Danibabad. | Members | Mrs. Manju Agarwal Mrs. K. Bahl |

Mrs. Anuradha Laroia Mrs. T. Sreelekha Mrs. Rema Warrier

CHAIRMEN OF TECHNICAL SESSIONS

| DATE | TIME | SESSION | CHAIRMEN |
|-----------------------|----------------|----------------------------------------|---------------------------------------------------|
| MONDAY 23-11-81 | 1115-1330 Hrs. | PLENARY SESSION | J.J. LOFERSKI P.T. LANDSBERG* |
| | 1430-1800 Hrs. | PLENARY SESSION | R.P. MERTENS V.M. TUCHKEVICH* |
| TUESDAY 24-11-81 | 0845-1100 Hrs. | PLENARY SESSION | J.L. MOLL B.D. NAG CHAUDHURI |
| | 1130-1330 Hrs. | SESSION I SESSION II SESSION III | L.S. KOTHARI A. SILARD D. REDFIELD* |
| | 1430-1730 Hrs. | SESSION IV SESSION V SESSION VI | CHATAR SINGH PREM SWARUP K.R. PARAMESVAR |
| WEDNESDAY 25-11-81 | 0845-1100 Hrs. | PLENARY SESSION | S. RADHAKRISHNA K.W. BOER |
| | 1130-1330 Hrs. | PLENARY SESSION | S. DEB |
| | | | B.A. JOYCE |
| | 1430–1615 Hrs. | PLENARY SESSION | J. NISHIZAWA R. KASSING* |
| THURSDAY 26-11-81 | 0845-1100 Hrs. | PLENARY SESSION | K.N. MATHUR J.J. LOFERSKI |
| | 1130-1330 Hrs. | PLENARY SESSION | S. SAMPATH T.J. COUTTS |
| FRIDAY 27-11-81 | 0830-1300 Hrs. | POSTER SESSIONS | SORIN CRISTOLOVEANU R. SHARAN S.K. KRAWCZYK |
| | 1430-1815 Hrs. | PLENARY SESSION | E.L. HEASELL* A.K. SREEDHAR |
| SATURDAY 28-11-81 | 0845-1100 Hrs. | PLENARY SESSION | R. VAN OVERSTRAETEN* AMARJIT SINGH |
| ±' | 1130-1330 Hrs. | PLENARY SESSION | A.W. PENN M.G. RAO |

^{*}Members of International Advisory Committee.

CONTENTS

| Pre | face | | iii |
|-----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------|-------|
| | nowledgements | | ix |
| Cor | nmittees | | xi |
| Cha | irmen of Technical Sessions | | xii |
| | I: BIPOLAR | | 1-127 |
| -1. | Effect of Heavy Doping, Drift Field, High Injection and Photo Generation of Carriers on the Open Circuit Voltage Decay and Switching Time of p-n Junction Diodes. | S.C. JAIN | _ 1 |
| 2. | Investigations of Some Physical Effects in Bipolar Transistors. | W. KUZMICZ | 18 |
| 3. | Auger Effects in Semiconductors. | P.T. LANDSBERG | 30 |
| 4. | Modeling of High Injection Effects. | R.P. MERTENS | 42 |
| 5. | Recombination in Heavily Doped Silicon. | R.P. MERTENS | 52 |
| 6. | The Theory of Ideal Heterojunctions. | A. NUSSBAUM | 60 |
| | Bandgap Narrowing in Heavily Doped Silicon—Theory. | R.J. VAN OVERSTRAETEN | 69 |
| | Bandgap Narrowing in Heavily Doped Silicon— Experiments. | R.J. VAN OVERSTRAETEN | 79 |
| 9. | Large Area P/N Junction for the Detection of Nuclear Radiation. | A.W. PENN | 84 |
| 10. | Recent Trends in the Utilization of Heterojunctions and Diffusion Problems Encountered. | B.L. SHARMA | 96 |
| 11. | Transport in Devices with Nonuniform Band Structure Including Devices with Band Gap Narrowing due to Heavy Doping. | C.M. VAN VLIET and A.H. MARSHAK | 115 |
| | II: MOS | 12 | 8-221 |
| 1. | Barrier Formation and Transport in Schottky Barrier and Semiconductor/Semiconductor Heterojunctions. | S.J. FONASH | 128 |
| 2. | MOS Theory for Short Channel Devices. | H. GUCKEL | 146 |
| 3. | The MOS Device as a Diagnostic Tool. | E.L. HEASELL | 154 |
| 4. | On the Origin of Interface States in Schottky Barriers and Thin MOS Diodes. | S. KAR | 163 |
| 5. | Recent Advances in Understanding of the Photoelectric Phenomena in Metal-Insulator-Semiconductor/MIS/Structures and Their Application. | S.K. KRAWCZYK | 164 |

| xiv | Contents | | |
|----------------------|------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|---------|
| 6. | Metal-TunnelInsulator-Silicon $(n/p^+, p/n^+)$ | A.G. NASSIBIAN | 181 |
| | Switching Devices. | | |
| 7. | Work Function Difference in MOS Structures; Current Understanding and New Measurement Methods. | H.M. PRZEWLOCKI | 191 |
| 8. | Non-uniformities in Ultrathin Oxide Layers and Their Thickness Distribution. | G.P. SRIVASTAVA, P.K. BHATNAGAR and S.R. DHARIWAL | 202 |
| 9. | Recent Understanding of Morphology of Si-SiO ₂ Interface and Trap States at the Interface. | T. SUGANO | 212 |
| | III: VLSI | 22 | 22-304 |
| 1. | Hot-Electron Trapping in SiO ₂ and Its Effect on the Reliability of VLSI. | A.B. RHATTACHARYYA | 222 |
| 2. | Scalability Issues and Limits in the Submicron VLSI Era | P.K. CHATTERJEE | 231 |
| 3. | Charge Coupled Devices: Physics, Technology and Applications. | L.J.M. ESSER | 235 |
| 4. | Developments in Silicon Technology for Increased Circuit Speed. | J. MOLL | 250 |
| 5. | Computer Aided Design and Design Automation in VLSI. | K.V. RAMANATHAN | 266 |
| 6. | Limits to Improvement of Silicon Integrated Circuits. | V.L. RIDEOUT | 274 |
| 7. | Present Trends in Charge Coupled Devices. | D.K. SCHRODER | 282 |
| 8. | Convenient Contactless Semiconductor Characterization Techniques for Enhanced LSI Diagnostic and Process Control Capability. | R.L. VERKUIL | 290 |
| 9. | Machines, Materials and Processes for Near and Submicron Fabrications. | N.S. VISWANATHAN | 304 |
| IV:SOLAR CELLS 305-5 | | | 305-524 |
| 1. | The Shape of Non Ideal Current Voltage Characteristics of Heterojunction Solar Cells. | K.W. BOER | 305 |
| 2. | Physics and Development of CdS/Cu ₂ S Heterojunctions for Thin Film Photovoltaic Generators. | H.G. BOGENSBERGER | 353 |
| 3. | Recent Advances in Silicon Solar Cells. | H.W. BRANDHORST, JR | |
| 4. | Physical Mechanism in Silicon Solar Cells. | L. CASTANER | 370 |
| 5. | Fundamental Studies on Solar Cells Based on InP. | T.J. COUTTS and N.M. PEARSALL | 380 |
| 6. | Application of Low High Junction to Solar Cells. | S.C. JAIN | 394 |
| 7. | Thin Film Heterojunction Solar Cells Based on n-CdS and p-Cu Ternary Alloys. | J.J. LOFERSKI | 408 |
| 8 | Progress in New Thin Film Solar Cell Technologies. | K.W. MITCHELL | 420 |
| 9 | Physics of Limitations on the Efficiency of Silicon Solar Cells. | D. REDFIELD | 450 |
| 10 | Recent Progresses in Crystalline Si Solar Cells. | M. RODOT | 459 |

| | Contents | ~ | xv |
|-----|----------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------|-----------------------|
| 11. | Potential of Polycrystalline Silicon Solar Cells. | B.L. SOPORI | 470 |
| 12. | Present Status of Photovoltaic Research and Development in Japan. | K. TAKAHASHI | 479 |
| 13. | High Injection Effects on Steady State and Transient Response of a p-n Junction Solar Cell/Diode. | V.K. TEWARY | 494 |
| 14. | High Doping Effects and Life Time Measurements in High Efficiency Solar Cells. | H.T. WEAVER | 507 |
| | V: MATERIALS | 5. | 25-666 |
| 1. | Transport in Amorphous Hydrogenated Silicon | S. GUHA | 525 |
| 2. | Some ESCA Studies on Surface and Interfaces with GaAs. | H.L. HARTNAGEL | 531 |
| 3. | Kinetics, Mechanism and Surface Processes in Molecular Beam Epitaxy of III-V Compounds and Alloys. | B.A. JOYCE | 542 |
| 4. | Deep Level Impurities in Semiconductors—Their Properties and Importance for Devices. | R. KASSING | 551 |
| 5. | Non Stoichiometry in III-V Compounds and Its Application for Devices. | J. NISHIZAWA and K. SUTO | 572 |
| 6. | Analysis of Surfaces and Interfaces for Polycrystalline Si and GaAs Films. | G.D. O'CLOCK, Jr. and L.D. FEISEL | 583 |
| 7. | Study of the Growth and Some Properties of Si-Ge Alloys, Semi-isolating GaAs, Al _x Ga _{1-x} As, GaP and Cubic SiC. | M.S. SAIDOV | 591 |
| 8. | Physics and Application of the Semiconductor InGaAsP. | A. SCHLACHETZKI | 601 |
| 9. | The Surface Chemistry of GaAs and Related Compounds. | B. SCHWARTZ | 614 |
| 10. | The Electronic Properties of Semiconductor Grain Boundaries. | C.H. SEAGER | 615 |
| 11. | Physics of Lead Tin Chalcogenides. | A.K. SREEDHAR | 643 |
| 12. | Recent Advances in III-V Ternary and Quaternary Alloys with Emphasis on Semiconductor Photocathodes. | PREM SWARUP | 650 |
| | VI: POWER AND OTHER I | | <7 P.01 |
| 1. | Recent Advances in Power Semiconductor Devices and Materials. | B.J. BALIGA | 6 7-801 667 |
| 2. | Physics of Microwave Transit-Time Devices. | W.S. KHOKLE | 675 |
| | Recent Advances in Power Devices. | P. RAI-CHOUDHURY | 686 |
| 4. | Recent Trends in High Power Devices. | E.S. RAMAMURTHY and B.D. SHARMA | d 697 |
| 5. | Recent Advances in Semiconductor Sensors. | M.G. RAO | 727 |
| 6. | Effects of Inhomogeneities in Large Area Semiconductor Junctions. | R. SHARAN | 740 |

| xvi | Contents | | |
|-----|--------------------------------------------------------------------------------------------------|--------------------------------|-----|
| 7. | Highly Sensitive, Fast Turning-on Light-activated Thyristor Structures. | A. SILARD | 747 |
| | Thermal Characteristics of Microwave Solidstate Devices: Modelling, Technology and Measurements. | Z.J. STASZAK | 767 |
| 9. | A Noise Model for Circuit and Device Noise Analysis. | L.A. TRINOGGA and S.W. HOLLAND | 777 |
| 10. | Recent Progress in Power Semiconductor Technology. | V.M. TUCHKEVICH | 788 |
| | | | |
| | | | |
| | | | |

Effect of Heavy Doping Drift Field, High Injection and Photo Generation of carriers on the Open Circuit Voltage Decay and Switching Time of p-n junction diodes.

S.C. Jain Solid State Physics Laboratory, Lucknow Road, Delhi-110007, India

ABSTRACT

The results of the Open Circuit Voltage Decay experiments show large discrepancies with the theory applicable to a base dominated diode. There are four important effects which modify the Open Circuit Voltage Decay plots. These are heavy doping effects, drift field effects, photo generation of carriers and high injection effects. The heavy doping effects increase the dark saturation current of the emitter. The diode does not remain base dominated and recombination of carriers during the transient processes in the emitter and the base become coupled. The mathematical theory shows that the quasi-static approximation in the emitter is valid to a good approximation but does not hold in the base. The heavy doping effects make the voltage decay faster in the early stages. For large values of time however, decay rate becomes practically independent of heavy doping effects. The drift field also modifies the voltage decay considerably. The retarding drift field in the base pushes both majority and minority carriers into the emitter during the voltage decay process. The number of recombinations in the emitter increases and the voltage decay becomes faster. The effect of photo generation makes the decay of the voltage some-what slower, particularly if the wavelength of the exciting light is long. At high injections, a plateau in the voltage decay curve has been observed for many years. Recent theoretical work to explain this plateau is described.