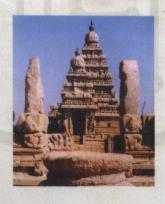
CONFERENCE PROCEEDINGS

1995 International Conference on Electromagnetic Interference and Compatibility (INCEMIC)

Organised by The Society of EMC Engineers (India)

6 — 8, December
Park Sheraton Hotel and Towers
Madras, India



1995 International Conference on Electromagnetic Interference and Compatibility (INCEMIC)

Conference Proceedings

Park Sheraton Hotel and Towers, Madras, India.

December 6-8, 1995

Organised by The Society of EMC Engineers (INDIA)

1995 International Conference on Electromagnetic Interference and Compatibility (INCEMIC '95)

Additional copies of the Conference Proceedings may be ordered from:

Society of EMC Engineers (INDIA), Madras Chapter, SAMEER - Centre for Electromagnetics CIT Campus, 2nd Cross Road, Taramani, Madras - 600 113 INDIA.

IEEE Catalog Number:

95TH8121

ISBN:

0-7803-2939-2

Softbound Edition Microfiche Edition

Library of Congress:

NA

Chairman's Message



The JNCEMJC'95 committee and the Society of EMC Engineers (India) extend a hearty welcome to all the delegates to the fourth International event being held in Madras during December 6-8, 1995. We, in India have come a long way in practicing rigorous EMJ testing and EMJ control of defence electronic systems as well as industrial products for compliance with EMC regulations. The realisation has bugged our industry captains to put concrete efforts for quality improvement of electronic products to be globally competitive in the post economic liberalisation era. The impact of non-performance of a designed product due to non-compliance of accepted EMC norms and standards without the iron rod legislative sanction in our case, will put our

indigenous products out of market. The EMC is a global concern and it is amply proved by European union's EMC directives and other agencies like CJSPR, FCC, JEEE etc. Hence, by its nature the practice of EMC requires a continuous awareness programme of what is happening in the contemporary technology. These days 'EMC Science' is accepted and respected as a technical discipline and deserves a rightful recognition in our over crowded under-graduate level of engineering.

The EMC design and standards have proliferated over the years, as governing bodies world-wide have imposed their own set of requirements to ensure that designed electronic equipment functions desirably in its intended environment. EMC standards and specifications are constantly modified/upgraded world over to suit the reality of globalisation of market. It is always prudent to think that it should be a reality now for harmonisation of EMC standards and method of testing on an international level. I strongly believe it is very important to become a party of the unified EMC culture. Our theme during this event "EMJ in Communication Systems - European Community EMC Directive" is absolutely perfect, considering the marriage of information technology, consumer electronics and communication systems in today's global market. The fruitful wireless revolution of nineties that will bring about an EM situation along with intelligent computation needs to be looked into in an over all EMC environmental design perspective to avoid information blockage.

The crux of the problem is "Human Resource development and EMC technology educated engineers". Hence, inclusion of a balanced EMC undergraduate course in our engineering curricula is a must. The slogan should be "EMC education for all" especially for those who are involved in a system design life cycle.

MADRAS INCEMIC is all set to discuss most of these problems during the workshop and conference to be attended by galaxy of EMC national and international experts during 4-8 December 1995.

Our committee has worked very hard and put its best. I am very thankful to all my colleagues for that.

J sincerely express my thanks to JEEE, Madras section, Society of EMC Engineers (India), SAMEER - CEM, Madras, Department of Electronics, Govt. of India, and several agencies and various academic institutions for extending their support for the success of fourth JNCEMJC'95.

On behalf of JNCEMJC '95 Committee a hearty welcome to all the delegates.

G.K. Deb Honorary Chairman, INCEMIC'95

INCEMIC '95 COMMITTEES

Advisory Committee

Patron

N Vittal

Members

APJ Abdul Kalam R Aravamudan Bharati Bhat **BN Das** JJ Baxi PS Bhargava

Edwin L Bronough George Joseph BD Pradhan NP Ramasubba Rao **RP Shenov**

KK Srivasatava

VP Sandlas SL Sarnot DPS Seth N Sreenivasan **BS Sonde**

P.B. Tole TK Sarkar Shuichi Nitta Gabriel V Meyer Zhang Linchang **DD Weiner**

Organising Committee

Honorary Chairman

G K Deb

Convenors K R Kini

Y Narayana Rao

Finance

S Karuanakaran

Members

NK Agarwai Ajoy Chakraborthy CL Arora R Chellappan R Jayakumar VR Katti

GR Nagabhushana K Nageswara Rao DC Pande DR Poddar NR Ravi

GSN Raju

Ramamani Sundaram SR Ramasami PNAP Rao S Ranganathan **BK Sinha** SK.Das

CR Sasi SVK Sastry ML Sharma SS Shekhawat **KR Suresh**

International Co-ordination Committee

DV Giri

D Heino Gregorek **Hugh W Denny**

Hussain Bin Ahmad NC Kini

Pro-tech, California, USA Rohde & Schwarz, Delhi

GTRI, USA

Univ. of Tech., Malaysia

Eltel, Dubai

A Lazarov Motohisa Kanda Oren Hartal SM Rao Vijay Bhargava

Technical Univ. of Sofia, Bulgaria NIST, Boulder, USA Rafael, Israel Auburn Univ., USA Univ. of Victoria, Canada

Steering Committee

Chairman

BK Sinha

Convenors

KR Kini Y Narayana Rao **Treasurer** S Karunakaran **Technical Programme** Sisir K Das

Publications Exhibits Registration

Local Arrangements

AL Das **KVK Sampath** KR Vijayan TS Ramakrishnan **Publicity** Hospitality

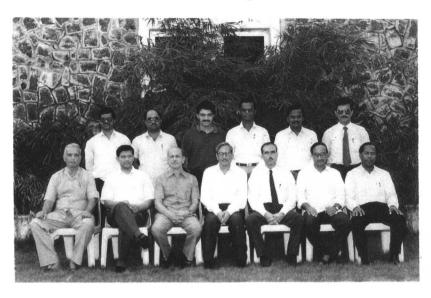
Conference Arrangements Workshop Arrangements

K Udayakumar

K Sridhar R Ganesan K Mourougayane AP Sajeev

INCEMIC '95

Conference Steering Committee



Seated from left to right

T.S. Ramakrishnan, Local Arrangements; Sisir K. Das, Technical Programme; Y. Narayana Rao, Convenor, B.K. Sinha, Chairman; K.R. Kini, Convenor, S. Karunakaran, Treasurer, A.L. Das, Publications.

Standing from left to right

K. Mourougayane, Conference, K.R. Vijayan, Registration; A.P. Sajeev, Workshop; R. Ganesan, Hospitality; K. Sridhar, Publicity; K.V.K. Sampath, Exhibits.

CONTENTS

Plenary Session P1		EMC in Digital Circuit Design HPS Bawa, Army EMC Agency, Mhow	44
The effect of New European Norms on the World EMC Market	1	Experimental Studies on the Effect of Microwave Energy in Digital Circuits	48
Michael Norton, President Key Tek Instrument Corp. USA		D.C. Pande, Manoj Gopinath, LRDE, Bangalore	
Session 1A EMC in Communication System		Spurious Alarm Problem in Alarm Annunciator System and Ways of overcoming the Same	49
Chairperson: S.R.Ramasamy, RCI, Hyderabad.		V.P. Verma;V.S. Sankara Narayanan, R. Manivannan, BHEL, Bangalore	
Optimum Placement of DC-Blocks in Ultra Broad Band Microwave Receivers	5	Session 2A	
A. Gangadhara Rao, K.V Narayana Murthy,		EMC in PCB	
DLRL, Hyderabad		Chairperson : Oren Hartal, Rafael, A.D.A., Israel.	
Over Coming Data Corruption in RS485	9	Out and at DOD Totals law and area by	F 0
Communication V: Ajay Kumar India Meters Ltd., Madras		Control of PCB Track Impedance by Adequate Buffering, Proper Termination,	53
V. Ajay Kulliar Illula Meters Etu., Madras		Careful Routing Strategies Establishes	
	13	Signal Quality in Microprocessor Based	
Intrasystem EMI Control in High Frequency Communication System		Systems V. Ajay Kumar, India Meters Ltd., Madras	
M.J. Mombasawala, G. Dharmaraj, Naresh		PMO Avaluate in DOD Decima Hoine on	60
Madan, C-DOT, New Delhi		EMC Analysis in PCB Designs Using an Expert System	59
Spectrum Utilisation by Frequency Hopping	20	K. Nageswara Rao, P. Venkata Ramana, RCI,	
Radio Systems VIS-A-VIS Fixed Frequency		Hyderabad	
Radio Systems		M.V. Krishnamurthy, K. Srinivas, DRDL, Hyderabad	
Rajender Singh Army EMC Agency, Mhow		1,740,4544	
Dynamic Range Improvement Through	27	High Frequency Characterisation of	63
Ground Noise Control in a Data Communication Equipment		Multilayer PCBs S.Y. Kulkarni, BVB College of Engg. and Tech.,	
Keta Meera Sahebu, M.R. Kesheorey, M.V. Rao		Hubli,	
LRDE, Bangalore		K.V.V. Murthy, IIT, Bombay, N.N.S.S.R.K. Prasad, Y.G.K. Patro, SAMEER, Bombay	
Susceptible Threshold Level Determination and Immunisation of Signal to Interference	31	EMC Design Verification of PCB	67
G.R. Panda, U. Apato, Interim Test Range,		A.K. Ghose, G.K. Deb, ER&DC, Calcutta	
Chandipur		PCB Design With Low EMI	69
		A.K. Ghose, G.K. Deb, S.K. Mandal, ER&DC, Calcutta	
Session 1B		Coupling Between Microstrip Transmission	77
EMC in Digital Systems Chairperson: P.N.A.P. Rao,		Lines	
ADA, Bangalore		P.S. Bhattacharjee, S. Das, S.K. Chowdhury, Jadavpur University, Calcutta	
Multichip Module Structures for Minimising Cross-Talk Effects in High-Speed Applications	34		
S.Y. Kulkarni, BVB College of Engg. and Tech., Hubli, K.V.K. Murthy, IIT, Bombay			
Notebook Computer and Low EMI Approach A.K. Ghose, G.K. Deb, ER&DC, Calcutta	40		

Session 2B Transient EMI Chairperson: P.H. Ron, BARC, Bombay		Session 4A EMC Measurements and Instrumentation - I Chairperson Motohisa Kanda. NIST, USA	
Some Studies of Corona Noise Interference Due to EHV Lines Pramod Kumar, Rachina Garg, Mini S. Thomas College of Engineering, Delhi	82	Reflectivity Measurements of Metalized Fabric P.C. Mehta, H.S. Raina. Space Application Centre, Ahmedabad	138
Effects of Lightning N. Suresh Kumar, A. Vallavaraj, S. Salivahanan Mepco Schlenk Engg. College, Sivakasi	91	Life Test and Electromagnetic Shielding Performance Evaluation of EMI Gaskets A K Subramanian, D.C. Pande, K. Boaz, LRDE, Bangalore	139
EMI Sources Transients Related to Lightning and EMP Rohit Shukla, Army EMC Agency, Mhow Transient Electromagnetic Field Coupling to	96 100	Analysis of Various Types of Pierce Electron Guns by Finite Element Method T. Tiwari, R.K. Jha, Banaras Hindu University, Varanasi	148
Underground Shelter D.C. Pande, J.P. Vardhani, LRDE, Bangalore EMI Analysis of Automotive Vehicles and its Suppression Techniques B. Subba Rao, Sisir K. Das, SAMEER-CEM,	108	Measurements and Analysis of Transient Electromagnetic Shielding Effectiveness for Nested Shield Configurations R.K. Rajawat, R.S. Kalghatgi, P.H. Ron, BARC, Bombay	153
Madras Session 3 EMC Education Chairperson : G.S.N. Raju,		Shielding Efficiency Measurements Under Pulsed Electromagnetic Fields V. Venkateswaralu, Y. Narayana Rao, IIT, Madras	161
Andhra University, Visakhapatnam Relevance of EMC Education in Undergraduate Course	118	A Current Branching Test JIG (CBTJ) - for Conducted Emission G. Abdul Latheef, A.P. Sajeev, L. Punitha SAMEER-CEM, Madras	166
G.K. Deb, ER&DC, Calcutta Electromagnetic Interference and Soical Audit T.K. Dey, KTS, Kanchrapara, D.R. Poddar, S.K.	126	Automated Radiated Emission Measurements from 18-40 GHz P.C. Mehta, H.S. Raina, Space Application Centre, Ahmedabad	169
Chowdhury, Jadavpur University, Calcutta EMC Education Thro' Distance Mode and Open Learning T.K. Dey, C.K. Ghosh, IGNOU, Calcutta	130	Designing a System for Testing to European Radiated Immunity Test Regulations Joe Sivaswamy, EMC Automation Austin, USA	174
Plenary Session P2 Recent Techniques in Electromagnetic Modelling and Analysis	131	Session 4B EMI Prediction and Modelling Chairperson: S.M. Rao, University of Auburn, USA	
SM Rao, G.K. Gothard Department of EE, Auburn Univ. USA.		Modelling of an Electromagnetic Pulse Simulator and Evaluation of its Radiated Field V. Venkateswarlu R. Sarathi, Y. Narayana Rao, IIT, Madras	184
		Simulation of the Response of External ESD Protection Circuits for CMOS ICs R. Renuka, R. Jeyanthi, V.K. Hariharan, S.V.K. Shastry ISRO Satellite Centre, Bangalore	190

Electromagnetic Field to Unshielded Wire Coupling Model V.K. Hariharan, V. Lakshminarayana, R.K. Pal, P.V.N. Murthy, S.V.K. Shastry, V.R. Katti, SRO Satellite Centre, Bangalore	198	Testing Michael Hopkins, Colin Hatchard, Thermo Voltek Corpn., Wilmington, USA	256
Lightning Attachment and High Current Studies on Aircraft Nose Radome M. Joy Thomas, G.R. Nagabhushana, IISc, Bangalore	208	Analysis of Higher Order Modes in TEM Cells and RCL Using Mode Matching Method Uma Balaji, Ruediger Vahldieck, University of Victoria, Canada	259
Automated Frequency Assignment to Obtain Operational EMC in the battle field AP Singh, Army EMC Agency, Mhow	209	Calibration Of Antenna Factor of a Tuned Dipole Using Two Antenna Method, NIST Method and a Proposed Reference Antenna Sisir K. Das, P.H. Reo SAMEER-CEM, Medras Motohisa Kanda, NIST, USA	263
Computer Aided Design for Shelter Performance Analysis	216		
<i>Amalendu Bhagwat,</i> LRDE, Bangalore, <i>Manik</i> <i>Mukherjee,</i> DRDO, New Delhi		Session 5B EMI Case Studies Chairpement V.P. Sandles	
Composite Stochastic Model for EMI	224	Chairperson: V.P. Sandlas, DEAL Dehradun	
D.P. Roy, FEL MCEME, Secunderabad		EMI Susceptibility Characteristics of Electromedical Equipment in a Typical	266
Computer Aided Design of Various Types of Pierce Electron Guns T. Tiwari, R.K. Jha, Banaras Hindu University, Varanasi	227	Hospital Electromagnetic Environment with Particular Reference to Electrocardiography S. Bendopadhyay, James K. Varkey, Army EMC Agency, Mhow	
		Case Study - MRI Facility in Hospital Environment	273
Session 5A EMC Measurements and Instrumentation - II Chairperson: N.K. Agarwal,		A. Sathyanarayanan, Ajay Aggrwal, Sulekh Chand, ERTL (North), Delhi	
VSSC, Trivandrum		Common Mode Coupling in INSAT-2: A Case History	280
Dipole Excitation of RF Shielded Chamber S.V.K. Shastry, S.K. Nagesh, M.N. Rao, ISRO Satellite Centre Bangalore	230	N. Prahlad Rao, M.N. Rao, T.V.S. Rambabu, H.Y. Managoli, S.C. Vinod, Y.K. Singhal, V.R. Katti, ISRO Satellite Centre, Bangalore	
Resistively-loaded Antennas for Transient Electromagnetic Field Measurements Karu .P Esselle, Macquarie University, Australia, Stan .S Stuchly, University of Victoria,	237	EMC design of a modern fighter aircraft- Case study P.N.A.P. Rao, N.S. Chandrasekhar, T. Parthasarathy, ADA, Bangalore	284
Canada		EMi Hardening of Missle Ground Support	300
EMI Test Facilities - Sensors and Instrumentation	241	Systems - A Case Study Bhudeb Chakravarti, Devender, RCI, Hyderabad	
D. Suresh , MAG No.1, Bangalore	049	Typical EMI Problems in a Surface to Air	305
Test System for Mobile Field-Strength Recording Yatish Mohan, Rohde & Schwarz, New Delhi	242	Missile - A Case Study K. Suryanarayana, K. Rajeshwar Rao, RCI, Hyderabad	
Automation of Conducted Susceptibility Testing L. Dinesh Roy, B.N. Prakash, Bharat Electronics, Bangalore	246	EMI Control Plan for Ships Arvind Ranganathan, Indian Navy, Hyderabad	310
Fluoroptic Thermometry - its Potential for Testing EED's Against HERO K. Sharma, Sanjay Datt, Naval EMC Centre, Bombay	248		

Session 6A EMI Control Techniques - I Chairperson : B.N. Das, I.I.T, Kharagpur		Implications of MIL-STD 461D/462D on Past Present and Future Designs D.C. Pande, LRDE, Bangalore, K.P. Shaju, R. Biliya, CABS, Bangalore	379
Some Studies on Arrays for Side Lobe Reduction Required for EMI Control P.M. Rao, P.V. Sridevi, G.S.N. Raju, C.S. Gutti,	314	Limitations of Bulk Current Injection Tests Oren Hartal, Rafael ADA, Israel	386
M.V.V. Prasad, Andhra University, Visakhapatnam, K.V.L. Raju, College of		Tailoring of EMI/EMC Specification and Test Plans	394
Engineering, Eluru		V.M. Tyagi, U.V. Natu, Pritam Lal, Naval EMC Centre, Bombay	
A Technique for Depositing Metal Layers Over Large Areas of EMI Shielding M.S. Bhatia, BARC, Bombay	321	Relevance of EMC Specification in ISO 9000/ IS 14000 Scenario V.N. Ray, Defence Standardisation Cell,	401
Application of Fittering, Shielding and Grounding Techniques for An On-Line	325	Bangalore	
Process Controller in a Jute Mill Arindam Chatterjee, G.K. Deb, ER&DC, Calcutta		Plenary Session P3	
Critical Core Parameters in the Design of	334	EMC Strategies : Industrial & Research Perspectives.	403
Common Mode Suppression Chokes H. Hemphill, B. Wallertz, Schaffner Elecktronik AG, Switzerland		Motohisa Kanda, NIST, Boulder, USA	
	227	Session 7A	
PSLV-D2 Grounding Scheme and EMC Consideration	337	EMI Control Techniques - II Chairperson : S. Nitta,	
N. Narayana Moorthy, P.S. Veeraraghavan, VSSC, Trivandrum		Tokyo, Jap a n.	
Novel Manufacturing and Assembly	338	Need for an Integrated Approach to Mitigate EMX Problems	405
Approach to EMI/RFI Suppression of High Density Connectors		A.K. Subramanian, D.C. Pande, LRDE,	
S.J. North, C.J. Noade, R. Pennel, Oxley		Bangalore	
Developments Co. Ltd., Ulverston Cumbria, U.K.		Radio Frequency Shielding with Aluminium Honeycomb	413
Noise Reduction Techniques in Pulse Width Modulated Inductive Switching Systems of	343	B.M. Rakshit, Aerospace International Inc., Bangalore	
PSLV R. Uma Maheswaran, N. Narayanamurthy, P.S.		Problems in EMI Control of Power Electronics for Futuristic Offshore Fixed and	416
Veeraraghavan, VSSC, Trivandrum		Floating Energy Platforms P. Mishra, Ministry of Surface Transport, Visakhapatnam	
Session 6B		·	
EMI Standards and Regulations Chairperson : G.K. Deb, ER & DC, Calcutte.		Analysis & Control of EMI from Single Phase Commutator Motors used in Home	423
Some Observations on Bulk Cable Current Injection Test Method M. Devarajan, VSSC, Trivandrum	355	Applicances R. Ganesan, Sisir. K. Das, B.K. Sinha, SAMEER-CEM, Madras	
Impact of MIL-STD-461D on Aerospace	362	EMI-Control Methodologies-Application in a Low Cost TV Demodulator System	428
Equipments	JU2	A.K. Sarkar, S.K. Mondal, A.K. Ghosh, G.K.	
N.K. Agarwal, VSSC, Trivandrum		Deb, ER&DC, Calcutta	
Practical Process to Enter the European Single Market Regarding the EMC directive Werner Hirschi, Hubert Sauvain, EMC Fribourg SA, Switzerland	370	Tuned Band Reject Power Line EMI Filter G. Abdul Letheef, S. Kerunekaran, K. Sridhar, SAMEER-CEM, Madras	436

A Practical Study of EMC - Aspect in Electronic Ballast: An Analysis	440	Poster Session	
S. Chatterjee, D. Basu, ERTL(E), Calcutta		Indigenous development of a RFI shielded cabinet for electronic assemblies	493
Metal Oxide Varistors as Surge Suppressors V.P. Rabde, Philips India Ltd., Pune	445	T.L. Venkatasubramani TIFR, Pune	40.4
Session 7B		Radar transparancy of coal mines G.Sahoo, S.Sanyal, A.Chakraborty, B.K. Sarap, IIT, Kharagpur.	494
Antennas		D.A. Surup, III, Islandgpul.	
Chairperson : B.K. Sinha, SAMEER, Madras.			
Patterns of Discrete Radiating Elements for Different Current Distributions R. Mohan, K. Ramakrishne, K. Srinath, K.N. Sunil Kumar, G.S.N.Raju, Andhra University, Vishakapatnam, K. Srinivasa Rao, SCRRCE, Eluru	450		
Radiation Patterns of Arbitrarily Oriented	458		
Radiating Elements P. Manju Letha, M. V. Naidu, Y. Gopala Rao, P.U.M. Rao, G.S.N.Raju, G.M. Rao Andhra University, Visakhapatnam			
Evaluation of Active Admittance of Radiating Apertures for the Purpose of Performance Analysis in a Dynamic Environment Ajay Chakraborty, K.S. Range Rao, IIT, Kharagpur S.K. Chowdhury, Jadavpur University Calcutta	466	•	
The Analysis and Placement of Yagi-Uda	473		
Arrays in Multi-Antenna Receiving System P.H. Reo, J.K. Mishra, Alok Chordia, SAMEER-CEM, Madras	4.0		
Pyramidal Horn Gain Prediction: Evolution of Formulations	476		
K.T. Selvan, P.H. Rao, P. Ramakrishna, B. Subba Rao, SAMEER-CEM, Madras			
Near-Field Analysis of Solid Dielectric Diagonal Horn Antenna V.N. Tiwari, T. Tiwari, S.P. Singh, R.K. Jha, Banaras Hindu University, Varnasi	478		
Far Field Patterns of Hollow Dielectric Wall Diagnoal Horn Antennas V.N. Tiwari, T.Tiwari, S.P. Singh, R.K. Jha, Banaras Hindu University, Varnasi	484	ļ	
EMI/EMC Considerations in Antenna Design and usage for a Communication System H.K Gupta, K.K. Jha, C.L. Arora, DEAL, Dheradun	489		

此为试读,需要完整PDF请访问: www.ertongbook.com

Japan.

THE EFFECT OF NEW EUROPEAN NORMS ON THE WORLD EMC MARKET

Michael Norton, President KeyTek Instrument Corporation

Introduction

The European Union has issued an EMC Directive requiring virtually all electrical and electronic products be tested for EMC compliance. This Directive will have an economic effect on manufacturers world-wide and influence standards development at international, national and corporate levels. Although the test requirements are driven by actual events involving public safety, the standards themselves now only require minimum levels of testing. As the EMC field grows and expands, standards need to evolve and become more realistic.

Driving Forces

As electronic products evolve, there is a continuing requirement for more capability in smaller packages. This requires smaller and faster electronic devices, which by their nature become more sensitive to the effects of electromagnetic fields in the environment, and become a source of unwanted electromagnetic radiation themselves.

Today, we have an unprecedented increase in use of portable cellular telephones, each of which produces electromagnetic fields intentionally, and each of which is a potential source of interference to nearby electronic circuits and systems. Add these to the numbers

of existing radio transmitters, plus all the *un*intentional radiators, such as your personal computer and the microprocessor controlling your automobiles engine, and it becomes easy to see that there will be an increasing compatibility problem.

In recent years, a number of incidents involving EMC safety have been reported in the media, including:

- An aircraft navigation system was being blocked by radiation from a lap-top computer. The pilot of the jetliner documented that when a passenger in first class turned off a lap-top computer, the malfunctioning navigation instruments recovered completely. All airlines now prohibit the use of electronic products during taxi, takeoff, and landing.
- An electric wheelchair went out of control and seriously injured a handicapped person. The ability of electromagnetic radiation to cause an electric wheelchair to become uncontrollable was documented by the U.S. FDA in lab experiments.
- In one hospital, a cellular telephone caused the incubators to fail in an incubation ward. In another hospital, a drug infusion machine malfunctioned, also caused by a

cellular telephone being used in the immediate vicinity. These incidents have led to a ban on the use of cellular telephones in many hospitals

There are many other documented cases, as well as a number of incidents where EMC is suspected of being the cause of failures in electronically controlled machines. Electronic pacemakers seemed to be relatively immune, but new anecdotal evidence exists regarding malfunctions being attributed to EMC.

The EMC Directive

The EMC Directive requires all electrical and electronic products be tested for both immunity to EMI and for unwanted emissions. It applies to all electrical and electronic products being shipped *into* the EU (European Union), or across borders *within* the EU. Compliance with the Directive is effective beginning January 1, 1996. (When the directive was first issued, there was concern that this would be a trade barrier; however, it is now clear that all European manufacturers must also meet the requirements of the Directive if they are to ship products across borders.)

To meet the Directive, one must determine which product or product family standards apply, and then perform the basic EMC tests that it specifies. If no product standard, or product family standard exists for a particular product, the Generic Standards for immunity and emissions apply.

One frequent question is, "Isn't there a grandfather clause for products already being shipped into Europe?", and the

answer is firmly, <u>no.</u> The original date for mandatory compliance was to be then end of 1992; however, it quickly became clear that manufacturers could not comply in such a short period of time (the directive was issued in 1989). As a result of industry pressure, the mandatory date was moved off until January 1, 1996.

It should be noted that enforcement of this directive is a matter of criminal law in each member state. Penalties for non-compliance range from shipping restrictions, to fines of hundreds of thousands of U.S. dollars.

Effects of the EMC Directive

The Directive is having an immediate effect on standards development worldwide. New IEC standards are being written to cover areas where standards didn't exist, national standards around the world are being "harmonized" with the European Norms, and even at the corporate level, in-house test standards are being modified to be in line with the mandatory European Norms.

Markets everywhere are already being affected in both positive and negative ways. Complying with many new standards can be a major investment for the manufacturer. The manufacturer has some choices, the major one being whether to perform all or some of the tests in-house, or to send products to outside test facilities. Each has its own set of trade-offs, including the cost of purchasing test equipment vs. the daily cost of using a test facility, the availability of equipment vs. the lead

times at test facilities¹, and the number of products to be tested.

As with all product costs, the cost of compliance to the EMC Directive will eventually be borne by the consumer. The investments made by the manufacturer to comply with mandatory regulations, must be recovered.

One positive effect of the Directive will be that products will be hardened to some minimum degree, against Electromagnetic Interference (EMI). This could result in a reduction of field failures and returns.

The EMC Test Standards

It is important to understand the requirement for standards, but even more important to understand what they do NOT accomplish. There has been so much emphasis on meeting the standards in order to comply with the regulations that reality sometimes gets lost.

There are always two levels to EMC testing: the requirements for compliance, and the manufacturers requirement for minimum field failures and problems related to EMC.

Immunity of a product to EMC related events is a balance between the costs to harden equipment, and the cost of failure or upset due to EMC. In some fields such as aircraft electronics, medical devices, and control systems at processing plants, no failure can be tolerated; therefore, a large amount of cost is added in redundant circuits and

hardening of the electronics for EMC. Other products, such as consumer electronics and appliances, don't justify the costs or effort required to achieve high levels of immunity. International standards, such as those produced by the IEC, are an attempt to set down some minimum test requirements that everyone can perform. The tests are meant to be repeatable, although it is a continuing struggle to make them so (especially with high frequency phenomena). Tests are also meant to assure compliance at levels that can be achieved with a moderate to low amount of effort and cost -- not always possible when some tests require test chambers or sites costing hundreds of thousands of dollars.

The biggest problem with testing to the European Norms is that it promotes a false sense of security and implies that compliance will make a product immune to real world EMC. This just isn't the case. Don't forget that the standards are meant to provide a *minimum* level of compliance, and must be both repeatable and achievable by most manufacturers.

Although the standards sometimes seem to be written out of context with reality, reality is always in the background. Basic investigative work is ongoing in many areas, especially in areas where the fastest EMC related events occur: ESD (Electrostatic Discharge). New technologies are being investigated to measure and characterize real current impulses. The results of these investigations may result in a better understanding of how to protect sensitive electronic devices and circuits in a more cost effective

¹ As of mid 1995, some test facilities had already gone to multiple shifts, and even with that, were quoting 2-3 month lead times.

way. In addition, basic investigative work is ongoing in all other areas of EMC with the same potential benefits.

The Future

The EMC market is growing very fast and should continue to do so for the immediate future. Always thought to be a "niche" market, EMC is something all manufacturers must now learn to deal with.² Once they begin to comply with mandatory regulations, the positive effects of fewer field problems and better quality products may begin to take over as the driving force. Competitive pressures will take over where the standards leave off and some customers will demand higher levels of immunity than are required by simple compliance.

Although one could argue that EMC test standards have been around for a long time, they are really still in their infancy, especially the immunity standards. In some cases, no immunity standards existed at the time the EMC Directive was issued, in other cases, existing standards were in need of considerable revision before they could be included as European Norms. In the rush to get standards in place before the mandatory dates for compliance, some standards were adopted prematurely, and will need considerable improvement before the objective of repeatability will be achieved.

Now that there is a library of "Basic EMC Standards" written by the IEC,

³ The IEC 1000-4-X Series standards are considered to be "Basic Immunity Standards".

² Although compliance with the EMC Directive becomes mandatory on January 1, 1996, many manufacturers still haven't begun to deal with it.

new work is underway to update, revise, improve, and modify them all. IEC standards are dynamic in the sense that they must be reviewed periodically, and each time they are reviewed, new discoveries or technologies will demand that changes be made.

It is possible that someday the standards will reflect both reality and the current state of technology, but it isn't likely to be soon. What is certain is that for manufacturers in the EMC business, growth is assured for some time to come; for manufacturers of electronic products, improved quality and safety will benefit us all.

OPTIMUM PLACEMENT OF D.C. BLOCKS IN ULTRA BROAD BAND M.W. RECEIVERS

A.Gangadhara Rao AND K.V.Narayana Murthy Defence Electronics Research Laboratory Hyderabad INDIA

ABSTRACT

band Ultra broad M.W. Receivers are used in some Airborne systems. Antennas Multioctave covering frequencies are mounted at convenient locations on the aircraft. The Receivers are kept as close to the antennas as possible. Video outputs from these receivers are carried to signal processor units located at different places in the aircraft. Single point grounding is many times used to avoid loop currents interfering with genuine signals. In such systems single point grounding calls for isolation of some of the hardware from aircraft. Inner/Outer D.C. blocks help in isolating microwave hardware. This paper discusses, as a case study, the optimum placement of D.C. blocks in ultrabroad band microwave receivers.

INTRODUCTION

Single point grounding is many times used to avoid loop currents from different ground returns interfering with genuine signals in system applications.

Practical implementation of single point grounding is very difficult in airborne systems. With many

subsystems located different places of aircraft, single point grounding is more difficult since the aircraft body is the main return point for systems installed on Ultra broad band microwave receivers are used in some airborne systems. In these systems, antennas covering multioctave frequencies are mounted at convenient locations on the aircraft causing minimum distortion to their radiation patterns. The associated receivers are located close to these antennas to reduce RF losses. Direct detection techniques are some times used for signal processing achieving high probability of intercept. The video signal strength detected is of the order of millivolts at the tangential signal sensitivity level and is required to be carried by long lengths of cables to video signal processor units located at the electronic bay of the aircraft. When the antennas, receivers and signal processor units located at different locations in the aircraft are returned to ground at their respective locations, loop currents are circulated among cables. The loop currents need not be restricted to the sub units of the receivers above, but caused by many other electrical systems of the aircraft. These loop currents induce

interference voltages in the video cables of the microwaves receivers, and are as such detrimental to weak video signals of these receivers and the signals get corrupted.

To overcome this problem, floating of the antennas and receivers from aircraft body is a possible solution but difficult to implement. Alternately, the signal processing units can isolated from the receivers by using INNER/OUTER DC block at RF level just before detectors. This technique is easy to implement since only video circuits are to be isolated from the ground before they are returned to ground at single point at the processor level. However, in one of our applications, it was observed that ground noise was exorbitantly high, when INNER/OUTER DC block was introduced in a broad-band microwave receiver. It took considerable diagnostic efforts to identify that the increase in ground noise is due to introduction of the DC block. Circuit analysis indicated that the equivalent inductance of the DC return in the microwave detector coupled to the capacitance of the DC block causing unwarranted oscillations, thereby increasing the ground noise level of the system.

OBSERVATIONS

The block diagram of the receiver system is shown in Fig.1. When this receiver was evaluated for its charactristics, the base line noise (which was 400 mv) far

exceeded the actual noise (100-120 mv) level of the detector log video amplifier. This called for detailed analysis of the designed RF Receiver.

ANALYSIS

As shown in Fig.1, the INNER/OUTER DC block is placed in between the RF amplifier and the detector log video amplifier. The equivalent circuit of this configuration is shown in Fig.2

From this circuit, it can be that the series capacitance of the DC block along with the inductance of the DC return at the input of the detector forms a series resonant circuit giving rise to oscillations. The value of this capacitance is about 50 PF and the self inductance of the DC return is about 7.0 uh. Thus the resonant frequency is around 8.0 MHz. This oscillation frequency is in the video bandwidth of the log video amplifier causing the enhancement of the noise floor.

SOLUTION

To avoid the oscillations causing the enhanced floor noise of the receiver, it is necessary to shift the position of the DC block in the receiver. The final block diagram of the receiver giving the optimum placement of the DC block is shown in Fig.3. When once the receiver was reconfigured to this modified design, the noise level came down to that of the log video amplifier level (i.e. to 120 mv level).