

TN7
B297

9261677

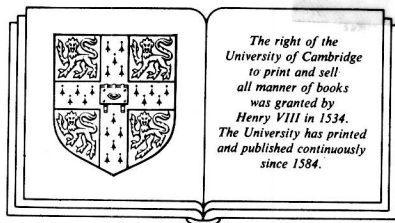
Analogue electronic circuits and systems

AMITAVA BASAK

University of Wales College of Cardiff



E9261677



*The right of the
University of Cambridge
to print and sell
all manner of books
was granted by
Henry VIII in 1534.
The University has printed
and published continuously
since 1584.*

CAMBRIDGE UNIVERSITY PRESS

Cambridge

New York Port Chester Melbourne Sydney

Published by the Press Syndicate of the University of Cambridge,
The Pitt Building, Trumpington Street, Cambridge CB2 1RP
40 West 20th Street, New York, NY 10011-4211, USA
10 Stamford Road, Oakleigh, Melbourne 3166, Australia

© Cambridge University Press 1991

First published 1991

Printed in Great Britain at the University Press, Cambridge

British Library cataloguing in publication data

Basak, Amitava

Analogue electronic circuits and systems.

1. Circuits integrated analogue, analysis and design

I. Title II. Series

621.3815

Library of Congress cataloguing in publication data

Basak, Amitava

Analogue electronic circuits and systems / Amitava Basak.

p. cm. — (Electronics texts for engineers and scientists)

Includes bibliographical references.

ISBN 0 521 36046 3 (hardback). — ISBN 0 521 36913 4 (paperback)

1. Analog electronic systems. I. Title. II. Series.

TK7870.B357 1991

621.381'5—dc20

90-21775

CIP

ISBN 0 521 36046 3 hardback

ISBN 0 521 36913 4 paperback

This book is an undergraduate textbook for students of electrical and electronic engineering. It is written at an intermediate level, with second year students particularly in mind, and discusses analogue circuits used in various fields, including the interfacing of microcomputer systems. Basic electronics has been omitted so that appropriate emphasis can be given to the design of the most popular and useful circuits. Indeed, the contents of Chapters 3, 5, 7 and 8 are not covered together by any other single textbook available on the market.

The author begins with a summary of the knowledge which is prerequisite to a full study of the book. The second chapter then discusses the operation of several basic circuits which are commonly used in most integrated circuit chips. Other sections included in this chapter will acquaint the reader with different types of power amplifier circuit. The three important areas on which the book places greatest emphasis are operational amplifier circuits and their applications, data acquisition circuitry, and computer aided analysis and design. Oscillators, phase-locked loops and different types of modulation are also discussed, and particularly helpful detail is given to the important topics of phase-locked loops and analogue filter circuits. Each chapter contains a significant number of worked examples, and several carefully chosen problems at various levels of difficulty are also included to help the reader gain a better understanding of the topics under discussion.

Each topic has thus been carefully selected, and the author concentrates on the practical details and applications of the material he covers. Both students and practising engineers alike will therefore find this book extremely useful and informative.

Electronics texts for engineers and scientists

Editors

P.L. Jones, *University of Manchester*

P. J. Spreadbury, *University of Cambridge*

Analogue electronic circuits and systems

Titles in this series

H. Ahmed and P.J. Spreadbury

Analogue and digital electronics for engineers

R.L. Dalglish

An introduction to control and measurement with microcomputers

W. Shepherd and L.N. Hulley

Power electronics and motor control

T.H. O'Dell

Electronic circuit design

K.C.A. Smith and R.E. Alley

Electrical circuits

A. Basak

Analogue electronic circuits and systems

To

Shyamali, Somita, Soumen and Swagata

Preface

This book is intended particularly as a text for undergraduate students of electrical and electronic engineering at the intermediate level of a degree course. Some material may, however, be appropriate to the final year. Each topic has been deliberately selected and emphasis has been given to operational amplifier circuits and their applications, data acquisition circuitry and computer aided analysis and design. Other useful topics which have been covered in some detail are analogue filter circuits and phase-locked loops. A list of the prerequisite knowledge for this text is given in the first chapter.

This book is most appropriate for students because (i) a specific subject area, analogue circuits for more advanced students has been highlighted and (ii) particular attention has been given to a descriptive treatment of practical details and applications, e.g. CAD rather than theoretical analysis, because these areas are often neglected and are essential for practising engineers. Therefore it is hoped that students and engineers alike will find this text useful and informative, but less analytical than many other books presently available, and thus be able to cover a wider range of topics within a given period of time. Several carefully chosen problems set at various levels of difficulty are included at the end of each section to help readers gain a better understanding of the topics under discussion.

I would like to thank my parents and my immediate colleagues both academic and industrial for their encouragement and advice in the preparation of the manuscript. Thanks are also due to the staff of Cambridge University Press for their support through the many stages of production. I am also grateful to my research assistant Paul Ling and student Fatih Anayi for checking the problems.

Contents

Preface	xiii
1 Introduction	1
2 Transistor circuit techniques and amplifiers	4
Objectives	4
2.1 Linear integrated circuits	4
2.1.1 Differential amplifiers	5
2.1.2 Constant current sources	11
2.1.3 Current mirrors	12
2.1.4 Darlington connection	17
2.1.5 Level shifting circuits	21
2.2 Cascaded amplifier stages	24
2.3 Tuned amplifiers	34
2.4 Power amplifiers	37
2.4.1 Class A amplifiers	38
2.4.2 Class B amplifiers	42
2.4.3 Class C amplifiers	48
2.4.4 Heat sinks	50
Summary	53
Problems	54
3 Operational amplifiers	59
Objectives	59
3.1 Imperfections in operational amplifiers	60
3.1.1 Slew rate and its effect on full power bandwidth	60
3.1.2 Input offset voltage	62
3.1.3 Bias and offset currents	65
3.1.4 Frequency response effects	70
3.2 Applications	80
3.2.1 Instrumentation amplifiers	80

x	Contents	
3.2.2	Comparators	85
3.2.3	Precision rectifiers	90
3.2.4	Logarithmic amplifiers	96
3.2.5	Analogue computation	101
3.2.6	Active filters	107
	First-order circuits	108
	Infinite gain multiple feedback (IGMF)	
	second-order circuits	112
	Switched-capacitor filters	123
3.2.7	Pulse and waveform generators	129
	Astable multivibrators	129
	Monostable or one shot multivibrators	133
	Triangular wave generators	135
3.2.8	Inverse function generators	137
	Divider	138
	Square root circuit	139
	Root mean square circuit	140
	Summary	141
	Problems	144
4	Oscillators	149
	Objectives	149
4.1	Criteria for oscillation	149
4.2	<i>LC</i> oscillator	151
4.3	<i>RC</i> phase-shift oscillator	157
4.4	Wien-bridge oscillator	160
4.5	Crystal oscillator	164
	Summary	167
	Problems	168
5	Phase-locked loops	169
	Objectives	169
5.1	Components of phase-locked loops	170
5.1.1	Phase detector	170
5.1.2	Low-pass filter	172
5.1.3	Voltage controlled oscillator	173
5.2	Principles of operation	174
5.2.1	Lock range	176
5.2.2	Acquisition range	177

5.2.3	'No-lock' conditions and remedies	180
5.3	Applications	183
	Summary	188
	Problems	189
6	Modulation in communication systems	192
	Objectives	192
6.1	Amplitude modulation	192
6.1.1	Modulation circuits	195
6.1.2	Demodulation circuits	197
6.1.3	Frequency division multiplexing	198
6.2	Frequency modulation	199
6.2.1	Modulation and demodulation circuits	201
6.3	Pulse modulation	202
6.4	Pulse coded modulation	205
6.5	Time division multiplexing	205
	Summary	206
	Problems	208
7	Data acquisition and distribution	210
	Objectives	210
7.1	Digital-to-analogue (D-to-A) converter	211
7.1.1	Weighted resistor D-to-A converter	212
7.1.2	$R-2R$ ladder D-to-A converter	215
7.2	Analogue-to-digital (A-to-D) converter	221
7.2.1	Parallel A-to-D converter	221
7.2.2	Servo A-to-D converter	223
7.2.3	Successive approximation A-to-D converter	225
7.2.4	Dual slope A-to-D converter	230
7.3	Errors in D-to-A and A-to-D converters	233
7.4	Multiplexers	237
7.5	Demultiplexers	241
7.6	Errors in multiplexers	242
7.7	Sample-and-hold circuits	251
7.8	System error analysis	257
	Summary	260
	Problems	262

8	Computer aided circuit design	266
	Objectives	266
8.1	Computer aided design models	266
8.1.1	Ebers–Moll models of bipolar transistors	267
	Development of the model	270
8.1.2	Models for field effect transistors	285
8.1.3	Macromodels	287
8.2	Analysis of circuits	296
8.2.1	D.C. analysis	296
8.2.2	A.C. small signal analysis	304
8.2.3	Transient analysis	305
8.3	Programs	306
	Summary	317
	Problems	318
	Appendices	
A	Typical h -parameter values for a transistor	319
B	HI 506 16-channel CMOS multiplexer	320
C	μ A 741 frequency-compensated operational amplifier	322
D	LM118/LM218/LM318 operational amplifiers	330
E	Polynomials for low-pass filters	335
F	Standard design table for low-pass filters	338
G	MF10-type switched-capacitor filters	339
	Glossary of symbols	345
	Answers to problems	349
	References	353
	Index	357

Introduction

In recent years there has been rapid progress in electronic circuit design and the main reason for this is the advance in digital techniques. This volume differs from the texts which are available on the market nowadays in two respects. Firstly it covers only analogue electronic circuits and systems; secondly basic electronics is omitted so that appropriate emphasis can be given to the design of the most popular and useful analogue electronic circuits. The following are prerequisites for studying this text:

- (a) P-N junction diodes: principles of operation both in the forward and reverse mode, characteristic equation, resistance and junction capacitance, Zener diodes.
- (b) Junction transistors: principle of operation, common-emitter (CE), common-collector (CC) and common-base (CB) configurations, static characteristics, definition of active, cut-off and saturation regions, the concept of load lines and the need for biasing, the transistor as an amplifier.
- (c) Amplifiers: voltage and current gains (A_v and A_i), input and output resistance (R_{in} and R_{out}), frequency response concept, the use of the h -parameter model of the transistor for circuit analysis, midband frequencies of the CE, CC and CB configurations and calculation of A_v , A_i , R_{in} and R_{out} for each case.
- (d) Field effect transistor: principle of operation, static characteristics, load lines, biasing circuits, use as an amplifier.
- (e) Positive and negative feedback and their advantages and disadvantages.
- (f) Operational amplifiers: ideal amplifier, analysis of inverting, noninverting, differential, buffer and summer amplifiers, use of operational amplifiers as integrators and differentiators.

A list of books which comprehensively cover the above topics is given at the end of this volume. Standard symbols are used throughout the text and a glossary is included as one of the appendices.

In the second chapter the operation of several basic circuits which are commonly used in most integrated circuit chips is described. A section is also devoted to the analysis of multistage amplifiers and the ways of choosing the right configuration of transistor amplifiers for a particular stage. These are included in the text in order to acquaint the reader with the operational amplifier integrated circuit which is the building block of most analogue circuits. The principle of operation of tuned amplifiers is briefly explained in the penultimate section. In the last section, different types of power amplifiers are studied. Various types of heat sinks and their use in power circuits are also discussed in this section.

Chapter 3 deals with operational amplifiers in great detail, but at a level higher than the introductory one which is [one of the] required knowledge, as mentioned earlier. The circuits are analysed and designed assuming operational amplifiers to be ideal, but in practice they are not so. In this book, therefore, the chapter starts with the imperfections in operational amplifiers, their effects on various operational amplifier circuits and the ways in which readers can minimise these effects. Several widely used linear and nonlinear circuits using operational amplifiers are discussed in the remaining sections of this chapter. One of the major applications of operational amplifiers is in active filters and therefore readers will find both resistor-capacitor and switched-capacitor type active filters, which are discussed in this chapter with design examples, rather useful. Principles of design of waveform and function generators and also analogue computation using operational amplifiers are comprehensively studied in the next section.

The next three chapters describe oscillators, phase-locked loops and different types of modulation respectively. Principles of design and operation of oscillators using transistors, operational amplifiers and quartz crystals have been presented. The most common uses of phase-locked loops in the field of communications and also in the field of control of motor speed are discussed to a limited extent, with a full treatment of the theory of phase-locked loops which will help readers to understand and design similar circuits.

In Chapter 7, under the heading of data acquisition and distribution systems, analogue-to-digital and digital-to-analogue conversion techniques are discussed in great detail. Sample-and-hold circuits, multiplexers and demultiplexers are also studied in great depth. Analysis of errors in individual circuits and also in complete systems are discussed. In the age of computerised measurement in research and in most aspects of control systems this chapter is a significant part of the text.

The last chapter deals with computer aided analysis and design of

electronic circuits. It describes various computer aided design models giving special attention to the Ebers–Moll model of a transistor. Models for bipolar junction and field effect transistors and also integrated circuits are described. Techniques of a.c. small signal, d.c. and transient analysis of circuits are discussed with the help of examples. Several commercially available computer programs are also discussed. Two widely used software packages are described with the aid of circuits and their analyses in order to make students familiar with the procedure for drawing and analysing various active and passive circuits.

To summarize it can be said that this volume on analogue electronic circuits and systems has been written mainly keeping in mind the requirements of undergraduate students at the intermediate level. Practising engineers interested in various aspects of analogue electronic circuit design will also find this text informative.

Transistor circuit techniques and amplifiers

Objectives

At the end of the study of this chapter a student should be:

1. familiar with the operation of the differential amplifier, its voltage gain, common-mode gain and common-mode rejection ratio
2. familiar with constant current sources and current mirror circuits
3. capable of explaining the principle of Darlington connections
4. able to design level shifting circuits
5. familiar with multistage amplifiers and able to calculate their input and output impedances and overall current and voltage gains
6. able to design class A, class B and tuned amplifiers
7. familiar with different types of heat sinks and able to choose the right heat sink for a particular circuit

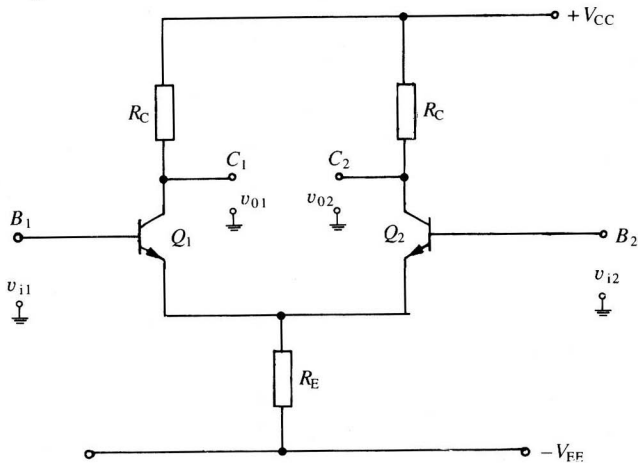
2.1 Linear integrated circuits

Complete multistage amplifiers and other linear devices can be constructed on a single chip of silicon occupying a very small volume by using modern techniques for the fabrication of integrated circuits. In the case of monolithic integrated circuits, all components may be manufactured on the chip by a diffusion process. A diffusion isolating technique is used to separate the various components from each other electrically. The design techniques used for the construction of these integrated circuits are basically the same as those used to build circuits employing discrete components, although, in many cases some modification in techniques is needed.

The operational amplifier is the most common type of integrated circuit (small scale integration) which is widely used with different forms of external circuitry to build summers, subtractors, integrators, filters, etc. Audio amplifiers, timers, modulators and frequency dividers are only a few among many other types of integrated circuits.

In this section, the operation of several basic circuits which are commonly used in integrated circuits are described.

Fig. 2.1. Basic differential amplifier.



2.1.1 Differential amplifiers

One of the basic circuits used in operational amplifiers is the differential amplifier. This forms the basis of practically all operational amplifiers. Also known as the long tailed pair, one of its advantages is that its gain tends to be very stable if any variations in supply voltage or ambient temperature occurs.

The basic form of the differential amplifier is shown in Fig. 2.1. It consists of two identical bipolar transistors coupled at their emitters. The collector resistors, connected between the positive voltage supply rail and the collectors of the transistors, are of the same value, R_C . Two input signals are applied to the bases of the two transistors Q_1 and Q_2 with respect to ground and the output is usually obtained from either of the two collectors, again with respect to ground.

Differential voltage gain

The differential voltage gain, A_d is the ratio of the output voltage to the difference between the input voltages applied to terminals B_1 and B_2 . If the output voltage is obtained from either of the two collectors with respect to ground, then the ratio gives the single-ended differential voltage gain $A_{d(s)}$. If the output voltage is the difference between the two collector voltages then the ratio yields the double-ended differential voltage gain $A_{d(d)}$.

In order to find the single-ended differential voltage gain $A_{d(s)}$ of the circuit let us apply a very small voltage v_d between the two bases B_1 and B_2 . Since the circuit is fundamentally symmetrical and the total emitter current