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COMPUTER AIDED DESIGN OF DIGITAL SYSTEMS A BIBLIOGRAPHY

W. M. VANCLEEMPOT



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W. M. VAN CLEEMPOT

DIGITAL SYSTEMS LABORATORY

STANFORD UNIVERSITY



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Computer Science Press, Inc.
4566 Poe Avenue
Woodland Hills, California 91364 USA

Library of Congress Cataloging in Publication

vanCleemput, William M.

Computer aided design of digital systems - a bibliography

(Digital system design series)

Includes indexes

1. Digital electronics -- Data processing -- Bibliography. 2. Electronics circuits design -- Data processing -- Bibliography. 3. Electronics industries -- Automation -- Bibliography.

I. Title II. Series

(TK7878.D5) 016.6213815'02854 75-40004

ISBN 0-914894-55-2

ISBN 0-914894-58-7 (pbk)

PREFACE

This bibliography contains publications in the field of computer-aided design of digital systems up to December 1974.

The material for this bibliography has been compiled by the author since 1968. Since a bibliography has to be up-to-date in order to be useful, updates will be published yearly by Computer Science Press Inc.

An attempt has been made to cover all major publications (books, papers in journals and published conference proceedings, research reports) in the main areas of computer-aided design of digital systems (automated logic design, fault diagnosis and test generation, simulation and circuit layout).

A number of references on the mathematical and computer science aspects of computer-aided design have been included in section J. Furthermore, some references on computer-aided circuit analysis and computer-aided manufacturing of digital systems have been selected on the basis of their usefulness to designers of digital systems. In these fields the bibliography is not intended to be complete but it contains a number of citations that are representative of the area.

This work consists of four major sections. The first section contains the citations, classified under 10 major subject headings:

- A. General References on Design Automation
- B. Automated Logic Design
- C. Logic and Fault Simulation
- D. Fault Diagnosis and Fault Test Generation
- E. Layout of Printed and Integrated Circuits
- F. Computer-aided Circuit Analysis
- G. Automated Documentation
- H. Computer-aided Manufacturing of Digital Systems
- I. Software Design Automation

J. Mathematical and Computer Science Aids for Digital
Design Automation

Within every subheading, citations are classified alphabetically by author, and chronologically for every author. Every citation is numbered and both the author and subject indices refer to the citations in this section,

The second section is the author index. This section allows quick access to all publications in the bibliography by an individual author.

The third section is the subject and keyword index, organized alphabetically by subject.

The fourth section is a research report index, which allows access to citations by their source of origin. For the purpose of this index dissertations are treated as research reports.

The appendices contain a list of frequently used abbreviations and a list of journals and conference proceedings that were covered systematically.

The author gratefully acknowledges the competent technical assistance of Ms. Magda Geersens, without which the publication of this bibliography would not have been possible.

W.M. vanCleemput

December, 1975.

How to Use this Bibliography

The citations in this bibliography are classified under 10 major headings.

- A. General References on Design Automation
- B. Automated Logic Design
- C. Logic and Fault Simulation
- D. Fault Diagnosis and Fault Test Generation
- E. Layout of Printed and Integrated Circuits
- F. Computer-aided Circuit Analysis
- G. Automated Documentation
- H. Computer-aided Manufacturing of Digital Systems
- I. Software Design Automation
- J. Mathematical and Computer Science Aids for Digital Design Automation.

Each of these subject headings is in turn broken up into several subheadings.

An important publication may be listed more than once if it clearly belongs to more than one subject area. For example the following citation will be listed under A1: General References on Design Automation - Books.

1. Breuer, M.A. (ed.) "Design Automation of Digital Systems: Theory and Techniques. Volume 1: Hardware.", Englewood Cliffs, N.J.: Prentice Hall, 1972, 495 pp.

Because of the importance of this work every chapter of this book will be listed separately under the appropriate heading. For example chapter 3 of this book will be listed under C1: Logic and Fault Simulation - Books, Surveys, Bibliographies.

3. Scheff, B.H. and Young, S.P. "Gate level logic simulation.", chapter 3 of M.A. Breuer, Design Automation of Digital Systems, Prentice Hall, 1972.

Multiple listings of the same citation have been avoided as much as possible and only important references belonging clearly

under more than one heading have been listed more than once.

The subject and keyword index provides easy access to all citations that belong to more than one subject area.

For many research reports, the NTIS (National Technical Information Service) accession number is given if the report is available through NTIS. Most doctoral dissertations are available from Xerox University Microfilms and when known the order number for these dissertations is given.

If the user is interested in a particular subject area such as Printed Circuit Layout, he may look up the subject heading that seems most appropriate.

However, some of the subheadings such as B2, Logic Design Algorithms and D2, Fault Diagnosis Techniques are likely to contain too many citations for efficiently locating the relevant references. The subject and keyword index in Section 3 is the key to the full resources of this bibliography. For every citation, this index contains entries to significant keyword and subject classifications.

For example, the following citation will be listed under subheading D3, Fault Diagnosis Programs and Systems.

2. Agrawal, V.D. and Agrawal, P. "An automatic test generation system for Illiac IV logic boards.", IEEE Trans. Computers, vol. C-21, no. 9, pp. 1015-1017, Sept. 1972.

In the subject and keyword index, this citation will be referred to under:

ILLIAC IV - Fault Test Generation D3.002
Fault Test Generation - ILLIAC IV D3.002

The author index allows the user quick access to all the publications by the same author, whether or not he is the primary author.

The research report index lists research reports and theses by the source of origin. This may be helpful in locating a particular item if only the affiliation of the author(s) is known.

Appendix 1 contains a list of the most frequently used abbreviations. A list of major journals that were covered systematically is given in Appendix 2. A similar list is provided for conference proceedings in Appendix 3.

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SECTION 1

CITATIONS

A. GENERAL REFERENCES ON DESIGN AUTOMATION.

A1. Books.

1. Breuer, M.A. (ed.) "Design Automation of Digital Systems: Theory and Techniques. Volume 1: Hardware.", Englewood Cliffs, N.J.: Prentice Hall, 1972, 495 pp.
2. Herskowitz, G.D. (ed.) "Computer Aided Integrated Circuit Design.", New York: McGraw Hill, 1968.
3. Kuo, F.F. and Magnuson, W. "Computer Oriented Circuit Design.", Englewood Cliffs, N.J.: Prentice Hall, 1969, 389 pp.
4. Kuo, F.F. and Kaiser, J.F. "System Analysis by Digital Computer.", New York: John Wiley and Sons, 1966, 438 pp.
5. Prince, M.D. "Interactive Graphics for CAD.", Reading, Mass: Addison Wesley, 1971, 301 pp.
6. Roth, J.P. "Computer design I.", IBM Research Rept. RA 45, Dec. 1972.
7. Roth, J.P. "Computer design Architecture II.", IBM Research Rept. RA 48.
8. Roth, J.P. "Algorithmic Design III. Embedding; Diagnosis, hard and soft.", IBM Research Rept. RA 50, July 1973.
9. Sabin, M.A. (ed.) "Programming techniques in Computer Aided Design.", Int. Pubus Service, 1974.
10. Vlietstra, J. and Wielinga, R.F. (eds.) "Computer Aided Design.", Amsterdam: North Holland, 1973.
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A2. Surveys.

1. Adams, J.A. and Kuo, F.F. "Computer aided design in Europe.", Office of Naval Research, London, England, Rept. ONRL-R-11-72, July 1972, 22 pp.; (NTIS AD 747 616).
2. Breuer, M.A. "General survey of design automation of digital computers.", Proc. IEEE, vol. 54, no. 12, Dec. 1966.
3. Breuer, M.A. "Recent developments in the automated design and analysis of digital systems.", Proc. IEEE, vol. 60, no. 1, pp. 12-27, Jan. 1972.
4. Breuer, M.A. "Recent developments in design automation.", Computer, vol. 5, no. 3, pp. 23-25, May/June 1972.
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9. Siders, R.A. "Computer aided design.", IEEE Spectrum, vol. 4, no. 11, pp. 84-92, Nov. 1967.

A3. Bibliographies.

1. Anderson, R.H. "A selective bibliography of computer graphics.", Rand Corp., Santa Monica, Rept. no. P-4629, April 1971, 35 pp.; (NTIS AD 738 058).
2. Berschback, T.P. "Annotated microprogramming bibliography.", Mitre Corp., Bedford, Rept. M69-65/ESD-TR-70-204, July 1970, 73 pp.; (NTIS AD 709 765).
3. Carroll, B.D. and Smith, E.W. "A bibliography of fault tolerant computing.", Auburn Univ., Rept. AU-T-22, Feb. 1972, 46 pp.; (NTIS AD 739 522).
4. DEO, N. "An extensive english language bibliography on graph theory and its applications.", California Institute of Technology, Technical Rept. 32-1413, Oct. 1969, 80 pp.
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14. Owens, A.B. "Graph and printed circuit imbeddings; a survey and bibliography.", Naval Research Labs, Washington, Rept. NRL-Bibliogr. 33, May 1969, 29 pp.; (NTIS AD 704 843).
15. Pollack, B.W. "A bibliography of computer graphics.", Stanford Univ., Computer Science Dept., Rept. STAN-CS-72-306.1, Aug. 1972.; also: IEEE Computer Group Repository, R73-108, 1973, 141 pp.; (NTIS SU-326-P-23-X-2).
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19. Rault, J.C. "Bibliographie sur la conception assistee par ordinateur des circuits electriques.", Thomson-CSF, Note technique DIB-CAS, Sept. 1973,
20. Rault, J.C. "Bibliographie sur les outils d'aide a la programmation.", Thomson-CSF, Note Technique CCTI-SAS.
21. Rault, J.C. "Bibliographie sur la microprogrammation.", Thomson-CSF, Note Technique CCTI-SAS.
22. Reed, W.E. "Printed circuits. Volume 1. 1964-1971 (A bibliography with abstracts).", Nat. Tech. Information Service, Springfield, Rept., Feb. 1975, 114 pp.; (NTIS PS-75/233).
23. Reed, W.E. "Printed circuits. Volume 2. 1972-1974 (A bibliography with abstracts).", Nat. Tech. Information Service, Springfield, Rept., Feb. 1975, 72 pp.; (NTIS PS-75/234).
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