

# **DIGITAL TECHNIQUES**

**BARRY G. WOOLLARD**

# Digital techniques

---

**Barry G. Woollard** M Phil, C Eng, MIERE, M Inst MC  
*Technical Director, Beal-Davis Electronics Limited*

**McGraw-Hill Book Company (UK) Limited**

---

London • New York • St Louis • San Francisco • Auckland • Bogotá  
Guatemala • Hamburg • Johannesburg • Lisbon • Madrid • Mexico • Montreal  
New Delhi • Panama • San Juan • São Paulo • Singapore • Sydney • Tokyo  
Toronto

*Published by*

**McGraw-Hill Book Company (UK) Limited**

MAIDENHEAD • BERKSHIRE • ENGLAND

---

British Library Cataloguing in Publication Data

Woollard, Barry G.

Digital techniques.

1. Digital electronics

I. Title

621.3815 TK7868.D5

ISBN 0-07-084669-3

Library of Congress Cataloging in Publication Data

Woollard, Barry G.

Digital techniques.

Includes index.

1. Digital electronics. I. Title.

TK7868.D5W63 1983 621.3819'58 82-20909

ISBN 0-07-084669-3

Copyright © 1983 McGraw-Hill Book Company (UK) Limited. All rights reserved. No part of this publication may be reproduced, stored in a retrieval system, or transmitted, in any form or by any means, electronic, mechanical, photocopying, recording, or otherwise, without the prior permission of McGraw-Hill Book Company (UK) Limited, or of the original copyright holder.

12345 JWA 86543

Printed in Great Britain at the Alden Press, Oxford

# Preface

---

Developments in the technology of integrated circuits during the last decade have caused major changes to be made in the field of digital electronics and micro-computing. These advances have led to the widespread use of electronic calculators, digital watches, and electronic games in the social and domestic field, and to digital multimeters (DMMs), digital frequency counters, digital panel meters (DPMs), direct digital control of industrial processes, programmable logic control (PLC), and the microcomputer—enabling industrial control applications to be solved at costs which can no longer be ignored.

This text is written to meet all of the objectives of the TEC Units: Digital Techniques 2, Digital Techniques 3A, and Digital Techniques 3B. In addition to the TEC requirements, the text is useful for a wide range of technicians and engineers who are interested in gaining some expertise in the use of digital electronics.

In the field of Digital Techniques, and in Microcomputing, it is *essential* that the normal learning process is complemented by 'hands-on' practical development. This text is prepared so that the diagrams can be interpreted as 'practical circuits'; a Digital Trainer is manufactured by Beal-Davis Electronics Ltd., 18A Friar Street, Worcester. This equipment is designed to provide all the necessary power supplies, binary signals, pulse generators, and monitoring capability required to satisfy all of the objectives in this text.

Many examples are included in the text, each chapter concluding with exercises. Answers to numerical exercises are included in the Appendices.

I sincerely acknowledge my gratitude to all those who have contributed to the realization of this book, in particular to my wife, for her patience and efforts during the preparation and typing of the manuscript.

Barry G. Woollard  
London, Staffs.

# Contents

---

<b>PREFACE</b>	<b>vii</b>
<b>1. BINARY ARITHMETIC</b>	<b>1</b>
Numbering systems, conversions between numbering systems, signed numbers, floating point, modulus, binary arithmetic, Exercises	
<b>2. BOOLEAN ALGEBRA AND LOGIC ELEMENTS</b>	<b>23</b>
Introduction, basic rules of boolean algebra, basic logic gates and truth tables, the laws of logic, boolean manipulations, truth tables, and logic networks, Exercises	
<b>3. BISTABLE ELEMENTS</b>	<b>34</b>
Introduction, the <i>S-R</i> bistable element, the gated <i>S-R</i> bistable element, the <i>D</i> -type bistable element, the 'master-slave' principle, the <i>J-K</i> bistable element, Exercises	
<b>4. COUNTERS</b>	<b>45</b>
Introduction, propagation delay and hazards, asynchronous (ripple-through) counters, ripple-through delay, synchronous counters, divide-by-sixteen counter, modification of counting period, scale-of-ten counter—decade counter, curtailing count to any number, 8421 BCD counter, TTL decade counter—SN 7490, squaring-up asymmetric divided waveforms, decoder/driver and denary display, CMOS ripple-through counters, CMOS synchronous counters, Exercises	
<b>5. REGISTERS</b>	<b>69</b>
Introduction, storage registers, shift registers, data control in shift registers, reversible shift register, TTL shift registers, the ring counter, CMOS registers, Exercises	
<b>6. LOGIC CIRCUIT FAMILIES</b>	<b>83</b>
Introduction, choice of logic family, speed of operation—propagation delay, noise immunity, fan-in and fan-out, power dissipation, operating temperature range, logic families, transistor-transistor logic (TTL), sinking and sourcing, TTL recognition—device numbering, specification—TTL data sheet, open-collector outputs—TTL, tri-state outputs—TTL, emitter-coupled logic (ECL), CMOS logic, CMOS, NOR, and NAND gates, Exercises	
<b>7. UNIVERSAL NAND AND NOR LOGIC</b>	<b>99</b>
NAND and NOR gates, connection of 'unused' inputs, the logical NOT function, the logical AND function, the logical OR function, logic networks and minimization, minimization using boolean algebra, EXCLUSIVE-OR logic function, logic comparator, simple machine safety interlock system, Exercises	
<b>8. KARNAUGH MAPS</b>	<b>116</b>
Introduction, function mapping, cell looping, Exercises	

---

<b>9. DIGITAL STORAGE MEDIA</b>	<b>130</b>
Introduction, semiconductor memory elements, semiconductor static RAMs, using the 2102A static RAM, semiconductor dynamic RAMs, using the 2107B dynamic RAM, semiconductor ROMs and PROMs, paper-tape storage, magnetic-tape storage on cassette, floppy-disc storage, ferrite core store, reading and writing in store, Exercises	
<b>10. CIRCUIT TECHNIQUES AND FAULT DIAGNOSIS</b>	<b>153</b>
Introduction, breadboarding, stripboard, printed circuit board (p.c.b.), mounting and connections, fault diagnosis, digital test equipment, typical faults, techniques of fault finding, short-circuit faults, partial device shorts and stuck nodes, Summary	
<b>APPENDICES</b>	<b>169</b>
A. Comparison of standard logic symbols	
B. Solutions to numerical exercises	
C. Pin-outs of a selected range of 74 series TTL ICs	
D. Pin-outs of a selected range of 4000 series CMOS ICs.	

# 1 Binary arithmetic

---

## 1.1 Numbering systems

Many different numbering systems are in common use. These have evolved out of the need to simplify the human operator's task in reading and writing data (information) and coded instructions to digital electronic control equipment and to computer systems. A *digital code* is a system of symbols that represent data values and make up a special 'language' that permits information to be communicated, stored, and manipulated by digital circuits and computers.

The number of characters (digits, symbols) used in a system is known as its *base*, or *radix*. Other numbers are 'constructed' by giving different 'weights' to the position of the digit relative to the 'decimal' point. The 'weights' of the different positions are given by powers of the radix, which, in general is:

$$R^4 \quad R^3 \quad R^2 \quad R^1 \quad R^0 \quad \cdot \quad R^{-1} \quad R^{-2} \quad R^{-3} \quad R^{-4}$$

- (a) *Denary numbering system.* Although the denary system is very widely understood, it is helpful to examine it here to appreciate the 'construction' technique used, so that numbering systems which are unfamiliar can be better understood.

The radix is 10, i.e., *ten symbols* are used to represent the quantities 0 (zero) through 9 (nine). Positional weights are powers of 10:

$$10^4 \quad 10^3 \quad 10^2 \quad 10^1 \quad 10^0 \quad \cdot \quad 10^{-1} \quad 10^{-2}$$

### EXAMPLE 1.1

$$\begin{aligned} 2345.63_{10} &= 2 \times 10^3 + 3 \times 10^2 + 4 \times 10^1 + 5 \times 10^0 + 6 \times 10^{-1} + 3 \times 10^{-2} \\ &= 2 \times 1000 + 3 \times 100 + 4 \times 10 + 5 \times 1 + 6 \times 0.1 + 3 \times 0.01 \\ &= 2000 + 300 + 40 + 5 + 0.6 + 0.03 \\ &= 2345.63_{10} \end{aligned}$$

- (b) *Binary numbering system.* This system is very widely used in logic and computing, and is the basis of machine code language used with computers.

The radix is 2, i.e., *two symbols* are used to represent the quantities 0 and 1. Positional weights are powers of 2:

$$2^7 \quad 2^6 \quad 2^5 \quad 2^4 \quad 2^3 \quad 2^2 \quad 2^1 \quad 2^0 \quad \cdot \quad 2^{-1} \quad 2^{-2} \quad 2^{-3} \quad 2^{-4}$$

**EXAMPLE 1.2**

$$\begin{aligned}
 11101011.101_2 &= 1 \times 2^7 + 1 \times 2^6 + 1 \times 2^5 + 1 \times 2^3 + 1 \times 2^1 + 1 \times 2^0 \\
 &\quad + 1 \times 2^{-1} + 1 \times 2^{-3} \\
 &= 128 + 64 + 32 + 8 + 2 + 1 + 0.5 + 0.125 \\
 &= 235.625_{10}
 \end{aligned}$$

- (c) *Octal numbering system.* This system has been widely used in computer systems, due to the ease with which it can be converted to binary and vice versa. Octal was used in preference to binary, since a given number can be represented by fewer digits than are required in binary.

The radix is 8, i.e., *eight symbols* are used to represent the quantities 0 through 7. Positional weights are powers of 8:

$$8^4 \quad 8^3 \quad 8^2 \quad 8^1 \quad 8^0 \quad \cdot \quad 8^{-1} \quad 8^{-2} \quad 8^{-3}$$

**EXAMPLE 1.3**

$$\begin{aligned}
 2345.63_8 &= 2 \times 8^3 + 3 \times 8^2 + 4 \times 8^1 + 5 \times 8^0 + 6 \times 8^{-1} + 3 \times 8^{-2} \\
 &= 2 \times 512 + 3 \times 64 + 4 \times 8 + 5 \times 1 + 6 \times 0.125 + 3 \times 0.015625 \\
 &= 1024 + 192 + 32 + 5 + 0.75 + 0.046875 \\
 &= 1253.796875_{10}
 \end{aligned}$$

- (d) *Hexadecimal (hex) numbering system.* This system has become much more widely used than octal, due to the advances made in the development of microcomputer systems. *Hex* has replaced the use of octal in most microcomputers, due to the reduced number of digits necessary to represent any particular binary number.

The radix is 16, i.e., *sixteen symbols* are used to represent the quantities 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, A, B, C, D, E, F:

<i>Decimal</i>	<i>Hexadecimal</i>
0	0
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	A



## BINARY ARITHMETIC

11	B
12	C
13	D
14	E
15	F

In general:

$$16^4 \quad 16^3 \quad 16^2 \quad 16^1 \quad 16^0 \quad \cdot \quad 16^{-1} \quad 16^{-2} \quad 16^{-3}$$

### EXAMPLE 1.4

$$\begin{aligned}
 \text{B6EF} \cdot 4\text{AH} &= \text{B} \times 16^3 + 6 \times 16^2 + \text{E} \times 16^1 + \text{F} \times 16^0 + 4 \times 16^{-1} + \text{A} \times 16^{-2} \\
 &= 11 \times 4096 + 6 \times 256 + 4 \times 16 + 15 \times 1 + 4 \times 0.0625 \\
 &\quad + 10 \times 0.00390625 \\
 \text{Hex} \swarrow & \\
 \text{radix } 16_{10} &= 45056 + 1536 + 224 + 15 + 0.25 + 0.0390625 \\
 &= 46831.2890625_{10}
 \end{aligned}$$

- (e) *Binary coded decimal (BCD)*. The binary number system is the simplest and best system for logic circuits and digital computers. However, the denary number system is the most familiar world-wide. Hence, for digital logic circuits and computers to be able to work in the binary system we must have a simple method of converting binary to denary and vice versa. The conventional method using powers of 2 is awkward, and although computers can be instructed to perform the conversions, human operators find it time-consuming to convert long strings of binary digits into a denary number.

The octal and hexadecimal systems are shorthand methods of writing binaries—but they are not a great deal of help in converting them to denary.

To overcome these problems, several binary codes have been devised to *translate each* denary digit into an *equivalent 4-bit binary code* and vice versa, some of which are shown in Fig. 1.1.

After a count of  $9_{10}$ , the BCD *weights* change by a factor equivalent to denary 10.

Denary	Excess-3 (XS-3)	8421 BCD	2421 BCD	7421 BCD
0	0011	0000	0000	0000
1	0100	0001	0001	0001
2	0101	0010	0010	0010
3	0110	0011	0011	0011
4	0111	0100	0100	0100
5	1000	0101	0101	0101
6	1001	0110	0110	0110
7	1010	0111	0111	1000
8	1011	1000	1110	1001
9	1100	1001	1111	1010

Fig. 1.1 Binary coded decimal (BCD) systems.

**EXAMPLE 1.5**

The denary number  $79_{10}$  may be represented in 8421 BCD as follows:

	0111	1001						
that is,	80	40	20	10	8	4	2	1
	0	1	1	1	1	0	0	1

**1.2 Conversions between numbering systems**

Digital electronic circuits, microprocessor-based systems, and computers work in binary numbers and binary digits (*bits*). Information is often fed into the above systems in several different forms—including binary, octal, hexadecimal, BCD, alphanumeric, etc.—and it is therefore necessary to be able to convert from one numbering system to another.

Many rules have been established for these conversions, but the following simple procedures can easily be mastered with a little practice:

- (a) *Denary-to-binary conversion.* The binary equivalent of a denary number may be found simply by subtracting the value of the highest power of 2—the *most significant digit*, or *bit (MSB)*—from the denary number and writing a 1 in that position. The procedure is repeated on the remainder, progressively, until all binary digits have been formed.

**EXAMPLE 1.6**

Convert  $157_{10}$  to binary.

157	–		
<u>128</u>	–	= highest power of 2 ∴ MSB	= 1
29	–	no 64, next significant bit	= 0
		no 32, next significant bit	= 0
<u>16</u>	–	16, next significant bit	= 1
13	–		
<u>8</u>		8, next significant bit	= 1
5	–		
<u>4</u>		4, next significant bit	= 1
1		2, next significant bit	= 0
<u>1</u>		1, least significant bit	= 1
0			

The binary equivalent of  $157_{10}$  is therefore  $10011101_2$ .

When large denary numbers are to be converted, the above method can lead to errors, and an alternative is continuously to divide by 2 while noting the remainder.

## EXAMPLE 1.7

Convert  $157_{10}$  to binary.

$$\begin{array}{rll}
 2 \overline{)157} & & \\
 2 \overline{)78} & \text{remainder 1 (LSB)} & \\
 2 \overline{)39} & \text{remainder 0} & \\
 2 \overline{)19} & \text{remainder 1} & \\
 2 \overline{)9} & \text{remainder 1} & \\
 2 \overline{)4} & \text{remainder 1} & \\
 2 \overline{)2} & \text{remainder 0} & \\
 2 \overline{)1} & \text{remainder 0} & \\
 0 & \text{remainder 1 (MSB)} &
 \end{array}$$

The binary equivalent of  $157_{10}$  is therefore  $10011101_2$ .

The above examples show how whole numbers (*integers*) may be converted. When the number to be converted is a *compound* number, i.e., it has a fractional part as well as an integer part, the conversion must be performed in two stages. The integer part is converted separately, as above. Then the fractional part is converted by continuously multiplying the fractional part by 2, the radix of the binary system; the integer part of each multiplication gives the binary digits of the solution.

## EXAMPLE 1.8

Converting  $25.875$  to binary.

Dealing with integer part ( $25_{10}$ ) first:

$$\begin{array}{rll}
 2 \overline{)25} & & \\
 2 \overline{)12} & \text{remainder 1 (LSB)} & \\
 2 \overline{)6} & \text{remainder 0} & \\
 2 \overline{)3} & \text{remainder 0} & \\
 2 \overline{)1} & \text{remainder 1} & \\
 0 & \text{remainder 1 (MSB)} &
 \end{array}$$

$$\therefore 25_{10} = 11001_2$$

Secondly, deal with the fractional part ( $0.875_{10}$ )

$$\begin{array}{ccc}
 \begin{array}{r} 0.875 \times \\ 2 \\ \hline 1.750 \\ \downarrow \\ 1 \text{ (MSB)} \end{array} & \xrightarrow{\quad} & \begin{array}{r} 0.75 \times \\ 2 \\ \hline 1.50 \\ \downarrow \\ 1 \end{array} & \xrightarrow{\quad} & \begin{array}{r} 0.5 \times \\ 2 \\ \hline 1.0 \\ \downarrow \\ 1 \text{ (LSB)} \end{array}
 \end{array}$$

$$\therefore 0.875_{10} = 0.111_2$$

Thus,  $25.875_{10} = 11001.111_2$ .

**EXAMPLE 1.9**

Convert  $158.85_{10}$  to binary.

Considering the integer part ( $158_{10}$ ) first:

$$\begin{array}{r}
 2 \overline{)158} \\
 2 \overline{)79} \quad \text{remainder 0} = \text{LSB} \\
 2 \overline{)39} \quad \text{remainder 1} \\
 2 \overline{)19} \quad \text{remainder 1} \\
 2 \overline{)9} \quad \text{remainder 1} \\
 2 \overline{)4} \quad \text{remainder 1} \\
 2 \overline{)2} \quad \text{remainder 0} \\
 2 \overline{)1} \quad \text{remainder 0} \\
 \quad \quad 0 \quad \text{remainder 1} = \text{MSB}
 \end{array}$$

$\therefore$  the binary equivalent of  $158_{10} = 10011110_2$ .

Secondly, dealing with the fractional part:

$$\begin{array}{cccccc}
 0.85 \times & & 0.7 \times & & 0.4 \times & & 0.8 \times & & 0.6 \times \\
 \hline 2 & & \hline 2 & & \hline 2 & & \hline 2 & & \hline 2 \\
 1.70 & \swarrow & 1.4 & \swarrow & 0.8 & \swarrow & 1.6 & \swarrow & 1.2 \\
 \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow \\
 1 \text{ (MSB)} & & 1 & & 0 & & 1 & & 1 \text{ (LSB)}
 \end{array}$$

$\therefore$  the binary equivalent of  $0.85_{10}$  is  $0.11011_2$  to five binary places.

Thus, the binary equivalent of  $158.85_{10}$  is  $10011110.11011_2$ , which is accurate to *five* binary places.

An error is obviously produced in this conversion. This is, of course, always possible and in this case a *true conversion cannot be achieved*. Greater accuracy can be obtained by extending the number of binary places.

- (b) **Binary-to-denary conversion.** The denary equivalent of a binary number is most *easily* determined by adding the equivalent values (or weights) of those digits where a 1 exists:

$$\begin{array}{cccccccccccc}
 2^7 & 2^6 & 2^5 & 2^4 & 2^3 & 2^2 & 2^1 & 2^0 & \cdot & 2^{-1} & 2^{-2} & 2^{-3} & 2^{-4} & 2^{-5} \\
 128 & 64 & 32 & 16 & 8 & 4 & 2 & 1 & & 0.5 & 0.25 & 0.125 & 0.0625 & 0.03125
 \end{array}$$

**EXAMPLE 1.10**

Convert  $10011101_2$  to denary.

$$\begin{aligned}
 10011101_2 &= 128 + 16 + 8 + 4 + 1 \\
 &= 157_{10}
 \end{aligned}$$

Check with Example 1.7.

**EXAMPLE 1.11**

Convert  $11001.111_2$  to denary.

$$\begin{aligned} 11001.111_2 &= 16 + 8 + 1 + 0.5 + 0.25 + 0.125 \\ &= 25.875_{10} \end{aligned}$$

Check with Example 1.8.

**EXAMPLE 1.12**

Convert  $10011110.11011_2$  to denary

$$\begin{aligned} 10011110.11011_2 &= 128 + 16 + 8 + 4 + 2 + 0.5 + 0.25 + 0.0625 + 0.03125 \\ &= 158.84375_{10} \end{aligned}$$

*Note:* If this is checked with Example 1.9 it is possible to determine the error in the denary-to-binary conversion.

- (c) *Denary-to-octal conversion.* Similar techniques to denary-to-binary conversion can be applied here. For this conversion, however, we must continuously divide by 8 and note the remainder.

**EXAMPLE 1.13**

Convert  $157_{10}$  to octal.

$$\begin{array}{r} 8 \overline{)157} \\ 8 \overline{)19} \quad \text{remainder } 5 = \text{LSD} \\ 8 \overline{)2} \quad \text{remainder } 3 \\ 0 \quad \text{remainder } 2 = \text{MSD} \end{array}$$

$\therefore$  the octal equivalent of  $157_{10}$  is  $235_8$ .

**EXAMPLE 1.14**

Convert  $25.875_{10}$  to octal.

Converting the whole number (integer) part first:

$$\begin{array}{r} 8 \overline{)25} \\ 8 \overline{)3} \quad \text{remainder } 1 \text{ LSD} \\ 0 \quad \text{remainder } 3 \text{ MSD} \end{array}$$

$\therefore$  the octal equivalent of  $25_{10}$  is  $31_8$ .

Converting the fractional part secondly:

$$\begin{array}{r} 0.875 \times \\ 8 \\ \hline 7.000 \quad \text{no further remainder} \\ \downarrow \\ \text{MSD} \end{array}$$

$\therefore$  the octal equivalent of  $0.875_{10}$  is  $0.7_8$  and the octal equivalent of  $25.875_{10}$  is  $31.7_8$ .

### EXAMPLE 1.15

Convert  $158.85_{10}$  to octal.

Converting the integer part first:

$$\begin{array}{r} 8 \overline{)158} \\ 8 \overline{)19} \quad \text{remainder } 6 = \text{LSD} \\ 8 \overline{)2} \quad \text{remainder } 3 \\ \underline{\quad\quad} 0 \quad \text{remainder } 2 = \text{MSD} \end{array}$$

$\therefore$  the octal equivalent of  $158_{10}$  is  $236_8$ .

Considering the fraction part:

$$\begin{array}{cccccc} 0.85 \times & \rightarrow & 0.8 \times & \rightarrow & 0.4 \times & \rightarrow & 0.2 \times & \rightarrow & 0.6 \times & \rightarrow & 0.8 \times \\ \frac{\quad}{8} & & \frac{\quad}{8} & & \frac{\quad}{8} & & \frac{\quad}{8} & & \frac{\quad}{8} & & \frac{\quad}{8} \\ \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow \\ 6.80 & & 6.4 & & 3.2 & & 1.6 & & 4.8 & & 6.4 \\ \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow & & \downarrow \\ 6 \text{ (MSD)} & & 6 & & 3 & & 1 & & 4 & & 6 \text{ (LSD)} \end{array}$$

Therefore, the octal equivalent of  $0.85_{10}$  is  $0.663146_8$  to six octal places. Note again that a true conversion cannot be achieved. Thus, the octal equivalent of  $158.85_{10}$  is  $236.663146_8$  to six octal places.

(d) *Octal-to-denary conversion.* The denary equivalent of an octal number is most easily determined by multiplying each digit by its value of power of 8, and then adding:

$$\begin{array}{ccccccccccc} 8^3 & 8^2 & 8^1 & 8^0 & \cdot & 8^{-1} & & 8^{-2} & & 8^{-3} & & 8^{-4} & & 8^{-5} \\ 512 & 64 & 8 & 1 & & 0.125 & & 0.015625 & & 0.0019531 & & 0.0002441 & & 0.00003051 \end{array}$$

### EXAMPLE 1.16

Convert  $31.7_8$  to denary.

$$\begin{aligned} 31.7_8 &= 3 \times 8^1 + 1 \times 8^0 + 7 \times 8^{-1} \\ &= 3 \times 8 + 1 \times 1 + 7 \times 0.125 \\ &= 24 + 1 + 0.875 \\ &= 25.875_{10} \end{aligned}$$

(e) *Octal-to-binary conversion.* Since  $8$  (the octal radix)  $= 2^3$ , each single octal digit can be represented by a 3-bit binary group. The conversion of an

octal number to a binary number is achieved by writing the 3-bit binary equivalent of each octal digit.

**EXAMPLE 1.17**

Convert  $235_8$  to binary.

$$\begin{array}{ccc} 2 & | & 3 & | & 5_8 \\ 010 & | & 011 & | & 101_2 \end{array}$$

Thus, the binary equivalent of  $235_8$  is  $10011101_2$ .

**EXAMPLE 1.18**

Convert  $31.7_8$  to binary.

$$\begin{array}{ccc} 3 & | & 1 & | & 7_8 \\ 011 & | & 001 & | & 111_2 \end{array}$$

Thus, the binary equivalent of  $31.7_8$  is  $11001.111_2$ .

(f) *Binary-to-octal conversion.* The binary number is divided into 3-bit groups, working from the binary point (either to the left or to the right).

**EXAMPLE 1.19**

Convert  $10011101_2$  to octal.

$$\begin{array}{ccc} 10 & | & 011 & | & 101_2 \\ 2 & | & 3 & | & 5_8 \end{array}$$

Thus, the octal equivalent of  $10011101_2$  is  $235_8$ .

**EXAMPLE 1.20**

Convert  $11001.111_2$  to octal.

$$\begin{array}{ccc} 11 & | & 001 & | & 111_2 \\ 3 & | & 1 & | & 7_8 \end{array}$$

Thus, the octal equivalent of  $11001.111_2$  is  $31.7_8$ .

(g) *Denary-to-hex conversion.* This is achieved by continuously dividing by 16 (the radix of the hexadecimal system) and noting the remainder.

**EXAMPLE 1.21**

Convert  $157_{10}$  to hexadecimal.

$$\begin{array}{r} 16 \overline{)157} \\ 16 \overline{)9} \quad \text{remainder } D = \text{LSD} \\ 0 \quad \text{remainder } 9 = \text{MSD} \end{array}$$

Thus, the hex equivalent of  $157_{10}$  is 9DH.

**EXAMPLE 1.22**

Convert  $25.875_{10}$  to hexadecimal.

Converting the integer part first:

$$\begin{array}{r} 16 \overline{)25} \\ 16 \overline{)1} \quad \text{remainder } 9 = \text{LSD} \\ 0 \quad \text{remainder } 1 = \text{MSD} \end{array}$$

The hex equivalent of  $25_{10}$  is 19H.

Converting the fractional part:

$$\begin{array}{r} 0.875 \times \\ \hline 16 \\ \hline 14.000 \quad \text{no further digits} \\ \downarrow \\ E \quad (\text{MSD}) \end{array}$$

The hex equivalent of  $0.875_{10}$  is 0.EH and the hex equivalent of  $25.875_{10}$  is 19.EH.

(h) *Hex-to-denary conversion.* The denary equivalent of a hex number is most easily determined by multiplying each digit by its value of power of 16 and then adding:

$$\begin{array}{r} 16^1 \quad 16^0 \cdot 16^{-1} \quad 16^{-2} \\ 16 \quad 1 \quad 0.0625 \quad 0.00390625 \end{array}$$

**EXAMPLE 1.23**

Convert 9DH to denary.

$$\begin{aligned} 9DH &= 9 \times 16^1 + D \times 16^0 \\ &= 9 \times 16 + 13 \times 1 \\ &= 144 + 13 \\ &= 157_{10} \end{aligned}$$



## EXAMPLE 1.24

Convert 19.EH to denary.

$$\begin{aligned} 19.EH &= 1 \times 16^2 + 9 \times 16^0 + E \times 16^{-1} \\ &= 1 \times 16 + 9 \times 1 + 14 \times 0.0625 \\ &= 25.875_{10} \end{aligned}$$

- (i) *Hex-to-binary conversion.* Since 16 (the hex radix) =  $2^4$ , each single hex digit can be represented by a 4-bit binary group. The conversion of a hex number to a binary number is achieved by writing the 4-bit binary equivalent of each hex digit.

## EXAMPLE 1.25

Convert 9DH to binary.

$$\begin{array}{ccc} 9 & | & D \quad H \\ 1001 & | & 1101_2 \end{array}$$

Thus, the binary equivalent of 9DH is 10011101<sub>2</sub>.

## EXAMPLE 1.26

Convert 19.EH to binary.

$$\begin{array}{ccc} 1 & | & 9 & | & E \quad H \\ 0001 & | & 1001 & | & 1110_2 \end{array}$$

Thus, the binary equivalent of 19.EH is 11001.111<sub>2</sub>.

- (j) *Binary-to-hex conversion.* The binary number is divided into 4-bit groups, working from the binary point (either to the left or to the right).

## EXAMPLE 1.27

Convert 10011101<sub>2</sub> to hex.

$$\begin{array}{ccc} 1001 & | & 1101_2 \\ 9 & | & D \quad H \end{array}$$

Thus, the hex equivalent of 10011101<sub>2</sub> is 9DH.

## EXAMPLE 1.28

Convert 11001.111<sub>2</sub> to hex.

$$\begin{array}{ccc} 0001 & | & 1001 & . & 1110_2 \\ 1 & | & 9 & . & E \quad H \end{array}$$

Thus, the hex equivalent of 11001.111<sub>2</sub> is 19.EH.