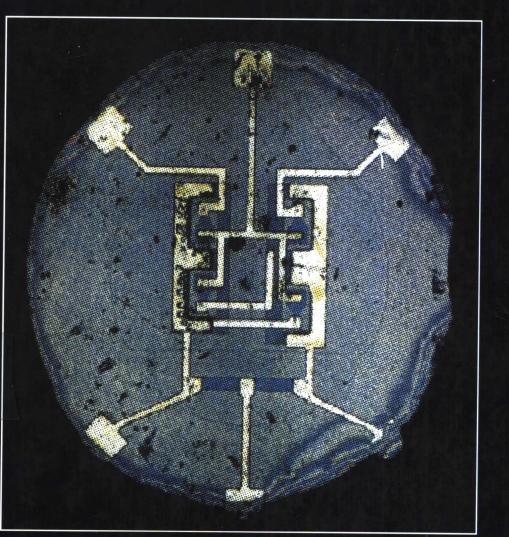
ULSI DEVICES

Edited by C. Y. Chang S. M. Sze



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C. Y. Chang National Chiao Tung University

S. M. Sze National Chiao Tung University and National Nano Device Laboratories



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Beginning in 1990, the semiconductor industry has entered the ultra-large-scale integration (ULSI) era with 10 million or more devices per integrated circuit (IC) chip. We expect that this trend toward higher integration levels will continue at least to year 2020. By then, the most advanced IC chips will contain over 1 trillion (10¹²) devices.

The most important ULSI device is, of course, the metal-oxide-semiconductor field-effect transistor (MOSFET), because of its advantages in device miniaturization, low power dissipation, and high yield compared to all other semiconductor devices. The MOSFET also serves as a basic component for many key device building blocks, including the complementary metal oxide semiconductor (CMOS), the dynamic random access memory (DRAM), and the static random access memory (SRAM). Another important ULSI device is the nonvolatile semiconductor memory (NVSM), which has the most desirable attributes for information storage compared to all other semiconductor memories. The third important ULSI device is the bipolar transistor. Although it is nearly completely replaced by the MOSFET in digital circuits, the bipolar transistor is often the preferred device for analog and mixed analog and digital circuits.

ULSI Devices gives a comprehensive and in-depth coverage of the physics and operational principles of the aforementioned devices and their building blocks. In addition, the book covers device simulation, reliability, digital and analog circuit building blocks, low-power and low-voltage operations, as well as system-on-chip for ULSI applications.

Each chapter starts with an introduction that provides a general discussion of a specific device, a device building block or a circuit building block. Subsequent sections present the physics and operational characteristics of these components, the evolution of device structures, and the ultimate limitation on device or circuit performances. The problem set at the ends of each chapter are an integral part of the development of the topic.

The book is intended as a textbook for senior undergraduate or first-year graduate students in applied physics and electrical and electronics engineering; it assumes that the reader has already acquired an introductory understanding of the physics of semiconductor devices. The book can also serve as a reference for those actively involved in IC design and process development.

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Hsinchu, Taiwan January 2000 C. Y. CHANG S. M. SZE

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Introduction

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1.1 SEMICONDUCTOR INDUSTRY

The electronic industry is the largest industry in the world with global sales over 1 trillion dollars. ¹⁻³ The foundation of the electronic industry is the semiconductor industry. Figure 1.1 shows the sales volumes of these two industries from 1980 to 1998 and projects the sales to year 2010. Also shown are the world gross national product (GNP), and the sales volumes of automobile and steel industries.²

We note that the electronic industry has surpassed the automobile industry in 1998. If the current trends continue, in year 2010 the sales volume of the electronic industry will reach \$2.7 trillion and will constitute about 10% of the world GNP. The semiconductor industry will grow at an even higher rate to surpass the steel industry in the early twenty-first century and to constitute 30% of the electronic industry in 2010 (i.e., about \$750 billion).

Figure 1.2 shows the composition of the world electronic industry. The largest segment is the computers and peripheral equipment (about 35% in 1998), followed by the telecommunication equipment (\sim 25%), industrial electronics (\sim 15%), consumer electronics (\sim 15%), defense and space electronics (\sim 5%), and transportation (\sim 5%). By the year 2010, the composition will remain essentially the same; however, the computers and peripheral equipment segment will expand more rapidly to capture about 45% of the total market.

The market for the world semiconductor industry^{2,3} is shown in Figure 1.3. The largest segment is the microprocessor and microcontroller units (about

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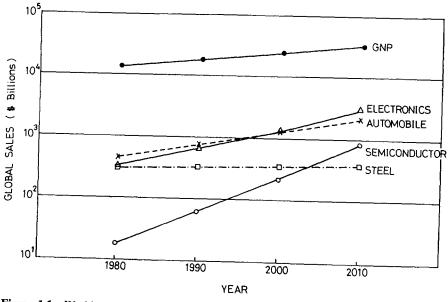


Figure 1.1 World gross national product and sales volumes of the electronics, automobile, semiconductor, and steel industries from 1980 to 1998 and projected to 2010. (After Refs. 1-3.)

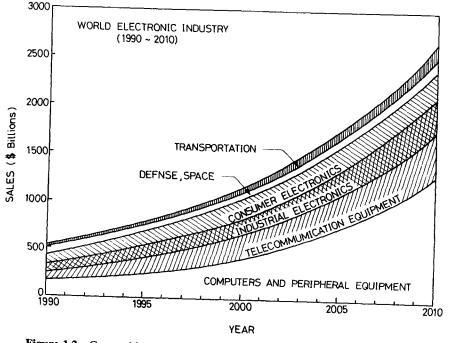


Figure 1.2 Composition of the world electronic industry. (After Refs. 2 and 3.)

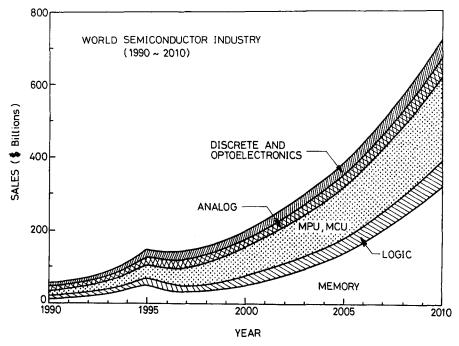


Figure 1.3 Composition of the world semiconductor industry. (After Refs. 2 and 3.)

35% in 1998), followed by memory (\sim 25%), analog (\sim 15%), discrete and optoelectronic (\sim 15%), and logic (\sim 10%). By the year 2010, memory may become the dominant segment and most of the memories will be integrated with microprocessors (and other logic circuits) to form systems-on-a-chip.

1.2 MILESTONES IN ULSI-RELATED DEVICES

The ULSI device is almost synonymous with the silicon MOSFET. This is mainly due to the MOSFET's intrinsic characteristics, which are uniquely suitable for highly complex ICs. In addition, many important device building blocks for ULSI such as CMOS, DRAM, and SRAM are also derived from MOSFET.

However, there are other important devices that can either enhance the MOSFET performance (e.g., bipolar transistor for BiCOMS) or have special features not obtainable from MOSFET (e.g., nonvolatile semiconductor memory for long-term information storage).

Table 1.1 lists some major ULSI related devices and device building blocks in chronological order. In 1947, the point-contact transistor was invented by Bardeen and Brattain.⁴ This was followed by Shockley's classic paper on bipolar transistor.⁵ Figure 1.4 shows the first transistor. The two point contacts at the bottom of the triangular quartz crystal were made from two stripes of gold foil

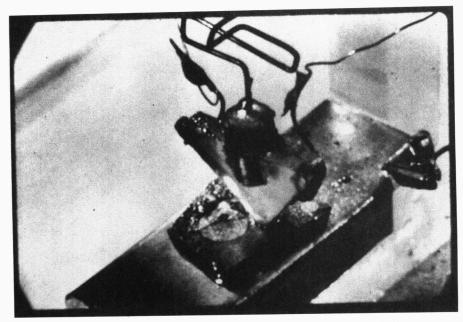


Figure 1.4 The first transistor. (After Bardeen and Brattain, Ref. 4.)

separated by about 50 µm and pressed onto the germanium surface. With one gold contact forward-biased and the other reverse-biased, the transistor action was observed. The invention of the bipolar transistor had an unprecedented impact on the electronics industry, and 1947 marked the beginning of the modern electronics era. In 1957, Kroemer proposed the heterojunction bipolar transistor (HBT) to improve the emitter efficiency; this device is potentially one of the fastest semiconductor devices. At present, the bipolar transistor has been replaced by MOSFET in a digital circuit. However, the bipolar transistor and

TABLE 1.1 Milestones of ULSI-Related Devices

Device or Device Building Block	Inventor(s) / Author(s)	Institution
Bipolar transistor	Bardeen, Brattain, Shockley	Bell Labs
	Kroemer	RCA Labs
-	Kilby	TI
Monoliohic IC	Noyce	Fairchild
Enhancement-mode MOSFET	Kahng, Atalla	Bell Labs
CMOS	Wanlass, Sah	Fairchild
	Kahng, Sze	Bell Labs
	Dennard	IBM
	Hoff et al.	Intel
Room-temperature single-electron	Yano et al.	Hitachi
	Bipolar transistor Heterojunction bipolar transistor Integrated circuit Monoliohic IC Enhancement-mode MOSFET CMOS Nonvolatile semiconductor memory One-transistor DRAM Microprocessor	Bipolar transistor Heterojunction bipolar transistor Integrated circuit Monoliohic IC Enhancement-mode MOSFET CMOS Nonvolatile semiconductor memory One-transistor DRAM Microprocessor Room-temperature single-electron Bardeen, Brattain, Shockley Kroemer Kilby Noyce Kahng, Atalla Wanlass, Sah Kahng, Sze Dennard Hoff et al.

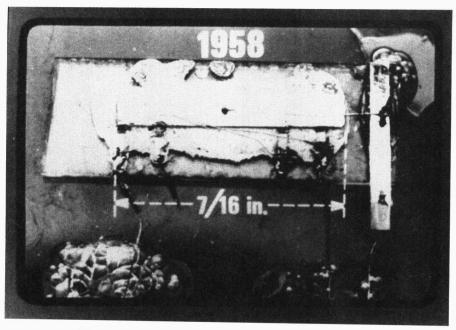


Figure 1.5 The first integrated circuit (After Kilby, Ref. 7.)

HBT are useful for analog and mixed analog and digital circuits. They can be combined with CMOS to form BiCMOS (bipolar CMOS) for high-speed/low-power applications.

In 1958, the first rudimentary integrated circuit (IC) was made by Kilby. It contained one bipolar transistor, three resistors, and one capacitor, all made in germanium and connected by wire bonding (see Fig. 1.5). In 1959, Noyce proposed the first monolithic IC by fabricating all devices in a single semiconductor substrate using oxide isolation and aluminum metallization (see Fig. 1.6). These inventions paved the way for the ever-expanding microelectronic industry.

In 1960, Kahng and Atalla proposed and fabricated the first enhancement-mode MOSFET using a thermally oxidized silicon structure. Figure 1.7 shows the device, which has a channel length of 25 µm and a gate oxide thickness of about 1000 Å. The two keyholes are the source and drain contacts, and the top elongated area is the aluminum gate evaporated through a metal mask. Although present-day MOSFETs have been scaled down to deep submicrometer regime, the choice of silicon and thermally grown silicon dioxide used in the first enhancement-made MOSFET remains the most important combination. The MOSFET and its related ICs now constitute over 90% of the semiconductor device market, and it is the most important device for ULSI applications.

As the complexity of the IC increases, we have moved from NMOS to the CMOS (complementary MOSFET) technology, which employs both NMOS and

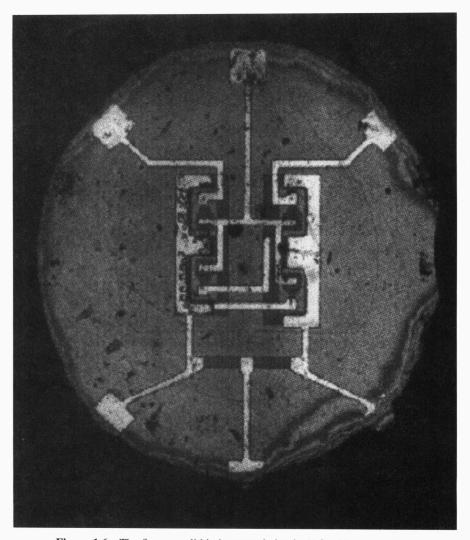


Figure 1.6 The first monolithic integrated circuit. (After Noyce, Ref. 8.)

PMOS to form the logic elements. The CMOS concept¹⁰ was proposed by Wanlass and Sah in 1963. The advantage of CMOS technology is that the logic elements draw significant current only during the transition from one state to another and draw very little current between transitions, allowing power consumption to be minimized. The CMOS technology is the dominant technology for ULSI.

In 1967, an important memory device was invented by Kahng and Sze. ¹¹ This is the nonvolatile semiconductor memory (NVSM), which can retain its stored

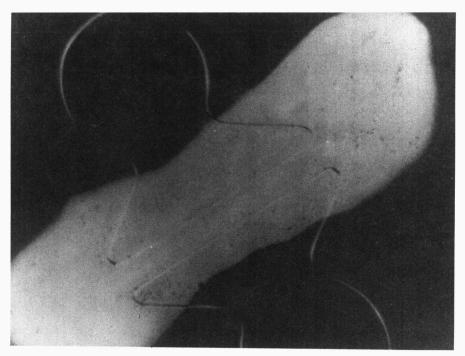


Figure 1.7 The first enhancement-mode MOSFET. (After Kahng and Atalla, Ref. 9.)

information when the power supply is switched off. A schematic diagram of the first NVSM is shown in Figure 1.8a. Although it looks like a conventional MOSFET, the major difference is the addition of the floating gate, in which semipermanent charge storage is possible. Because of its attributes of non-volatility, high density, low power consumption, and electrical rewritability, NVSM has become the dominant memory for portable electronic systems such as the cellular phone and the notebook computer.

A limiting case of the floating-gate memory is the single-electron memory cell (SEMC) shown in Figure 1.8b. By reducing the length of the floating gate to ultra small dimensions (e.g., 10 nm), we obtain the SEMC. Because of its small size the capacitance is also very small ($\sim 10^{-18}$ F, which is about a million times smaller than that of the device in Fig. 1.8a). At this dimension, when an electron tunnels into the floating gate, the tunneling of another electron is blocked due to quantum effect. The SEMC is an ultimate floating-gate memory cell, since we need at least one electron for information storage. The operation of a SEMC at room temperature was first demonstrated by Yano¹² et al. in 1994 (last item in Table 1.1). The SEMC can serve as a building block for multiterabit NVSM.

The one-transistor DRAM, which is a combination of a MOSFET and a charge-storage MOS capacitor, was proposed in 1967 by Dennard. A circuit representation and a cross-sectional view of the memory cell are shown in Figure