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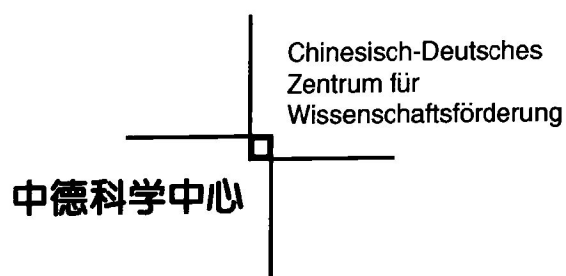
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**CHINESE PHYSICAL SOCIETY**

**CHINESE — GERMAN WORKSHOP**  
**ON**  
**CHARACTERIZATION AND DEVELOPMENT**  
**OF**  
**NANOSYSTEMS**

30<sup>th</sup> October – 2<sup>nd</sup> November 2000



Sino – German Center for Research Promotion, Beijing

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Tel: (86-10) 82649026 or 82649656

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主办单位: 中国物理学会

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编辑部地址: 北京 中关村 中国科学院物理研究所内

通讯地址: 100080 北京 603 信箱《中国物理》编辑部

电话: (010) 82649026, 82649656

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(Continued on the back of page 205)

## Editorial

In this special issue of Chinese Physics manuscripts of lectures and posters are published, which were presented at the Chinese - German workshop on "Characterization and Development of Nanosystems" held at the Sino - German Center for Research Promotion, Beijing, from October 30 to November 2, 2000. The workshop was organized by the Institute of Physics of the Chinese Academy of Sciences and the Institute for Nanotechnology of the Karlsruhe Research Center within the framework of a bilateral cooperation project on fundamentals of nanotechnology.

The workshop contributions reflected a broad field of research activities in nanoscience: Apart from seven plenary lectures, 28 special lectures and 17 posters have been presented, dealing with the following subjects: Manipulation and tunneling; nanocrystalline materials; tubes, wires and clusters; organic molecules and biomaterials; growth and fluctuation phenomena as well as structuring, sensing, and storage on the nanometer scale.

We take the opportunity to express our gratitude to all authors for their contributions to this issue as well as to the reviewers for their efforts to the ensuring the high quality of the published papers. We are grateful to the Editor -in - Chief of Chinese Physics Prof. Wang Nai-yan for the excellent cooperation and the production team for the careful preparation of this issue.

The organizers of the workshop are also grateful to the sponsors, who financed the workshop and enabled the preparation of this special issue, they are: Center of Materials Science, Chinese Academy of Sciences, National Natural Science Foundation of China, Ministry of Science and Technology of China, Chinese Academy of Sciences, Karlsruhe Research Center, International Office of the German Ministry of Research and Education, German Research Society, Sino - German Center for Research Promotion.

Last but not least, we would like to thank all colleagues from the Institute of Physics, the Physics Department of the Peking University, the Sino - German Center, and the Institute for Nanotechnology for their engaged contributions to the success of the workshop and especially also to the supporting program.

Beijing and Karlsruhe, June 2001

Shen Dian-hong  
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Editors

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## SINGLE-ELECTRON TRANSISTORS FOR FUTURE APPLICATIONS\*

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*Institute of Physics, Chinese Academy of Sciences, Beijing 100080, China*

(Received 1 January 2001; revised manuscript received 6 February 2001)

Single electron transistors with wire channels are fabricated by a nanoelectrode-pair technique. Their characteristics strongly depend on the channel widths and the voltages on the in-plane gates. A few dips in the Coulomb blockade oscillations were observed at the less positive gate voltages for a device with a 70nm-wide wire due to Coulomb blockade between the coupled dots. By applying negative voltages to the in-plane gates, the oscillations became periodic, which indicated the formation of a single dot in the conducting channel. These gates facilitate fabricating single-electron transistors with single dot structures, which have potential applications on its integration.

**Keywords:** single-electron tunneling, Coulomb blockade, quantum dots

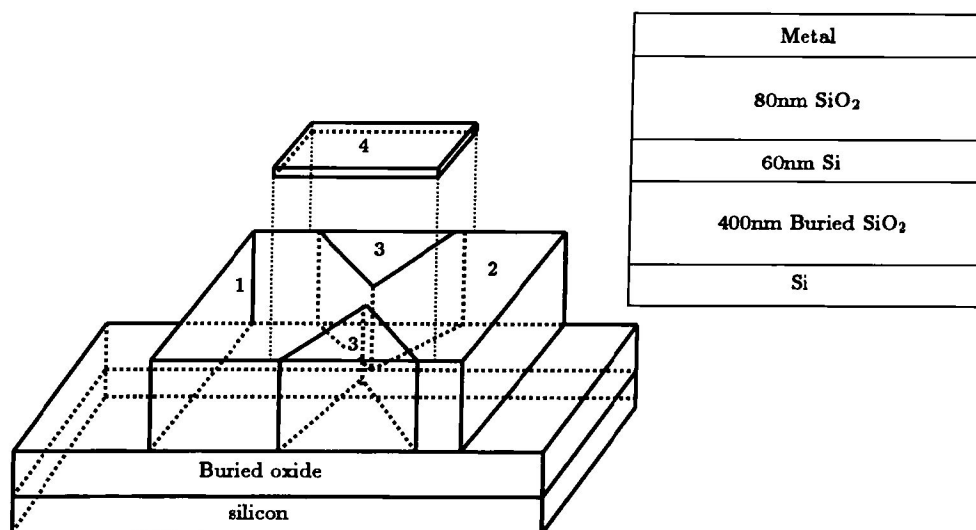
**PACC:** 7280E, 7335C, 7340G

With the recent rapid progress of fabrication techniques for nanostructures, many types of single-electron transistors (SET) have been fabricated. From the engineering viewpoint, single-electron transistors with wire channels<sup>[1-7]</sup> have a larger chance to be used to construct next-generation electronics with new system architectures. These transistors are not only suitable for their integration but also have potential applications in investigating quantum effects. Efforts have been paid to raise their working temperatures. The operation at room temperature for such kinds of devices has also been reported.<sup>[2]</sup> However, some of these devices have shown complicated behaviors because of the formation of multiple-dots in the wire channels. The realization of the high-temperature operation for these devices is mainly dependent on the channel width. As the channel width is decreased, the fluctuation of the geometric width becomes more serious. This leads to the formation of multiple-dots. Therefore, it is very important to develop techniques which can be used to realize one single dot in the narrow channels. We have combined the in-plane gate technique and the split-gate technique and developed them into a nanoelectrode-pair technique. By using this technique, we fabricated Si single-electron transistors with in-plane point contact metal (IPPCM) gates by using a self-aligned process. Point contacts were formed by etching a constriction with a designed

length of 100nm and a width of 70nm. The effective width of the conducting area can be modified by biasing the IPPCM gates. The pattern was defined by photolithography and mesa-etching.

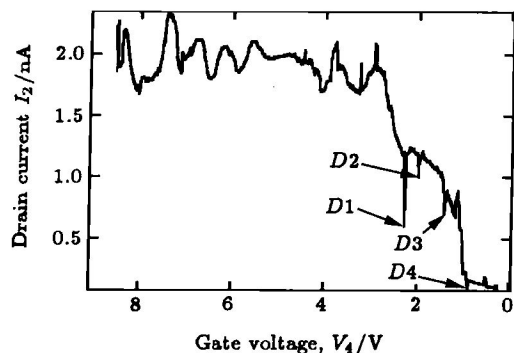
Figure 1 shows the schematic view of the single-electron transistors with IPPCM gates. A (001) oriented, p-type silicon-on-insulator (SOI) substrate prepared by the separation by implanted oxygen (SIMOX) technique was employed. The silicon single crystal layer was thinned by repeated thermal oxidation and HF etching of the oxide to 70nm. A point contact channel was formed in the Si layer between source and drain electrodes by photolithography and electron-cyclotron-resonance (ECR) plasma etching. Then the wafer was subjected to thermal oxidation in dry oxygen ambient. By using a self-aligned process, IPPCM gates were deposited. This was realized by rotating the wafer to expose both sides of the channel during evaporation. A layer of silicon dioxide with the thickness of 80nm was deposited and finally a top metal gate was formed. The oxide layer serves to insulate the IPPCM gates and the top gate. Applying positive voltages to the top gate leads to the formation of two-dimensional electron gases (2DEGs) at the inversion layer and controls the electrostatic potential of the SET island. Here we report on transport behavior of one device with 70nm-wide channel.

\*Project supported by the National Natural Science Foundation of China (Grant Nos. 69925410 and 19904015).



**Fig.1.** A schematic illustration of the fabricated device. The source and the drain are indicated by 1 and 2, the in-plane gates by 3, and the top gate by 4.

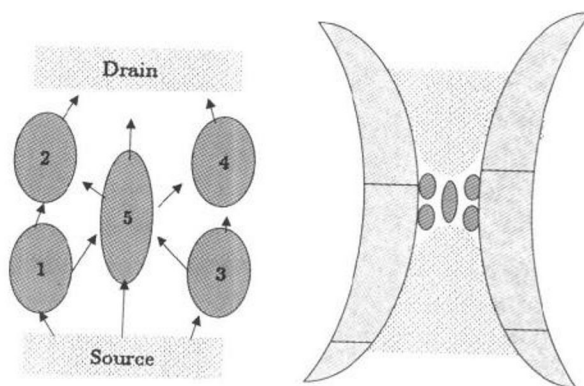
Figure 2 shows the drain current  $I_d$  versus the top gate voltage  $V_4$  characteristic of the device with 70nm-wide channel. The source is grounded. With scanning  $V_4$ ,  $I_d$  oscillates and decreases significantly at  $V_4 < 3.5\text{V}$ . Above  $V_4 = 3.5\text{V}$ , both the amplitude of the oscillations and the minimum of the current change a little. In the range of  $0.22\text{V} < V_4 < 3.5\text{V}$ ,  $I_d$  shows peaks at 2.16, 1.89, 1.36, 1.12, and 0.53V, and also exhibits dips at 2.26, 1.97, 1.41, and 0.90V. The shape of these three peaks is irregular and similar to that of the peaks above 3.5V. The irregular shape is typical characteristic of the Coulomb blockade oscillations (CBOs) through coupled dots.<sup>[7-9]</sup> Therefore, it can be inferred that a number of dots are formed at the constriction.



**Fig.2.**  $I_d$ - $V_4$  characteristic of the device with 70nm-wide channel at  $V_2=1\text{mV}$ ,  $V_3=0\text{mV}$ , and  $T=4.2\text{K}$ .

It could be easily understood by simply assuming that five dots be formed, as shown in Fig.3. The dips, which are indicated by D1, D2, D3, and D4 in Fig.2 arise from the stochastic Coulomb blockade within the

multiple-dot structure.<sup>[7-9]</sup> Decreasing  $V_4$  reduces the electron density in the inversion layer. Below some critical density, electrons localize in the potential minimum within the channel. So quantum dots are formed at the constriction. We argue that dots are formed even in the range  $9\text{V} > V_4 > 3.5\text{V}$  from the observation of both the irregular shape and their Coulomb staircases.



**Fig.3.** The shape of the channel and formation of the dots. The channel has a length of about 100nm and a width of 70nm.

Considering the shape of the channel, we assume that two dots be formed at each side of the channel and one dot at the center. This is reasonable because the fluctuation of the potential at the edge of the channel is more serious than that at the center of the channel. Transport through the channel is controlled by charging and discharging of all these five dots. For a special arrangement of the charge within the five dots, either Dot-1, Dot-2, and Dot-3 or Dot-5, Dot-2, and Dot-4

are in the Coulomb blockade regime. For such two cases, the channel was blocked and no electrons can flow through the channel. These give rise to a sharp drop in the drain current.<sup>[6,10,11]</sup> The drain current at the four dips in Fig.2 is not zero and this could be partly caused by cotunneling.

Applying negative voltages on the in-plane gates  $V_3$  squeezes the channel and depletes Dot-1, Dot-2, Dot-3, and Dot-4. The electrostatic potential of the four dots is raised so that all the electrons in these four dots are moved out and no other electrons can tunnel into these dots. So the charging and discharging disappear in the four dots. In this case, only Dot-5 controls the current flow through the channel and this leads to the observation of the CBOs with a period of 0.64 V in Fig.4.

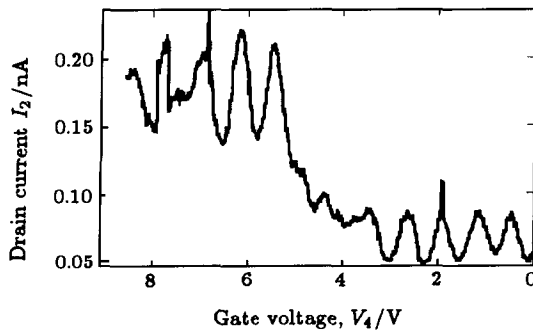


Fig.4. Coulomb blockade oscillations of the devices with 70nm-wide channel at  $V_2=1\text{mV}$ ,  $V_3=-116\text{mV}$ , and  $T=4.2\text{K}$ .

Formation of dots could arise from (1) the thickness fluctuations of the SOI layer, (2) the fluctuation of the channel width, (3) pattern dependent oxidation, (4) strain by etching and oxidation, (5) localized states and (6) Si nanoparticles at the interface between the SOI layer and the buried oxide layer. The narrow channel in our case is a point contact wire and the period of the thickness fluctuations should be much larger than its wire length. This rules out the thickness fluctuations for the formation of the dots. Ishikuro *et al.*<sup>[2]</sup> reported formation of the dots resulting from the fluctuation of the channel width. This contradicts our results on formation of multiple-dots for the devices with wide channels. We also checked the devices without thermal oxidation and found that CBOs could not be observed in these devices. So the considerable band gap shrinkage<sup>[12]</sup> from the oxidation induced compressive strain probably accounts for the formation of the dots in our devices.

In summary, we investigated a Si single-electron transistor with in-plane point metal gates. The device with 70nm-wide channel exhibited dips in the  $I_2 - V_4$  characteristic, which were attributed to stochastic Coulomb blockade within the multiple-dot structure. By applying negative voltages on the in-plane gates, the device showed clear periodic CBOs. These indicate that the in-plane point gates can be used to realize single-electron transistors with single dot structures.

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## RESONANT ANDREEV REFLECTION IN HYBRID SUPERCONDUCTING-NORMAL NANOSTRUTURES\*

LIN TSUNG-HAN(林宗涵)

*State Key Laboratory for Mesoscopic Physics Department of Physics, Peking University, Beijing 100871, China*

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After a brief description of hybrid superconducting-normal nanostructures and a brief introduction of the Andreev reflection, we present a theoretical investigation of the electron tunneling through a normal-metal/quantum-dot/superconductor (N-QD-S) system where multiple discrete levels of the QD is considered. By using the nonequilibrium-Green- function (NGF) method, the current  $I$  is obtained and studied in detail. We find that the Andreev tunneling shows clear resonant behavior, as obtained in previous works. Moreover, the current  $I$  versus the gate voltage  $V_g$  exhibits different kinds of peaks, depending on the bias voltage, the level-spacing of the QD, and the energy gap of the superconducting electrode. Besides, in  $I$ - $V$  characteristics extra peaks superimposed on the conventional current plateaus emerge, which stem from the resonant Andreev reflections.

**Keywords:** transport properties, tunneling phenomena

**PACC:** 7430F, 7450

### I. INTRODUCTION

Since early 1990s, the tremendous development of the nanofabrication technology has made it possible for manufacturing various hybrid superconducting-normal (S-N) structures, leading to the emergence of a very active research field. As a review of the achievements of early 1990s, the International Symposium on "Mesoscopic Superconductivity" was held in Karlsruhe, Germany in 1994.<sup>[1]</sup>

What is interesting in hybrid S-N nanostructures? It is well known that the essential feature of mesoscopes is the phase coherence of carriers, electrons or holes; while the superconductivity is a macroscopic quantum condensate with the coherence of Cooper pairs. How these two coherence effects couple with each other? The key mechanism is Andreev reflection, which causes the interplay of the above two kinds of coherences possible, and makes this new field of the condensed matter physics a very fruitful one. It was proposed by Andreev<sup>[2]</sup> that an electron is reflected from N-S interface in a peculiar way. Because of the energy gap of the superconductor, an electron incoming from normal metal with an energy  $\epsilon$  below the gap is forbidden to enter the superconductor. However, the incoming electron with the energy  $\epsilon$  and up-spin may pick up an electron with the energy  $-\epsilon$  and down-spin, both entering into the superconductor and

forming a Cooper pair; meanwhile a hole with a up-spin is left and going back to the normal metal. Although Andreev reflection is a second order process, it dominates for the clean N-S interface at low temperatures and small bias, since the conventional first order processes are forbidden.<sup>[3,4]</sup>

### II. MAIN PROPERTIES OF ANDREEV REFLECTION

The basic properties of Andreev reflection (AR) are: (1) Retro-reflection: the group velocities of the incident electron and Andreev reflected hole are in opposite directions (perfect only for the electron incoming at the Fermi level). (2) AR is a two-particle process, by which the normal current in N is converted into supercurrent in S. (3) AR is a phase coherent process: the Andreev reflected hole(electron) carries phase information both from the incident electron(hole) and from the the superconductor.<sup>[3,4]</sup>

Because the combination of mesoscopes and superconductivity, a number of novel and interesting effects appear, including the quantization of the maximum supercurrent of the superconducting quantum point contacts (SQPC); the enhanced conductance oscillations in Aharonov-Bohm ring with superconducting "mirrors"; the enhanced shot noise in N-S junctions; the AR bound states in SNS structures; the even-odd asymmetry in N-SQD-N and S-SQD-S sys-

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tems; the supercurrent control and reverse in SNS; the nonequilibrium and proximity effects in systems with ferromagnetic elements; the hybrid systems with carbon nanotube; etc.<sup>[1,3-9]</sup>

Works we have done for mesoscopic hybrid S-N structures include: (1) For the electron transport through a mesoscopic hybrid multiterminal resonant-tunneling system which contains a normal central region coupled to multiple superconducting leads, a general expression of the current is derived. This formula can be used to describe the case with time-dependent external fields applied to any parts of the system, arbitrarily finite voltages, and any kinds of interactions in the central region;<sup>[10]</sup> (2) Resonant Andreev reflection in a N-QD-S system;<sup>[11]</sup> (3) Photon-assisted Andreev tunneling through a mesoscopic system;<sup>[12]</sup> (4) Control of the supercurrent in a four-terminal Josephson junction;<sup>[13]</sup> and some others.

Here we only present our theoretical investigation for the electron tunneling through a N-QD-S structure, a normal quantum-dot connected to two leads, one is a normal metal and the other is a superconductor. Different from Refs.[14] and [15] where only a single level of the QD has been considered, here we assume that the QD has discrete multiple levels, but the intra-dot Coulomb interactions will be neglected for simplicity. Using the nonequilibrium-Green-function(NGF) method, the current formula has been derived. The dependence of the current  $I$  upon the gate-voltage, the bias voltage, the level-spacing of the QD, and the energy gap of the superconducting electrode have also been studied in detail.<sup>[11,12]</sup>

### III. MODEL AND FORMULATION

We assume that the system under consideration is described by the following Hamiltonian<sup>[16,17]</sup>

$$H(\tau) = H_L + H_R + H_{\text{dot}} + H_T(\tau), \quad (1)$$

where

$$\begin{aligned} H_L &= \sum_{k,\sigma} (\epsilon_{L,k}^0 - ev_L) a_{L,k\sigma}^\dagger a_{L,k\sigma} \\ H_R &= \sum_{p,\sigma} \epsilon_{R,p}^0 a_{R,p\sigma}^\dagger a_{R,p\sigma} \\ &\quad + \sum_p \left[ \Delta^* a_{R,p\downarrow} a_{R,-p\uparrow} + \Delta a_{R,-p\uparrow}^\dagger a_{R,p\downarrow}^\dagger \right] \\ H_{\text{dot}} &= \sum_{i,\sigma} (\epsilon_i^0 - eV_g) c_{i\sigma}^\dagger c_{i\sigma}, \end{aligned} \quad (2)$$

$$\begin{aligned} H_T &= \sum_{k,\sigma,i} \left[ t_L a_{L,k\sigma}^\dagger c_{i\sigma} + t_L^* c_{i\sigma}^\dagger a_{L,k\sigma} \right] \\ &\quad + \sum_{p,\sigma,i} \left[ t_R e^{iev_R \tau} a_{R,p\sigma}^\dagger c_{i\sigma} + t_R^* e^{-iev_R \tau} c_{i\sigma}^\dagger a_{R,p\sigma} \right]. \end{aligned}$$

where  $H_L$  describes the noninteracting electrons in the left normal-metal lead,  $a_{L,k\sigma}^\dagger$  ( $a_{L,k\sigma}$ ) are the creation (annihilation) operators of the electron in the left lead, and  $v_L$  is the voltage of the left lead.  $H_R$  describes the right superconducting lead with the energy gap  $\Delta$ .  $H_{\text{dot}}$  is the Hamiltonian of the quantum dot with multiple discrete energy levels, characterized by the index  $i$  and spin  $\sigma$ . For simplicity, the intra-dot electron-electron Coulomb interaction has been neglected.  $V_g$  is the gate voltage which controls the energy levels in the dot.  $H_T$  denotes the tunneling part of the Hamiltonian, and  $t_v$  ( $v = L, R$ ) is the hopping matrix and is assumed to be independent of the state  $k$  of the leads and the dot-state  $(i, \sigma)$  for simplicity. In order to obtain the Hamiltonian (1) and (2), we have performed a unitary transformation similar to Ref.[16], so that the voltage of the right lead,  $v_R$ , appears as a phase factor in the hopping elements.

Since the current is conserved, it is enough to calculate the current from the left lead to the QD. From the evolution of the total number operator of the electrons in the left lead,  $N_L = \sum_{k,\sigma} a_{L,k\sigma}^\dagger a_{L,k\sigma}$ :<sup>[18,19]</sup>

$$\begin{aligned} I(\tau) &= -e < \dot{N}_L(\tau) > \\ &= \frac{ie}{\hbar} < [N_L, H] > \\ &= \frac{2e}{\hbar} \text{Re} \sum_{k,i,\sigma} t_L \ll c_{i\sigma}(\tau) | a_{L,k\sigma}^\dagger(\tau) \gg^<, \end{aligned} \quad (3)$$

where the Green function  $\ll A(\tau) | B(\tau') \gg^<$  is defined as:  $\ll A(\tau) | B(\tau') \gg^< \equiv i < B(\tau') A(\tau) >$ . With the help of Dyson's equation, using the exact Green functions of the electron in the left lead (normal-metal) without the coupling between the dot and the lead, taking a replacement of the sum over  $k$  by an integral over energy with the help of the density of states in the left lead, and introducing the linewidth function  $\Gamma_L(\omega) = 2\pi |t_L|^2 \rho_L(\omega)$ , then Eq.(3) becomes to

$$\begin{aligned} I(\tau) &= -\frac{4e}{\hbar} \text{Im} \int \frac{d\omega}{2\pi} \Gamma_L \int_{-\infty}^{\tau} d\tau' e^{i(\omega - ev_L)(\tau - \tau')} \\ &\quad \cdot \sum_{i,j} \left[ \ll c_{i\uparrow}(\tau) | c_{j\uparrow}^\dagger(\tau') \gg^r f_L(\omega) \right. \\ &\quad \left. + \ll c_{i\uparrow}(\tau) | c_{j\uparrow}^\dagger(\tau') \gg^< \right] \\ &\equiv -\frac{4e}{\hbar} \text{Im} \int \frac{d\omega}{2\pi} \Gamma_L \int_{-\infty}^{\tau} d\tau' e^{i(\omega - ev_L)(\tau - \tau')} \\ &\quad \cdot [\mathbf{G}^r(\tau, \tau') f_L(\omega) + \mathbf{G}^<(\tau, \tau')]_{11}, \end{aligned} \quad (4)$$

where  $f_L(x) = [\exp(x/k_B T) + 1]^{-1}$  is the Fermi distribution function with  $T$  the temperature, and we have

$$G^{r,a}(\tau, \tau') \equiv \mp i \theta(\pm \tau \mp \tau') \sum_{i,j} \begin{pmatrix} \langle \{c_{i\uparrow}(\tau), c_{j\uparrow}^\dagger(\tau')\} \rangle & \langle \{c_{i\uparrow}(\tau), c_{j\downarrow}(\tau')\} \rangle \\ \langle \{c_{i\downarrow}^\dagger(\tau), c_{j\uparrow}^\dagger(\tau')\} \rangle & \langle \{c_{i\downarrow}^\dagger(\tau), c_{j\downarrow}(\tau')\} \rangle \end{pmatrix}, \quad (5)$$

$$G^<(\tau, \tau') = i \sum_{i,j} \begin{pmatrix} \langle c_{j\uparrow}^\dagger(\tau') c_{i\uparrow}(\tau) \rangle & \langle c_{j\downarrow}(\tau') c_{i\uparrow}(\tau) \rangle \\ \langle c_{j\uparrow}^\dagger(\tau') c_{i\downarrow}^\dagger(\tau) \rangle & \langle c_{j\downarrow}(\tau') c_{i\downarrow}^\dagger(\tau) \rangle \end{pmatrix}. \quad (6)$$

After a tedious calculation (details see Refs.[11] and [12]), we finally obtain the following current formula:

$$I = I_A + I_1,$$

with

$$I_A = \frac{2e\Gamma_L^2}{\hbar} \int \frac{d\omega}{2\pi} |\tilde{G}_{12}^r|^2 [f_L(\omega + ev_L - ev_R) - f_L(\omega - ev_L + ev_R)], \quad (7)$$

$$I_1 = \frac{2e\Gamma_L\Gamma_R}{\hbar} \int \frac{d\omega}{2\pi} \tilde{\rho}_R(\omega) \left[ |\tilde{G}_{11}^r|^2 + |\tilde{G}_{12}^r|^2 - \frac{2\Delta}{|\omega|} \text{Re}[\tilde{G}_{11}^r(\omega)(\tilde{G}_{12}^r(\omega))^*] \right] \cdot [f_L(\omega + ev_L - ev_R) - f_R(\omega)]. \quad (8)$$

The formulas of the current, Eqs.(7) and (8), are the central results of this paper. The current  $I_A$  represents the contribution from the Andreev reflection. The current  $I_1$  consists of three different processes:<sup>[16]</sup> (1) The conventional electron tunneling through the system, i.e., the term  $\Gamma_L\Gamma_R\tilde{\rho}_R|\tilde{G}_{11}^r(\omega)|^2$ ; (2) An electron incident from the left lead will convert into a holelike in the right superconducting lead, i.e., the term  $\Gamma_L\Gamma_R\tilde{\rho}_R|\tilde{G}_{12}^r(\omega)|^2$ , corresponding to a “branch crossing” process in the BTK’s theory;<sup>[21]</sup> (3) An electron (or a hole) incident from the left lead tunnels into the right superconducting lead, picks up a quasi-particle (or a quasi-hole) in the superconductor and creates (or annihilates) a Cooper pair, i.e., the term  $-\Gamma_L\Gamma_R\tilde{\rho}_R \frac{2\Delta}{|\omega|} \text{Re}[\tilde{G}_{11}^r\tilde{G}_{12}^{r*}]$ . At zero temperature, if  $|eV| < \Delta$  one has  $I_1 = 0$ , i.e., only the Andreev reflection process contributes to the current. However, when  $|eV| > \Delta$ , all of the processes mentioned above make the contributions to the current.

#### IV. DIFFERENT SERIES OF RESONANT ANDREEV REFLECTION PEAKS

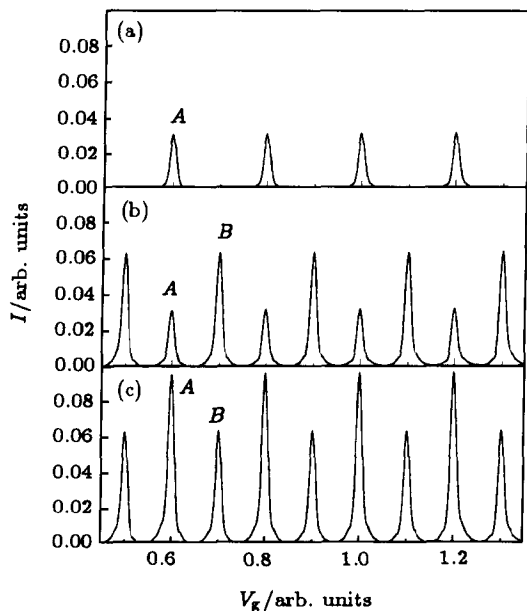
On the basis of the current formulas, Eqs.(7), and (8), we investigate the properties of the current

introduced the Nambu representation ( $2 \times 2$  matrix) of the Green functions  $G^r(\tau, \tau')$  and  $G^<(\tau, \tau')$ :<sup>[16,20]</sup>

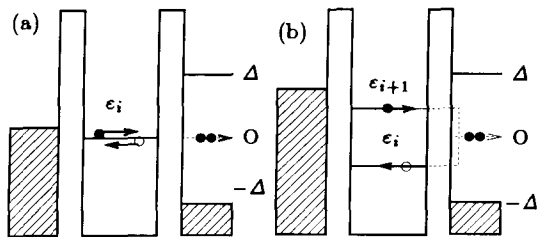
$I$  versus the gate voltage  $V_g$ . Unlike the system of N-QD-N in which the current  $I$  versus the gate voltage  $V_g$  at small bias  $V$  exhibits a single series of equally spaced resonance peaks. Now with one of the leads replaced by a superconducting lead, the situation becomes more complicated. In the following we shall discuss in detail two different cases for  $eV < \Delta$  and  $eV > \Delta$ . We set (1) the temperature  $T = 0$ , (2) the voltage of the right leads  $v_R = 0$  due to the gauge invariance, and carry out all calculations in units of  $\hbar = e = 1$ .

##### A. The case with $V < \Delta$

In this case only the Andreev reflection current  $I_A$  exists. Figure 1 presents  $I$  vs  $V_g$  at different bias. In numerical studies, we have assumed that the QD has 10 discrete levels with equal level-spacing. These levels move downward as  $V_g$  increases. At small bias,  $I$  vs  $V_g$  exhibits a single series of peaks with equal spacing (marked with “A” in Fig.1(a)). Notice that these resonance peaks are not due to the conventional resonant tunneling, because the conventional tunneling is completely forbidden for  $V < \Delta$ . Instead, these peaks come from the Andreev reflections. When the energy of a state- $i$  of the QD is lined up with the chemical potential of the right superconducting lead,  $\mu_R$  ( $\mu_R = -v_R$  is set to be zero), then an electron incident from the left lead with the energy  $\epsilon = \mu_R$  can tunnel into the state- $i$  of the QD, leading to a hole propagating back to the state- $i$  in the QD and the creation of a Cooper pair in the right superconducting lead because of Andreev reflection (see Fig.2(a)). As a result, a current peak emerges in the  $I - V_g$  curve. Notice that in this Andreev reflection process the incident electron and the reflected hole go through the same state- $i$  of the QD. If the energy level is not lined up with  $\mu_R$ , the Andreev reflection will be very weak, corresponding to a very small Andreev reflection current  $I_A$ .



**Fig.1.** The current  $I$  vs the gate voltage  $V_g$  for  $V < \Delta$ . The QD has 10 states with equal level-spacing,  $\Delta\epsilon = 0.2$ ; and  $\epsilon_0 = 0$  at  $V_g = 0$ . Other parameters are chosen as  $\Gamma_L = \Gamma_R = 0.01$ ,  $\Delta = 1$ . (a), (b), and (c) corresponding to three different bias:  $V = 0.02, 0.15$ , and  $0.25$ , respectively.



**Fig.2.** A Schematic diagram for the Andreev reflections: (a) the Andreev reflection through the state- $i$  itself; (b) the Andreev reflection through two states, the state- $i$  and state- $(i+1)$ .

Gradually increasing the bias  $V$ , the basic feature of  $I$ - $V_g$  does not change much, as long as  $V < \Delta\epsilon/2$ , where  $\Delta\epsilon$  is the level-spacing of the QD. Hence the corresponding differential conductance  $dI/dV$  is almost zero for  $V < \Delta\epsilon/2$ . However, when  $V$  is greater than  $\Delta\epsilon/2$ , a series of extra peaks emerges in the  $I$ - $V_g$  curve (marked with “B” in Fig.1(b)). The  $B$ -type peaks are located in the middle of the  $A$ -type peaks with the same peak spacing as that of the  $A$ -type peaks, but have much larger amplitudes than that of the  $A$ -type peaks (approximately doubled).

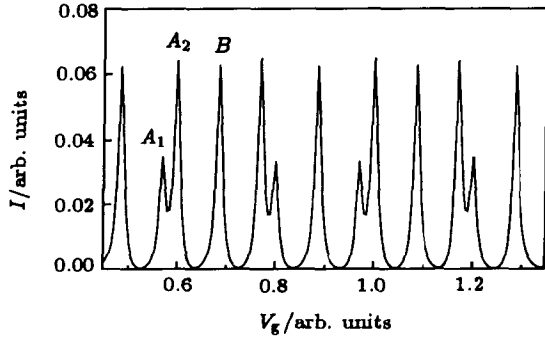
The  $B$ -type peaks are originated from the following Andreev reflection. For a certain gate voltage  $V_g$ , two neighboring states of the QD, say  $\epsilon_i$  and  $\epsilon_{i+1}$ , satisfy two conditions: the chemical potential of the right

superconducting lead,  $\mu_R$ , is located in the middle of the two levels; while both of these two levels are below the chemical potential of the left lead,  $\mu_L$  ( $\mu_L = -v_L$ ). So an electron incident with energy  $\epsilon = \epsilon_{i+1}$  (or  $\epsilon_i$ ) tunnels from the left lead through the left barrier into the QD’s state- $(i+1)$  (or state- $i$ ), then Andreev reflected at the right barrier as a hole back to the QD’s state- $i$  (or state- $(i+1)$ ), with a Cooper pair created in the superconductor in the meantime (see Fig.2(b)). Notice that in the  $B$ -type Andreev reflection both state- $(i+1)$  and state- $i$  contribute to the current, while the  $A$ -type Andreev reflection only involves a single state, so  $B$ -type peak has approximately twice the amplitude as that of  $A$ -type peak. If  $V < \Delta\epsilon/2$ , the  $B$ -type Andreev reflection can not happen at zero temperature.

When  $V$  is less than  $\Delta\epsilon$  the amplitude of the  $A$ -type peak is smaller than that of the  $B$ -type peak. As the bias  $V$  is larger than  $\Delta\epsilon$  (within the energy range of  $\Gamma$ ), the amplitude of the  $A$ -type peak becomes larger than that of  $B$ -type peak and eventually reaches a height three times its original value (see Fig.1(c)). The positions of  $A$ -type peaks do not change. In fact, for a certain gate voltage  $V_g$ , a QD’s state- $i$  will line up with the chemical potential  $\mu_R$ , so the Andreev reflection through the state- $i$  itself replaces the original  $A$ -type peak. Meanwhile, not only the state- $(i+1)$  and the state- $(i-1)$  are just at  $\Delta\epsilon$  and  $-\Delta\epsilon$ , respectively, both of them are below the left chemical potential  $\mu_L$  as well, due to the condition  $V > \Delta\epsilon$ . So the Andreev reflection can also occur through the state- $(i+1)$  and state- $(i-1)$ . Now three states  $(i-1)$ ,  $i$ , and  $(i+1)$  contribute to the  $A$ -type peaks, resulting in a much higher peak.

In the above numerical calculation, we have assumed that the energy levels in the dot are equal-spacing. If the level-spacing are unequal, the results will be more complicated. Generally, the interval between peaks of each type becomes different from peak to peak. Here we consider a special case of the quantum dot with 10 energy levels and the level-spacing as:  $\Delta\epsilon_i = 0.17$  for even  $i$ ,  $\Delta\epsilon_i = 0.23$  for odd  $i$ , and  $\epsilon_0 = 0$  at  $V_g = 0$  ( $\Delta\epsilon_i \equiv \epsilon_{i+1} - \epsilon_i$ ). Figure 3 shows the case of  $V > \Delta\epsilon_i$ . In contrast to the equal-spacing case (compare to Fig.1(c)), the original  $A$ -type peaks are splitted into two peaks (marked with “ $A_1$ ” and “ $A_2$ ” in Fig.3). For the unequal level-spacing case, the two peaks from above mentioned two kinds of Andreev reflections no longer overlap completely. So one sees a series of splitted two-peak resonances.



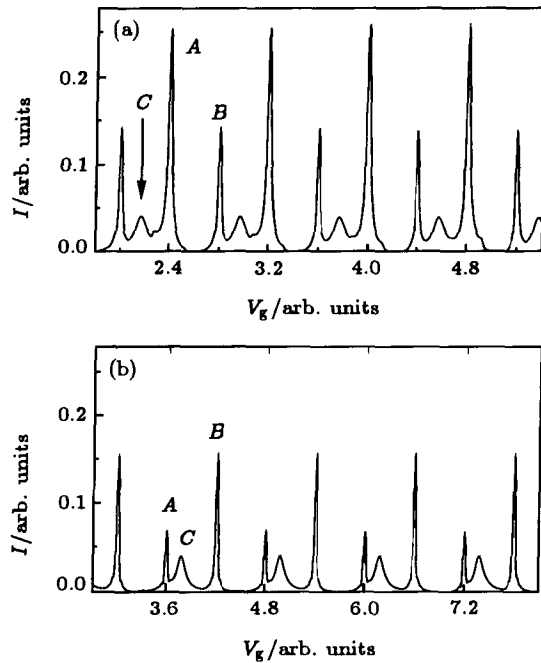


**Fig.3.** The current  $I$  vs the gate voltage  $V_g$  for  $V < \Delta$ . The QD has 10 states but with unequal level-spacing:  $\Delta\epsilon_i = 0.17$  for even  $i$ ,  $\Delta\epsilon_i = 0.23$  for odd  $i$ , and  $\epsilon_0 = 0$  at  $V_g = 0$ . The other parameters are the same as in Fig.1(c).

In the rest part of this paper, we will keep the assumption of equal level-spacing for simplicity.

### B. The case with $V > \Delta$

In the case of  $V > \Delta$ , all kinds of the processes contribute to the current  $I$ , including Andreev reflection and all three kinds of the tunneling processes mentioned above, i.e., both  $I_A$  and  $I_1$  are non-zero.

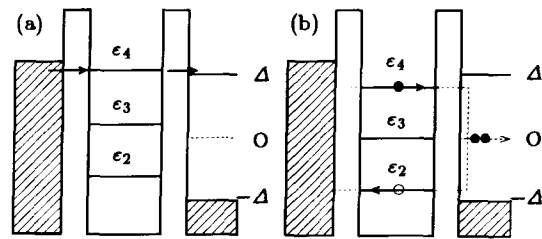


**Fig.4.** The current  $I$  vs the gate voltage  $V_g$  for  $V > \Delta$ . The QD has 10 states with equal level-spacing; and  $\epsilon_0 = 0$  at  $V_g = 0$ . Other parameters are chosen as  $\Gamma_L = \Gamma_R = 0.02$ ,  $\Delta = 1$ , and  $V = 1.04$ . (a) and (b) correspond to  $\Delta\epsilon = 0.8$  and  $1.2$ , respectively.

Here we studied  $I$ - $V_g$  for  $V$  slightly larger than  $\Delta$  (see Fig.4). The difference between Fig.4.(a) and (b) is due to the different level-spacing of the QD's states.

Now three series of the peaks emerge in the curve, marked by "A", "B", and "C", respectively, and with the same intervals (or "periods") between the peaks for each series. The positions of the B-type peaks are in the middle of the A-type peaks; and the position of the C-type peak is shifted from the A-type peak by a certain amount which depends on the level-spacing  $\Delta\epsilon$  and the gap  $\Delta$ . The height of the C-type peak is the smallest one.

In the following calculation, we fix the parameters as indicated in Fig.4.(a), and set  $\Delta\epsilon = 0.8$ . Since  $\epsilon_i = \epsilon_i^0 - V_g$  which varies with the gate voltage  $V_g$ , the state  $\epsilon_4$  is just below the chemical potential  $\mu_L$  and above the gap  $\Delta$  when  $V_g = 2.2$  (see Fig.5(a)). So the conventional electron and quasi-particle tunneling processes are allowed, leading to the C-type peaks. With the increase of the gate voltage, each level of the QD moves downward. When  $V_g = 2.4$ , the state  $\epsilon_3$  is lined up with the chemical potential  $\mu_R$ , and the energy difference between the state  $\epsilon_4$  or  $\epsilon_2$  and  $\mu_R$  are equal (See Fig.5(b)). So the Andreev reflection can occur through either the state  $\epsilon_3$  itself or other two states  $\epsilon_4$  and  $\epsilon_2$ , leading to the A-type peaks with the largest amplitude (See Fig.4(a)). As  $V_g$  is increased further at  $V_g = 2.8$ , the energy difference between the state  $\epsilon_4$  or state  $\epsilon_3$  and  $\mu_R$  are equal (not shown here), and both of  $\epsilon_3$  and  $\epsilon_4$  are below the chemical potential of the left lead,  $\mu_L$ . Then the Andreev reflection occurs through the states  $\epsilon_4$  and  $\epsilon_3$ , corresponding to the B-type peaks. For the case with symmetric barriers (as assumed in our calculations), i.e.  $\Gamma_L = \Gamma_R$ , the maximum probability of the Andreev reflection can reach one, but the conventional transmission probability is less than one because  $\Gamma_L \neq \Gamma_R \tilde{\rho}_R$ . Therefore the height of the C-type peak is the smallest one.



**Fig.5.** A schematic diagram for the tunneling processes responsible for the C-type and the A-type peaks. (a) and (b) describe the positions of the QD's states, and show the related conventional tunneling (C-type) and the Andreev tunneling (A-type) processes, respectively.