

GOTOP

LD-789: Amp

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chaining com

semblies(P/N

PLD

數位邏輯

實務應用

碁峯資訊

陳兆麟

30分鐘系列

易懂・易學・易用



書號: CQ001
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雙色印刷

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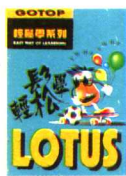
書號: CV001

漢書



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Windows
中文版



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Lotus 易爾算



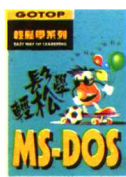
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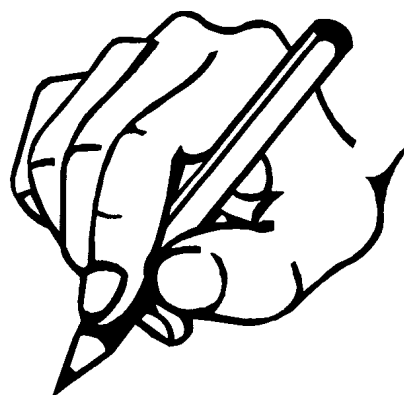
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附



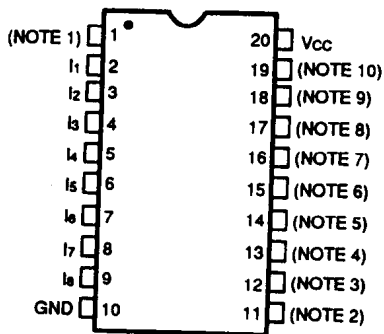
D

錄

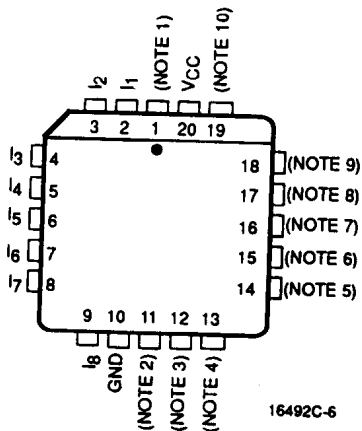
各種 PAL 之 接腳圖

PAL16R8 Family

20-Pin PLCC

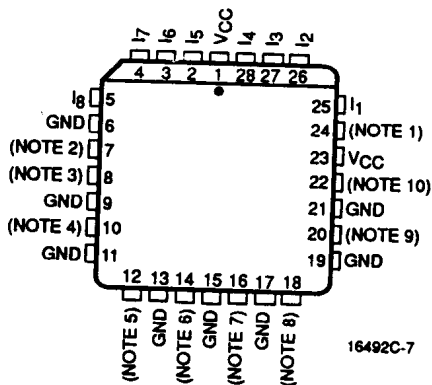


16492C-5



16492C-6

28-Pin PLCC



16492C-7

PIN DESIGNATIONS

CLK = Clock
GND = Ground
I = Input
I/O = Input/Output
O = Output
OE = Output Enable
Vcc = Supply Voltage

Note:

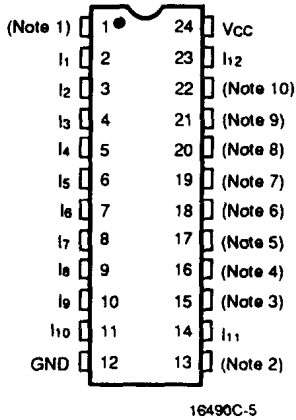
Pin 1 is marked for orientation.

Note	16L8	16R8	16R6	16R4
1	I ₆	CLK	CLK	CLK
2	I ₆	OE	OE	OE
3	O ₁	O ₁	I/O ₁	I/O ₁
4	I/O ₂	O ₂	O ₂	I/O ₂
5	I/O ₃	O ₃	O ₃	O ₃
6	I/O ₄	O ₄	O ₄	O ₄
7	I/O ₅	O ₅	O ₅	O ₅
8	I/O ₆	O ₆	O ₆	O ₆
9	I/O ₇	O ₇	O ₇	I/O ₇
10	O ₈	O ₈	I/O ₈	I/O ₈

圖 D-1

PAL20R8 Family

SKINNYDIP/FLATPACK



Note: Pin 1 is marked for orientation.

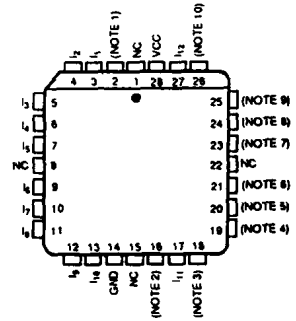
Note	20L8	20R8	20R6	20R4
1	I ₀	CLK	CLK	CLK
2	I ₁₃	$\overline{OE}$	$\overline{OE}$	$\overline{OE}$
3	O ₁	O ₁	I/O ₁	I/O ₁
4	I/O ₂	O ₂	O ₂	I/O ₂
5	I/O ₃	O ₃	O ₃	O ₃
6	I/O ₄	O ₄	O ₄	O ₄
7	I/O ₅	O ₅	O ₅	O ₅
8	I/O ₆	O ₆	O ₆	O ₆
9	I/O ₇	O ₇	O ₇	I/O ₇
10	O ₈	O ₈	I/O ₈	I/O ₈

PIN DESIGNATIONS

CLK = Clock
 GND = Ground
 I = Input
 I/O = Input/Output
 NC = No Connect
 O = Output
 $\overline{OE}$ = Output Enable
 Vcc = Supply Voltage

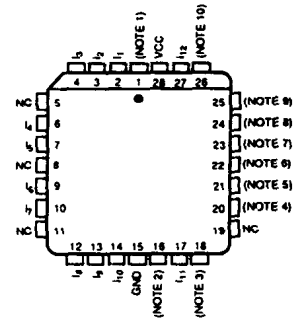
PLCC/LCC

JEDEC: Applies to -5, -7(-12/10 mil),
 -10(-15 mil), B-2 Series Only



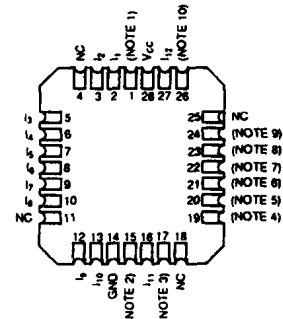
PLCC

Applies to B and A Series Only



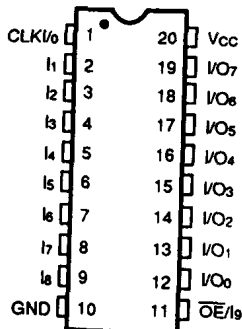
LCC

Applies to B and A Series Only

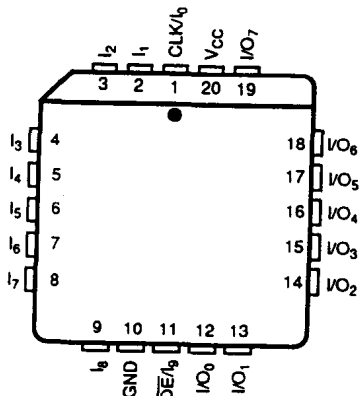


PALCE16V8 Family

DIP/SOIC

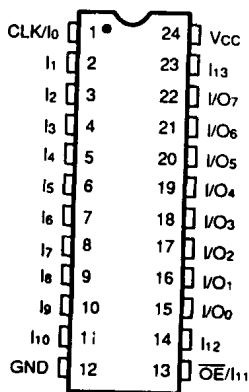
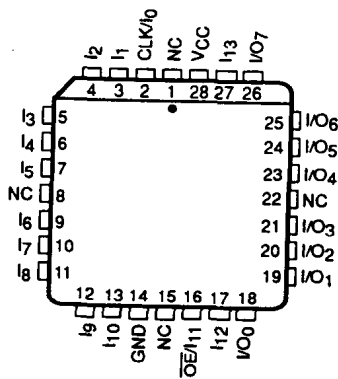


PLCC/LCC



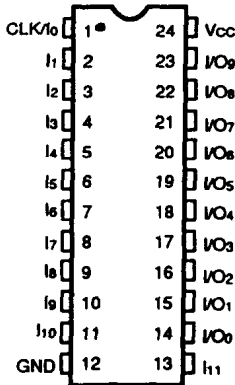
PALCE20V8 Family

SKINNYDIP

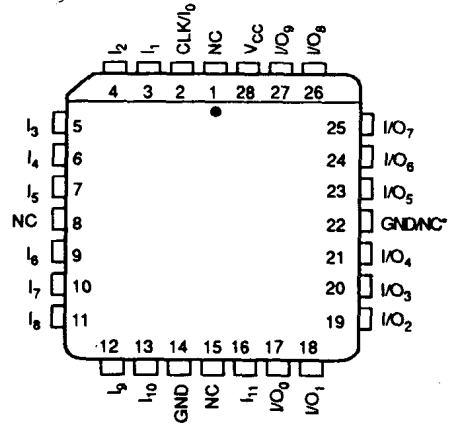
**PLCC/LCC**

PALCE22V10 Family

SKINNYDIP/SOIC/FLATPACK

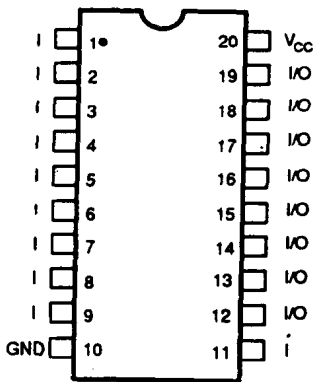


PLCC/LCC

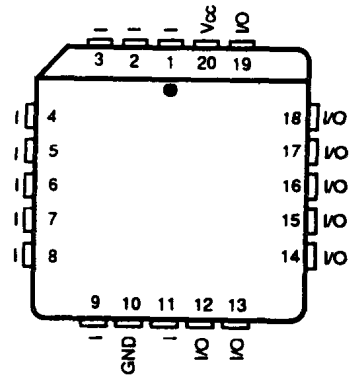


AmPAL18P8B/AL/A/L

DIP

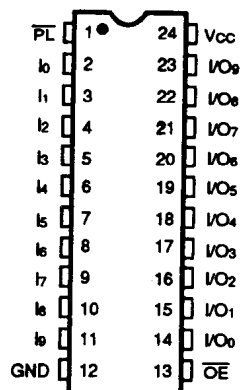


PLCC

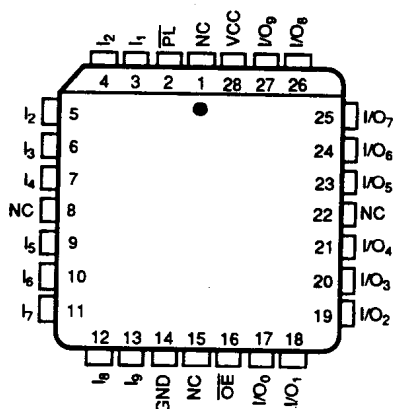


PALCE20RA10 Family

SKINNYDIP

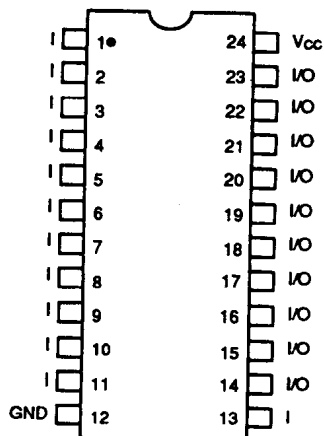


PLCC JEDEC

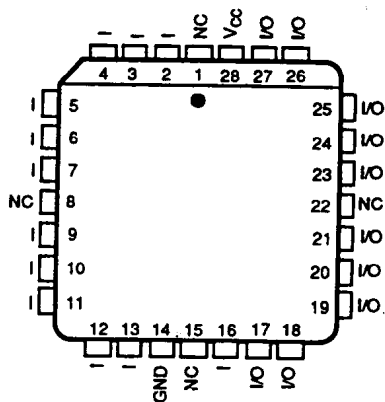


AmPAL22P10B/AL/A

SKINNYDIP

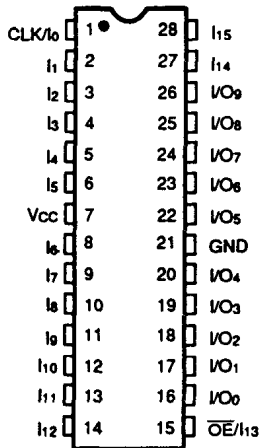


PLCC



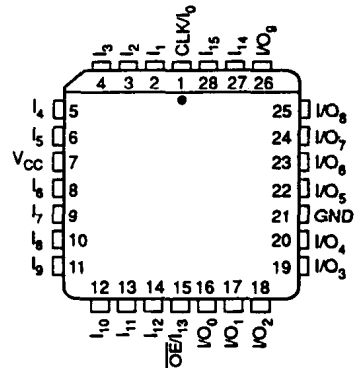
PALCE24V10H-15/25

SKINNYDIP



12222E-2

PLCC



12222E-3

Note:

Pin 1 is marked for orientation.

PIN DESIGNATIONS

- CLK = Clock
- GND = Ground
- I = Input
- I/O = Input/Output
- OE = Output Enable
- Vcc = Supply Voltage

PLD數位邏輯實務應用



磁片簡介

本學習磁片含有書中所列組合電路例題11個PDS檔及循序電路例題10個PDS檔。



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學 歷：交通大學資訊科學研究所
著 作：PCAD入門及應用實例
PC/AT 286，386線路
分析及檢修實務



本書以簡單實例使讀者輕易瞭解 PAL 的基本構造，並說明如何使用 PALASM 4 軟體，對每一例題先以數位邏輯的觀點分析，並選擇適當的 PAL，再以 PALASM 4 的語法描述之，因此熟讀本書將使讀者更進一步瞭解數位邏輯，並快速的學會 PALASM 4 程式的撰寫。



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使用者指引

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| ■ 進 階 | □ 參考手冊 |
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| | □ 應用技巧 |



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