

计算机科学技术文献索引 CAD题录专集

中国科学院科学数据库工程
计算机科学技术文献数据库协作组

中国科学院科学数据库工程JKJ计算机文献数据库是在国务院电子振兴办、院科学技术部、院科学数据库筹备处的领导与支持下,由中国科学院计算所牵头建立的。参加建库的单位有:沈阳计算所、沈阳自动化所、成都计算所、自动化所、电子所、电工所、计算中心、软件所、管理学院、数据库筹备处、院文献情报中心参考部、院微电子中心和计算所等。

JKJ计算机文献库收录了有关计算机科学技术的中外文期刊,会议录5000多种,现进库量近40000题录篇。

1. 著录项目举例

J0009(1) 8603481(2) H.1.2(3) 数据网络(4)
DATABASE NETWORK(5) ASANO SHOUICHIRO(6)
INFORMATION PROCESSING(JAPAN)(7)
BJ44, BJ41, BJ40(8) E(9) J(10) 1986(11)
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SY02 中国科学院沈阳计算所
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CD06 中国科学院成都计算机应用所
BJ44 中国科学院北京自动化所
BJ41 中国科学院北京电子所
BJ42 中国科学院北京电工所
BJ45 中国科学院北京计算中心
BJ40 中国科学院北京计算所
BJ40# 中国科学院北京软件所
BJ45# 中国科学院干部管理学院
中国科学院微电子中心

计算机辅助设计

- J.9.1 计算机辅助工程(CAE)
- J.9.2 计算机辅助制造(CAM)
- J.9.3 计算机辅助概念
- J.9.4 计算机辅助开发
- J.9.5 计算机辅助设计服务
- J.9.6 计算机辅助设计语言
- J.9.7 计算机辅助试验
- J.9.8 计算机辅助解题系统
- J.9.9 体系结构的计算机辅助设计
- J.9.10 自动设计辅助工具
- J.9.11 自动设计工程
- J.9.12 其它

作者索引
主题索引

J00001 8504144 J.9.1

计算控制的结合

THE INTEGRATION OF COMPUTING AND CONTROL
SHORTLAND M.G.
IEEE PROCEEDINGS - PART E:
SY02,CD06,BJ40,SY03,BJ42
E J 1985 V.132-E,N.1,P.1-7
CAD, APPLICATIONS(ON ENGINEERING),
CONTROL DEVICE, CONTROL SYSTEM,
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J00002 8504170 J.9.1

计算机辅助保护装置配置—情况研究

COMPUTER-AIDED PROTECTIVE DEVICE
COORDINATION--A CASE STUDY
SCHULTZ D.E., WATERS S.S.
IEEE TRANSACTIONS ON POWER
APPARATUS & SYSTEMS
BJ45,BJ42
E J 1984 V.PAS-103,N.11,P.
CAD, PROGRAM, POWER PLANT,
PROTECTION SYSTEM, APPLICATIONS(ON
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J00003 8504301 J.9.1

计算机辅助工程标准

COMPUTER-AIDED ENGINEERING
STANDARDS
PATTON C.
ELECTRONIC DESIGN
SY02,CD06,BJ42,BJ40,SY03,BJ41
E J 1984 V.32,N.26,P.99-10
CAD, CAM, CAE, CAD LANGUAGE, DATA
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J00004 8504902 J.9.1

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IEEE PROCEEDINGS - PART A:
BJ44,BJ40,SY03,BJ42
E J 1985 V.132-A,N.1,P.15-
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J00005 8505219 J.9.1

在富士通电子元件上精密度进展的CAD系统

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INOUE KAZUYUKI, MAEDA AKIRA ET AL.
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BJ40
J J 1985 V.36,N.3,P.287-29
CAD, APPLICATIONS(ON ENGINEERING),
DATA PROCESSING, SYSTEM
CONFIGURATION, SOFTWARE STRUCTURE,
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J00006 8505755 J.9.1

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ENGINEERING WORKSTATIONS-A TUTORIAL
SAPIRO S.
IEEE INTERNATIONAL CONFERENCE ON
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BJ40
E C 1984 P.2-3
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J00007 8506146 J.9.1

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THE COMPUTER AIDED DESIGN OF
TURBINE BLADES FOR AEROENGINES
ZHAO Y., ZHAN T. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
COMPUTER AND APPLICATIONS
BJ40
E C 1984 P.147-152
CAD, APPLICATIONS(ON ENGINEERING),
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J00008 8506147 J.9.1

数值控制机器工具的模块多处理机控制装置

A MODULAR MULTIMICROPROCESSOR
CONTROL UNIT FOR NUMERICALLY
CONTROLLED MACHINE TOOLS
TURN S., BUDIN L. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
COMPUTER AND APPLICATIONS
BJ40
E C 1984 P.153-159

CAD, APPLICATIONS(ON ENGINEERING),
APPLICATIONS(ON INDUSTRY),
MULTIPROCESSOR CONTROL, STRUCTURE
PROGRAMMING,

SY02,CD06,BJ42,BJ45,BJ40,SY03
E J 1985 V.24,N.6,P.131-13
CAD, DESIGN AUTOMATION SYSTEM, DATA
BASE PERFORMANCE, DATA BASE
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J00009 8508762 J.9.1

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YUTARO HORI, JUSHI IDE ET AL.
THE HITACHI HYORON
BJ42
J J 1985 V.67,N.3,P.51-56
CAD, LOCAL NETWORK, SYSTEM
ARCHITECTURE, SOFTWARE, COMPUTER
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J00010 8508796 J.9.1

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COMPUTER ASSISTED DESIGN OF
SUBSTATION GROUNDING
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IEEE TRANSACTIONS ON POWER
APPARATUS & SYSTEMS
BJ45,BJ42
E J 1985 V.PAS-104,N.7,P.1
CAD, PROGRAM, ENERGY ENGINEERING,
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J00011 8509932 J.9.1

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AND BOARD TEST SYSTEMS
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BJ45,BJ41,BJ40,SY03,BJ42
E J 1984 N.11.1,P.63-69
CAD, AUTOMATIC TEST SYSTEM,
PCB,VLSI, PROGRAMMING, CAE
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J00012 8511430 J.9.1

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SY02,CD06,BJ42,BJ45,BJ40,SY03
E J 1985 V.24,N.6,P.141-14
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AUTOMATIC DESIGN AIDS, CONTROL
LOGIC DESIGN, PROGRAM LIBRARY
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数字集成系统的计算机辅助发展设计

COMPUTER AIDED EVOLUTIONARY DESIGN
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SUSSMAN G.J., HOLLOWAY J. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.612-615
CAD, CAM, DIGITAL IC
DIGITAL INTEGRATED SYSTEM

J00015 8501651 J.9.2

加工模拟程序

UN SIMULATEUR D'USINAGE
BARRIER B.
INTER ELECTRONIQUE
BJ41
F J 1983 N.369, P.35
CAD, CAM, PROCESSOR(HARDWARE),
PROCESSING METHOD, ASSOCIATIVE
PROCESSING, SIMULATION, SIMULATOR
INTERACTIVE PROCESSING

J00016 8501917 J.9.2

用于VLSI和印刷电路打印设计的CAD

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BJ44, BJ41, BJ40
J J 1984 U.25, N.10, P.1078-
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LAYOUT DESIGN, VLSI CAD, PRINTED
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J00017 8501828 J.9.2

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BJ44, BJ41, BJ40
J J 1984 U.25, N.10, P.1161-
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DESIGN, DIGITAL IC, DATA BUS,
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CAD/CAM数据库的发展趋势

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C J 1985 U.7, N.2, P.44-47
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CAD/CAM和生产管理系统

CAD/CAM & PRODUCTION CONTROL SYSTEM

KURIYAMA S., SHIMOSAKON T. ET AL.
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J J 1984 U.5, N.2, P.16-23
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实用CAD/CAM系统的研究

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J J 1984 U.5, N.2, P.10-15
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E J 1984 U.5, N.3, P.205-210
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E J 1984 U.5, N.3, P.239-252
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E J 1983 U.31, N.23, P.165-1
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E J 1983 V.31, N.23, P.93-96
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COMPUTER RECOGNITION AND EXTRACTION
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E J 1984 V.5, N.4, P.329-339
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J00030 8505478 J.9.2

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E J 1983 V.31, N.5, P.145-15
CAD, CAM, SYSTEM DESIGN, CAE
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E J 1984 V.5, N.4, P.341-350
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J J 1985 V.36, N.3, P.237-24
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E J 1984 V.5, N.3, P.221-237
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CD06, BJ44
E J 1985 V.6, N.3, P.173-183
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J00033 8505698 J.9.2

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CAD/CAM AND NEW DESIGN TECHNOLOGIES
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APPLICATIONS
LARSEN R.S.
IEEE TRANSACTIONS ON NUCLEAR
SCIENCE
BJ42*
E J 1985 V.NS-32,N.1,P.316
CAD, CAM, PARTICLE PHYSICS, VLSI,
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J00034 8506254 J.9.2

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J00036 8507136 J.9.2

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CAD ELECTRONIQUE: L'INTEGRATION DES
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MALY W., FIZLO Z.
IEEE TRANSACTIONS ON CAD OF
INTEGRATED CIRCUITS AND SYSTEMS
SY02,BJ42,BJ40
E J 1985 V.CAD-4,N.2,P.156
CAD, CAM, IC TESTING, ASSIGNMENT,
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J00038 8508872 J.9.2

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COMPUTER INTEGRATED MANUFACTURING
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SY02,CD06,BJ45,BJ40,SY03,BJ42
E J 1984 V.30,N.3,P.169-17
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J00039 8509422 J.9.2

空军决策中的兰色高级概念鉴定系统

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KOSTREBA A., ZETANA M. ET AL.
PROCEEDINGS OF THE SUMMER COMPUTER
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BJ40
E C 1984 V.2,P.1179-1182
CAD, CAM, SIMULATION APPLICATION,
SIMULATOR, MILITARY ENGINEERING,
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BJ40
E C 1984 V.2,P.1189-1194
CAD, CAM, SIMULATION APPLICATION,
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MANIP: AN INTERACTIVE MODELLING AND SIMULATION TOOL FOR THE ENGINEER
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BJ40
E C 1984 V.2,P.1201-1203
CAD, CAM, SIMULATION APPLICATION, MODELING, INTERACTIVE SYSTEM, SIMULATOR
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J00042 8509427 J.9.2

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ZEIDNER L.E., HAZONY Y. ET AL.
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BJ40
E C 1984 V.2,P.1204-1209
CAD, CAM, SIMULATION APPLICATION, ARTIFICIAL INTELLIGENCE, SIMULATOR APPLICATIONS(ON BUSINESS MANAGEMENT), SIMULATOR, ROBOT
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MANUFACTURING HEADS TOWARD FULL COMPUTER INTEGRATION
ISM
ELECTRONICS WEEK
BJ45, BJ41, BJ40, SY03, BJ42
E J 1984 N.10.1, P.45-53
LYMAN J., BIERMAN H.

J00044 8511000 J.9.2

用于做童装(女服)的CAD系统

A C.A.D. SYSTEM FOR DRESSMAKING
AGUI TAKESHI, TAHARA KATSUMI ET AL.
TRANSACTIONS OF INFORMATION PROCESSING SOCIETY OF JAPAN
BJ41
J J 1985 V.26,N.3,P.529-53
CAD, CAM, APPLICATIONS(ON INDUSTRY), INFORMATION PROCESSING
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J00045 8501637 J.9.3

CAD公司的发展

CAO A 1' HEURE DE "1' OUVERTURE"
JOLIVET J.P.,
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BJ41
F J 1983 N.365, P.30-33
CAD, COMPUTER ASSISTED CONCEPTION, COMPUTER PERFORMANCE, COMPUTER EFFICIENCY
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J00046 8501696 J.9.3

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DE L'IDEE AU CIRCUIT AVEC UN SEUL LOGICIEL
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BJ41
F J 1983 N.383, P.11
CAD, COMPUTER ASSISTED CONCEPTION, COMPUTER CIRCUIT, DATA BASE LOGICAL ORGANIZATION, IC, CAE

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BJ40
F J 1984 V.64,N.2,P.86-94
CAD, COMPUTER ASSISTED CONCEPTION, TESTING, AUTOMATIQUE TESTING, CAT

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INTER ELECTRONIQUE
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用于VLSI的一个CAD国家程序

POUR LES VLSI UN PROGRAMME NATIONAL
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JOLIVET J.P.,
INTER ELECTRONIQUE
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F J 1984 N.386,P.25
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PROGRAM, VLSI

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一项90亿法郎的计划

CII-HB:9 MILLIARDS POUR UNE
STRATEGIE
JOLIVET J.P.,
INTER ELECTRONIQUE
BJ41
F J 1983 N.367, P.14-15
CAD, COMPUTER AIDED DEVELOPMENT
MINI AND MICROINFORMATION, STRATEGY

J00051 8504215 J.9.4

交换机局设计用的计算机辅助设计系统

A CAD SYSTEM FOR EXCHANGE OFFICE
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KONISHI KAN, ASANUMA TOHRU ET AL,
ELECTRICAL COMMUNICATION
LABORATORIES TECHNICAL JOURNAL
BJ41,BJ40
J J 1984 V.33,N.9,P.2205-2
CAD, COMPUTER AIDED DEVELOPMENT,
SWITCHING CENTER, DATA
COMMUNICATION

J00052 8510117 J.9.4

已测得的高阻抗故障数据同数字计算机模拟结果的比较

A COMPARISON OF MEASURED HIGH
IMPEDANCE FAULT DATA TO DIGITAL
COMPUTER MODELING RESULTS
LEE R.E., BISHOP M.T.,
IEEE TRANSACTIONS ON POWER
APPARATUS & SYSTEMS
BJ45,BJ42
E J 1985 V.PAS-104,N.10,P.
CAD, CAD DEVELOPMENT, SIMULATION,
PROGRAM, DATA COLLECTION, DATA
ANALYSIS

J00053 8501576 J.9.5

接近容限设计的统计调查

THE STATISTICAL EXPLORATION
APPROACH TO TOLERANCE DESIGN
SPENCE R., GEFFERTH L. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.582-585
CAD, CAD SERVICE, ARTIFICIAL
INTELLIGENCE, STATISTICS
TOLERANCE DESIGN

J00054 8501607 J.9.5

模块快速沃尔什-阿达玛变换器件的计算机辅助设计

CAD DESIGN OF A MODULARIZED FAST
WALSH-HADAMART TRANSFORMATION
DEVICE
SMITH E.G.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.916-920
CAD, FAST FOURIER TRANSFORM,
MODULAR, SEMICONDUCTOR DEVICE
FAST W-H TRANSFORM DEVICE

J00055 8501811 J.9.5

硬件描述语言及其应用

HARDWARE DESCRIPTION LANGUAGES AND
THEIR APPLICATIONS
NAKAMURA YUKIHIRO, OGURI KIYOSHI
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1032-
CAD, ALGORITHM, HARDWARE ALGORITHM,
DESCRIPTOR LANGUAGE, COMPUTER
DESIGN, HARDWARE DESCRIPTION
LANGUAGE

J00056 8501883 J.9.5

适用于大批生产的透镜设计程序

COMMERCIALLY AVAILABLE COMPUTERIZED
LENS DESIGN PROGRAMS
LOVELL D.J.
LASER FOCUS
BJ44,BJ41,BJ40,SY03
E J 1984 V.20,N.8,P.98-100
CAD, PROGRAM, CPU, SPECIAL PURPOSE

COMPUTER, PERSONAL COMPUTER,
MICROCOMPUTER, ROUTINE, COMPUTER
GRAPHICS, TERMINAL
SOFTWARE, NEW ALGORITHM

J00057 8504090 J.9.6

使用Prolog分析和重建并行系统

ANALYSIS AND RESTRUCTURING, OF
CONCURRENT SYSTEMS USING PROLOG
HEATH F.G., FOULK P.W., ET AL.
IEEE PROCEEDINGS - PART E:
SY02,CD06,BJ40,SY03,BJ42
E J 1984 V.131,N.5,P.169-1
CAD, CONCURRENT CONTROL SYSTEM,
SYSTEM DESIGN, SYSTEM ANALYSIS, CAD
LANGUAGE

J00058 8501563 J.9.8

CARS: 用递归综合的计算机辅助

CARS: A COMPUTER AID FOR RECURSIVE
SYNTHESIS
MATE L.L., BOHUS M., ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.529-532
CAD, RECURSIVE ALGORITHM,
SYNTHESIS, SYSTEM DESIGN, VLSI
VLSI DESIGN AIDS

J00059 8501816 J.9.8

逻辑综合

LOGIC SYNTHESIS
NANYA TAKASHI
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1071-
CAD, SYNTHESIS, LOGIC DESIGN,
DYNAMIC PROGRAMMING, DATA BUS,
CONTROL SYSTEM CAD, CONTROL LOGIC
DESIGN, LOGIC CIRCUIT
LOGIC SYNTHESIS, CONTROL THEORY

J00060 8501818 J.9.8

分块、赋值和布局算法

PARTITIONING, ASSIGNMENT AND
PLACEMENT ALGORITHMS
KUBO NOBORU, NISHIOKA IKUO
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40

J J 1984 V.25,N.10,P.1084-
CAD, ASSIGNMENT, PROGRAMMING
LANGUAGE, PARTITIONING, DESIGN
ALGORITHM, LSI CAD
PLACEMENT ALGORITHM

J00061 8504866 J.9.8

非线性取优算法用户入门

A USER'S GUIDE TO NONLINEAR
OPTIMIZATION ALGORITHMS
DENNIS J.E.
PROCEEDINGS OF THE IEEE
SY02,CD06,BJ44,BJ45,BJ40
E J 1984 V.72,N.12,P.1765-
CAD, ALGORITHM, OPTIMAL ALGORITHM,
ALGORITHM THEORY, DESIGN ALGORITHM
NONLINEAR OPTIMIZATION ALGORITHM

J00062 8504932 J.9.8

适用于为状态反馈所确定的本征值课题 的数值算法

NUMERICAL ALGORITHMS FOR EIGENVALUE
ASSIGNMENT BY STATE FEEDBACK
PATEL R.V., MISRA P.
PROCEEDINGS OF THE IEEE
SY02,CD06,BJ44,BJ45,BJ40
E J 1984 V.72,N.12,P.1755-
CAD, THEORY OF COMPUTATION SYSTEM,
ALGORITHM, NUMERICAL ANALYSIS,
ALGORITHM ANALYSIS

J00063 8510976 J.9.8

采用逻辑程序设计和归纳推论的智能CAI 系统的研讨

A FRAMEWORK FOR INTELLIGENT CAI
SYSTEMS BASED ON LOGIC PROGRAMMING
AND INDUCTIVE INFERENCE
KAWAI KAZUHISA, MIZOSUCHI RIICHIRO E
TRANSACTIONS OF INFORMATION
PROCESSING SOCIETY OF JAPAN
BJ41
J J 1985 V.26,N.6,P.1089-1
CAD, COMPUTER ASSISTED
PROBLEM-SOLVING SYSTEM, LOGIC CAD,
LOGIC ANALYSIS, PROGRAMMING,
INFERENCE SYSTEM
COMPUTER ASSISTED INSTRUCTION(CAI),
INTELLIGENT CAI SYSTEM

J00064 8501532 J.9.9

LSI设计和测试

LSI DESIGN AND TESTING

KDO J.T.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.245-247
CAD, TESTING, LSI, LSI CAD, LSI
TESTING, LSI TESTING SYSTEM
LSI DESIGN

J00065 8501533 J.9.9

用于VLSI设计的改进工程生产率的技术

TECHNIQUES FOR IMPROVING
ENGINEERING PRODUCTIVITY OF VLSI
DESIGNS
LOGUE J.C., KLEINFELDER J., ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.248-251
CAD, LSI CAD, VLSI, LOGIC DESIGN
VLSI DESIGN

J00066 8501562 J.9.9

GAELIC80—趋近CADC的综合系统

GAELIC 80-THE INTEGRATED SYSTEM
APPROACH TO CADC
LOOSEMORE K.J.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.525-528
CAD, DESIGN AIDS, CIRCUIT CAD,
VLSI, IC, SYSTEM DESIGN
INTEGRATED SYSTEM

J00067 8501571 J.9.9

采用几何描述的IIL的SPEICE电路模拟

SPEICE CIRCUIT SIMULATION OF IIL
USING GEOMETRICAL DESCRIPTION
SMITH K.F., HOWARD W.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.562-566
CAD, CIRCUIT CAD, SIMULATION,
SYSTEM SIMULATION, IIL CIRCUIT,
GEOMETRIC GRAPH
CIRCUIT SIMULATION

J00068 8501572 J.9.9

大型电路和说明展望：最坏条件可能的 临时分布

LARGE CIRCUITS & SPECIFICATION
FORCAST:THE WORST CASE POSSIBLE
TEMPORARY DISTRIBUTION
DELASALLE C.R.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.567-570
CAD, CIRCUIT CAD, LSI
CADC

J00069 8501573 J.9.9

DIGSIM II—电路模拟之一

DIGSIM II---AN IN CIRCUIT
SIMULATION
MAGNHAGEN B.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.571-573
CAD, CIRCUIT CAD, SIMULATION,
SYSTEM SIMULATION, IC
CADC, CIRCUIT SIMULATION

J00070 8501574 J.9.9

预估计VLSI逻辑电路的延时变化的统计分 析技术

STATISTICAL ANALYSIS TECHNIQUE FOR
PREDICTING THE DELAY VARIATION OF
VLSI LOGIC CIRCUITS
CRAFT B.G.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.574-577
CAD, CIRCUIT CAD, VLSI, LOGIC
CIRCUIT, STATISTICS, STATISTICAL
ANALYSIS
CADC, STATISTICAL ANALYSIS TECHNIQUE

J00071 8501575 J.9.9

CAPRICE—用于IC评价的电路分析程序

CAPRICE---CIRCUIT ANALYSIS PROGRAM
FOR INTEGRATED CIRCUIT EVALUATION
HEATLEY R.A., LINGHAM C.J., ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.578-581
CAD, CIRCUIT CAD, ANALYZER, IC,
EVALUATION, COMPUTER AIDED
EVALUATION
CIRCUIT ANALYSIS

J00072 8501578 J.9.9

微单元——一种用户设计单元基系统

MICROCELL---A CUSTOM-DESIGN
CELL-BASED SYSTEM
BATHER K.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.590-593
CAD, DESIGN TECHNIQUE, CUSTOMER
CONTROLLER
MICROCELL, DESIGN CELL

J00073 8501579 J.9.9

双向三稳态的逻辑模拟

LOGIC SIMULATION OF BI-DIRECTIONAL
TRISTATE GATES
FLAKE P.L., MOORBY P.R. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.594-600
CAD, CRICUIT CAD, SIMULATION, LOGIC
GATE, BIDIRECTIONAL OPERATION, LOGIC
CAD
TRI-STATE GATE

J00074 8501593 J.9.9

用于VLSI门阵列的双极电路设计

BIPOLAR CIRCUIT DESIGN FOR VLSI
GATE ARRAYS
DANSKY A.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.674-677
CAD, CIRCUIT CAD, LOGIC DESIGN,
BIPOLAR IC, VLSI, BIPOLAR LOGIC
GATE

J00075 8501687 J.9.9

电子系列所面临的研究项目

FILIERE ELECTRONIQUE: OBJECTIFS
CONFIRMES
BARRIER B.
INTER ELECTRONIQUE
BJ41
F J 1983 N.378, P.16-17
CAD, VLSI, COMPUTER TYPE(BY SIZE),
MICROCOMPUTER

J00076 8501730 J.9.9

VLSI用估障模式及其在芯片设计上作用

FAILURE MODE FOR VLSI & THEIR
EFFECT ON CHIP DESIGN
MANGIR T.E., AVIZIENIS A.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.685-688
CAD, CIRCUIT CAD, VLSI, LOGIC
DESIGN, FAULT MODELING
VLSI CIRCUIT DESIGN

J00077 8501748 J.9.9

单元基IC布线CAD用SIEMENS CALCOS系统

THE SIEMENS CALCOS SYSTEM FOR
COMPUTER AIDED DESIGN OF CELL BASED
IC LAYOUT
LAUTHER U.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.768-771
CAD, CIRCUIT LAYOUT CAD, CIRCUIT
CAD, IC, LOGIC CAD

J00078 8501749 J.9.9

符号MD-MOS电路的自动布线

AUTOMATIC LAYOUT OF SYMBOLIC MD-MOS
CIRCUITS
LELOND A., SERRERO G. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.772-776
CAD, CIRCUIT LAYOUT CAD, MOS IC,
AUTOMATION DESIGN AIDS

J00079 8501810 J.9.9

绪论：逻辑系统的设计工具

INTRODUCTION: DESIGN TOOLS FOR
LOGIC SYSTEMS
MOTO-OKA TOHRU
INFORMATION PROCESSING (JAPAN)
BJ44, BJ41, BJ40
J J 1984 V.25, N.10, P.1026-
CAD, VLSI, LOGIC CAD, LSI CAD,
CIRCUIT CAD, AUTOMATED DESIGN
ENGINEERING

CAD SYSTEM, LOGIC DESIGN AUTOMATION

J00080 8501819 J.9.9

走线方法

ROUTING METHODS
OHTSUKI TATSUO
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1090-
CAD, LSI CAD, PRINTED CIRCUIT
BOARD, ROUTING PROBLEM, DESIGN
AUTOMATION SYSTEM
ROUTING METHOD, PRINTED CIRCUIT
BOARD CAD

J00081 8501820 J.9.9

符号打印设计方法

SYMBOLIC LAYOUT DESIGN METHOD
TAMARU KIICHIRO
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1100-
CAD, LSI CAD, VLSI, SUPPORT
PROGRAM, SYMBOL PLOTTING, CAD
SERVICE, ALGORITHM
COMPRESSION ALGORITHM, LAYOUT
DESIGN CAD, SYMBOLIC LAYOUT DESIGN

J00082 8501821 J.9.9

VLSI打印验证CAD及其算法

VLSI LAYOUT VERIFICATION CAD AND
ITS ALGORITHMS
ISUKIZOE AKIRA, KOZAWA TOKINORI
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1106-
CAD, VLSI, LSI CAD, VERIFICATION,
ALGORITHM, LOGIC CAD
LAYOUT VERIFICATION CAD, VLSI
LAYOUT DESIGN

J00083 8501822 J.9.9

测试与CAD

TEST AND CAD
KINOSHITA KOZO
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1112-
CAD, LOGIC CAD, LSI CAD, TEST, LSI
TESTING
TEST METHOD, TEST COMPLEXITY

J00084 8501825 J.9.9

设计数据库管理

DESIGN DATABASE MANAGEMENT
SUDO TSUNETA, KARATSU OSAMU ET AL.
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1137-
CAD, DATA BASE, LSI CAD, LOGIC CAD,
DATA BASE LOGICAL ORGANIZATION,
DATA BASE MANAGEMENT SYSTEM
DESIGN DATA BASE MANAGEMENT

J00085 8501826 J.9.9

CAD专用机

CAD MACHINE
OHMORI KENJI, KOIKE NOBUHIKO
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1144-
CAD, LOGIC CAD, SIMULATION,
SIMULATION APPLICATION, SPECIAL
PURPOSE COMPUTER, CAD SERVICE
CAD MACHINE, LOGIC SIMULATION

J00086 8501827 J.9.9

硅编译程序

SILICON COMPILER
HIRAYAMA MASAHARU
INFORMATION PROCESSING (JAPAN)
BJ44,BJ41,BJ40
J J 1984 V.25,N.10,P.1153-
CAD, LSI CAD, LOGIC CAD, VLSI,
COMPILER, AUTOMATION DESIGN AIDS
SILICON COMPILER, LOGIC SYNTHESIS,
AUTOMATED LAYOUT

J00087 8501987 J.9.9

LAS: 具有新探讨的电路图图形分析系统

LAS: LAYOUT PATTERN ANALYSIS SYSTEM
WITH NEW APPROACH
KAMURA Y., MURAISHI Y. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.308-311
CAD, CIRCUIT LAYOUT CAD, PCB,
PATTERN, IMAGE ANALYSIS
LAYOUT PATTERN ANALYSIS

J00088 8502007 J.9.9

在自动VLSI线路工艺图设计上结构线组织

STRUCTURED WIRING ORGANIZATION IN
AUTOMATIC VLSI LAYOUT DESIGN
SZEPIENIEC A.A.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.394-397
CAD, CIRCUIT LAYOUT CAD, VLSI
CIRCUIT LAYOUT DESIGN

J00089 8502008 J.9.9

用于相互连接的最佳敷设分配

OPTIMAL LAYER ASSIGNMENT FOR
INTERCONNECT
PINTER R.Y.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.398-401
CAD, CIRCUIT LAYOUT CAD, COMPUTER
CIRCUIT, OPTIMIZATION
OPTIMAL LAYER ASSIGNMENT

J00090 8502012 J.9.9

根据分级路线工艺图方法的逻辑VLSI设计的自动布局和走线程序

AUTOMATIC PLACEMENT AND ROUTING
PROGRAM FOR LOGIC VLSI DESIGN BASED
ON HIERARCHICAL LAYOUT METHOD
TERAI H., HAYASE M. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.415-418
CAD, CIRCUIT LAYOUT CAD, VLSI,
ROUTING PROBLEM, AUTOMATIC DESIGN
AIDS
ROUTING PROGRAM, CIRCUIT AUTOMATIC
PLACEMENT

J00091 8502034 J.9.9

用于用户LSI/VLSI的门阵列片和计算机辅助设计

GATE-ARRAY CHIPS & COMPUTER AIDED
DESIGN METHODS FOR CUSTOM LSI/VLSI
KATZ D.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.207-209
CAD, SEMICONDUCTOR DEVICE, LSI,
VLSI, ARRAY LOGIC

GATE ARRAY

J00092 8502060 J.9.9

用于计算机系统的控制结构的CAD

COMPUTER-AIDED DESIGN OF CONTROL
STRUCTURES FOR DIGITAL COMPUTERS
ZIMMERMAN G.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1980 P.103-106
CAD, CIRCUIT CAD, DIGITAL COMPUTER,
DIGITAL IC, DIGITAL CONTROL
DIGITAL CIRCUIT CAD, CONTROL
STRUCTURE

J00093 8502095 J.9.9

采用分层多细胞探讨16b CPU的设计

A 16B CPU DESIGN BY A HIERARCHICAL
POLYCELL APPROACH
TOKUDA T., OHKURA I. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.102-107
CAD, SYSTEM DESIGN, MICROPROCESSOR,
LSI, CPU
INTEGRATED CAD SYSTEM, CAD SYSTEM

J00094 8502115 J.9.9

在测试中雷德麦彻-沃尔什谱的用途和数字电路的设计

THE USE OF RAMECHER-WALSH
SPECTRUM IN TESTING AND DESIGN OF
DIGITAL CIRCUITS
HSIAO T.-C., SETH S.C.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.202-206
CAD, CIRCUIT CAD, DIGITAL CIRCUIT,
TESTING
CIRCUIT DESIGN

J00095 8502139 J.9.9

VLSI设计输入与分割用辅助工具

AIDS FOR VLSI DESIGN ENTRY AND
PARTITIONING
WISNIEWSKI J.A.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41

E C 1982 P.500-502
CAD, CIRCUIT CAD, DESIGN TECHNIQUE,
VLSI
IC DESIGN, DESIGN ENTRY

J00096 8502141 J.9.9

电路图-81的制作

THE MAKING OF SCHEME-81
SHROBE H.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.507-511
CAD, CIRCUIT CAD, SCHEMA, LOGIC
DESIGN
DESIGN ENTRY

J00097 8502142 J.9.9

用户呼叫和门阵列VLSI设计用的集成设计自动化系统

INTEGRATED DESIGN AUTOMATION SYSTEM
FOR CUSTOM AND GATE ARRAY VLSI
DESIGN
OHNO Y., YAMADA N. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.512-515
CAD, CIRCUIT CAD, VLSI, CAM,
AUTOMATED DESIGN AIDS
CAD SYSTEM

J00098 8502143 J.9.9

综合用户VLSI设计系统

AN INTEGRATED CUSTOM VLSI DESIGN
SYSTEM
SATO K., KAKINUMA M. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.516-519
CAD, CIRCUIT CAD, VLSI, AUTOMATED
DESIGN AIDS
CAD SYSTEM

J00099 8502144 J.9.9

在UNIX操作系统上IC设计用的相互作用数据库管理

INTERACTIVE DATABASE MANAGEMENT FOR
INTEGRATED CIRCUIT DESIGN ON THE
UNIX OPERATING SYSTEM
DE DONCKER E., DEWILDE P. ET AL.
IEEE INTERNATIONAL CONFERENCE ON

CIRCUITS AND COMPUTERS
BJ41

E C 1982 P.520-525
CAD, DATA BASE MANAGEMENT SYSTEM,
OPERATING SYSTEM, CIRCUIT CAD, IC
CAD SYSTEM

J00100 8502145 J.9.9

在相互作用环境中分级分析工具

HIERARCHICAL ANALYSIS TOOLS IN AN
INTERACTIVE ENVIRONMENT
WEISS D.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.526-529
CAD, CIRCUIT CAD, DESIGN AIDS, IC

J00101 8502145 J.9.9

用LSI设计中的CAD软件的开发在晶体管与系统的用途实验

EXPERIENCES IN THE USE OF A
TRANSLATOR WRITING SYSTEM FOR THE
DEVELOPMENT OF CAD SOFTWARE FOR LSI
DESIGN
KJELKERUD E., HEDENGRAN U. ET AL.
IEEE INTERNATIONAL CONFERENCE ON
CIRCUITS AND COMPUTERS
BJ41
E C 1982 P.616-619
CAD, CIRCUIT CAD, SYSTEM DESIGN,
SOFTWARE DEVELOPMENT, LSI, SOFTWARE
DESIGN
CAD SOFTWARE

J00102 8502740 J.9.9

过程自动化系统的计算机辅助设计说明 第二部分

COMPUTER-AIDED SPECIFICATION OF
PROCESS AUTOMATION SYSTEM PART 2
EPPLE W.K.
REGELUNGSTECHNISCHE PRAXIS(RTP)
SY03
D J 1984 V.26,N.4,P.175-18
CAD, PROCESS CONTROL SYSTEM,
SPECIFICATION

J00103 8502741 J.9.9

过程自动化系统的计算机辅助设计说明 第一部分

COMPUTER-AIDED SPECIFICATION OF
PROCESS AUTOMATION SYSTEM PART 1

EPPLÉ W.K.
 REGELUNGSTECHNISCHE PRAXIS(RTP)
 SY03
 D J 1984 V.26,N.3,P.133-13
 CAD, PROCESS CONTROL SYSTEM,
 SPECIFICATION

J00104 8503166 J.9.9

检验MOS电路版图的逻辑模拟

LOGIC SIMULATION FOR CHECKING THE
 MOS CIRCUIT MASK
 JU TI
 MICROELECTRONICS AND COMPUTER
 BJ40*
 C J 1985 V.2,N.1,P.7-11
 CAD, CIRCUIT LAYOUT CAD, IC,
 DIRECTED GRAPH, TRUTH TABLE, DELAY,
 ALGORITHM
 LOGIC SIMULATION

J00105 8503497 J.9.9

计算机辅助生存来完成数字逻辑系统的静态有效运载

A COMPUTER AIDED PROCEDURE FOR
 PERFORMING STATIC LOADING
 VALIDATION OF DIGITAL LOGIC SYSTEMS
 ROSENBERGER F.U., WANN D.F.
 IEEE TRANSACTIONS ON COMPUTERS
 SY02,CD06,BJ42,BJ41,BJ40,SY03
 E J 1984 V.C-33,N.4,P.301-
 CAD, DIGITAL ADDER, LOADING
 PROBLEM, LOGIC CAD, STATIC STORAGE

J00106 8503842 J.9.9

在VSI模型上d次曳行图的埋入面积

THE EMBEDDING AREA OF D-WAY SHUFFLE
 GRAPH ON A VLSI MODEL
 WADA KOICHI, HAGIHARA KEN'ICHI ET AL
 TRANS. INSTITUTE OF ELEC. AND
 COMMUN. ENGINEERS OF JAPAN (D)
 BJ41,BJ40,SY02,CD06
 J J 1985 V.J48-D,N.5,P.101
 CAD, CIRCUIT CAD, CIRCUIT LAYOUT
 CAD, VLSI, SEMICONDUCTOR TECHNOLOGY
 CIRCUIT GRAPH DESIGN

J00107 8503974 J.9.9

利用小型计算机进行SRAM电路分析

CIRCUIT ANALYSIS OF SRAM USING
 MINICOMPUTER

ZHENG MUCAI
 MICROELECTRONICS AND COMPUTER
 BJ40*
 C J 1984 V.1,N.3,P.4-10
 CAD, CIRCUIT-OPTIMIZATION,
 CIRCUIT-ANALYSIS, MINICOMPUTER
 PDP-11

J00108 8504008 J.9.9

电路的计算机辅助设计(五) — 网络的瞬态分析

COMPUTER-AIDED CIRCUIT
 DESIGN(5)--TRANSIENT ANALYSIS
 JU TI
 MICROELECTRONICS AND COMPUTER
 BJ40*
 C J 1984 V.1,N.6,P.36-43
 CAD, LSI CAD, ALGORITHM, RUNGE
 KUTTA METHOD, NUMERICAL INTEGRATION
 TAYLOR ALGORITHM

J00109 8504058 J.9.9

模拟程序推动数字系统设计

SIMULATOR DRIVES DIGITAL DESIGNS
 DOSHI M., SULLIVAN R. ET AL.
 COMPUTER DESIGN
 SY02,CD06,BJ42,BJ45,BJ40,SY03
 E J 1984 V.23,N.11,P.213-2
 CAD, SIMULATOR, LOGIC CAD, LOGIC
 CIRCUIT, DESIGN SIMULATION

J00110 8504113 J.9.9

采用CAE工作站的交互式电子电路模拟系统

INTERACTIVE ELECTRONIC-CIRCUIT
 SIMULATION USING A CAE WORKSTATION
 SHINTARO SHIMPO, MASAHIRO MURAI ET AL
 MITSUBISHI DENKI GIHO
 BJ42*
 J J 1985 V.59,N.2,P.19-22
 CAD, ELECTRONIC CIRCUIT, IC, LSI,
 SIMULATION SYSTEM, CAE
 WORK STATION

J00111 8504150 J.9.9

供大学生设计练习用的IIL时钟门阵列

AN IIL CLOCKED GATE ARRAY FOR
 UNDERGRADUATE DESIGN EXERCISES
 JESSHOPE C.R., ASHBURN P.
 IEE PROCEEDINGS - PART E:
 SY02,CD06,BJ40,SY03,BJ42
 E J 1985 V.132-E,N.2,P.54-