AND NONLINEAR CIRCUITS

Leon O. Chua Charles A. Desoer Ernest S. Kub

LINEAR AND NONLINEAR CIRCUITS

Leon O. Chua
Charles A. Desoer
Ernest S. Kuh
University of California—Berkeley

McGraw-Hill Book Company

New York St. Louis San Francisco Auckland Bogotá Hamburg Johannesburg London Madrid Mexico Milan Montreal New Delhi Panama Paris São Paulo Singapore Sydney Tokyo Toronto This book was set in Times Roman by Intercontinental Photocomposition Limited.

The editors were Sanjeev Rao and Alar E. Elken; the cover was designed by Scott Chelius; the production supervisor was Diane Renda.

Project supervision was done by The Total Book.

R. R. Donnelley & Sons Company was printer and binder.

LINEAR AND NONLINEAR CIRCUITS

Copyright © 1987 by McGraw-Hill, Inc. All rights reserved. Printed in the United States of America. Except as permitted under the United States Copyright Act of 1976, no part of this publication may be reproduced or distributed in any form or by any means, or stored in a data base or retrieval system, without the prior written permission of the publisher.

1234567890DOCDOC89210987

ISBN 0-07-010898-6

Library of Congress Cataloging-in-Publication Data

Chua, Leon O. (date)

Linear and nonlinear circuits.

(McGraw-Hill series in electrical engineering. Circuits and systems)

Includes bibliographical references and index.

1. Electric circuits, Linear. 2. Electric circuits, Nonlinear. I. Desoer, Charles A. II. Kuh, Ernest S. III. Title. IV. Series.

TA454.C56 1987 621.319'21 86-12498

ISBN 0-07-010898-6

ISBN 0-07-010899-4 (solutions manual)

ABOUT THE AUTHORS

Leon O. Chua received the S.M. degree from Massachusetts Institute of Technology in 1961 and his Ph.D. from the University of Illinois, Urbana in 1964. He was awarded Doctor Honoris Causa from the Ecole Polytechnique Fédérale de Lausanne (Switzerland) in 1983 and an Honorary Doctorate from the University of Tokushima (Japan) in 1984. In 1971 he joined the faculty of the University of California, Berkeley, where he is currently Professor of Electrical Engineering and Computer Sciences. Dr. Chua is the author of Introduction to Nonlinear Network Theory and coauthor of Computer-Aided Analysis of Electronic Circuits: Algorithms and Computational Techniques. His research interests are in the areas of general nonlinear network and system theory. He has published numerous research papers and has been a consultant to various electronic industries.

Dr. Chua is a Fellow of the IEEE (1973), the Editor of the IEEE Transactions on Circuits and Systems (1973-75), and the President of the IEEE Circuits and Systems Society (1976). He has been awarded four patents and received several honors and awards, including the IEEE Browder J. Thompson Prize (1967), the IEEE W.R.G. Baker Prize (1973), the Frederick Emmons Terman Award (1974), the Miller Professor (1976), the Senior Visiting Fellow at Cambridge University (1982), the Alexander von Humboldt Senior U.S. Scientist award (1983) at the Technische Universität (München), the Japan Society for Promotion of Science Senior Visiting Fellowship (1983) at Waseda University (Tokyo), the IEEE Centennial Medal (1984), the Guillemin-Cauer Prize (1985), and the Professeur Invité award (Université de Paris-SUD) from the French National Ministry of Education (1986).

Charles A. Desoer received his Ph.D. from the Massachusetts Institute of Technology in 1953. He then worked for several years at Bell Telephone Laboratories on circuit and communication system problems. In 1958 he joined the Department of Electrical Engineering and Computer Sciences at the University of California, Berkeley, where he has taught and researched in the areas of circuits, systems, and control.

Dr. Desoer has received a number of awards for his research, including the Medal of the University of Liege (1970), the Guggenheim Fellowship (1970–1971), the Prix Montefiore (1975), and the 1986 IEEE Control Systems Science and Engineering Award. He received the 1971 Distinguished Teaching Award at the University of California, Berkeley and the 1975 IEEE Education Medal. Dr. Desoer's interests lie in system theory with emphasis on control systems and circuits. He has coauthored several books, including Linear System Theory with L. A. Zadeh, Basic Circuit Theory with E. S. Kuh, and Multivariable Feedback Systems with F. M. Callier. He is author of Notes for a Second Course on Linear Systems.

Ernest S. Kuh is author of over 80 technical papers in circuits, systems, electronics, and computer-aided design, and is a coauthor of three books, including *Basic Circuit Theory*, which he wrote with one of his present coauthors, Charles A. Desoer. He received the B.S. degree from the University of Michigan, Ann Arbor in 1949, the S.M. degree from the Massachusetts Institute of Technology in 1950, and the Ph.D from Stanford University in 1952.

Dr. Kuh was a member of the Technical Staff of Bell Telephone Laboratories from 1952 until 1956. He joined the faculty of the University of California, Berkeley in 1956, and there, he served as Chairman of the Department of Electrical Engineering and Computer Sciences from 1968 to 1972. From 1973 to 1980 he was Dean of the College of Engineering. Dr. Kuh is a Fellow of IEEE and AAAS and a member of the National Academy of Engineering and of the Academia Sinica. He has been the recipient of a number of awards and honors, including the Lamme Medal of the American Society of Engineering Education, the IEEE Education Medal, and the IEEE Centennial Medal. He has served on several committees of the National Academy of Engineering and the National Academy of Sciences.

Electrical engineering is a discipline driven by inventions and technological breakthroughs. To mention one: 20 years ago, engineers barely knew how to produce an IC chip: now some chips have one million devices; it is expected that with foreseeable developments in silicon technology during the next decade chips will have 10⁸ devices. Also the ubiquitous presence of the computer terminal reminds us of the enormous impact of computers on engineering design. Clearly such tremendous changes would have considerable influence on engineering education.

In teaching a course on introductory electrical circuits, the traditional approach has been to teach exclusively linear time-invariant passive RLC circuits. Admittedly they constitute a good vehicle to learn the dynamics of

such simple circuits. Clearly such an approach is obsolete.

It is clear that circuit theory is one of the basic disciplines of electrical engineering; a well designed circuit theory course should cover the basic concepts and the basic results used in circuit design. It should serve as a foundation course to be followed by courses in various fields of electrical engineering, e.g., communication and signal processing, electronic devices and circuits, control and power systems, microwaves and optoelectronics, etc. . . . The concept of device modeling and its applications to currently used devices are crucial in a course on linear and nonlinear circuits: many examples of device modeling are given in the text. Furthermore, the course should be designed so that the graduate from such a curriculum knows how to approach the devices and circuits yet to be invented but that he or she will encounter, say, 10 to 15 years from now. With these goals in mind, the present book presents material with sufficient breadth, depth, and rigor to give a solid foundation to the student's future professional life.

At the University of California, Berkeley, as in most American engineering schools, there is a sophomore 45-lecture-hour course called Introductory Electrical Engineering. Its purpose is to give a broad introduction to most of the aspects of electrical engineering.

This book is intended as a textbook for the junior course that follows.

Since it is a junior course, it takes advantage of the greater competence and maturity of the students: in particular, physics, linear algebra, and differential equations. This course is the electrical analog of the typical junior physics course in say, mechanics, electromagnetism, and so on.

This book differs from many other texts on circuit theory by the following features:

- 1. Due to the ubiquitous op amp and similar devices, it views a circuit as an interconnection of *multiterminal* elements rather than of *two-terminal* elements.
- 2. Active and passive circuits are given equal emphasis.
- 3. Linear and nonlinear elements are treated together. (Note that computers simulate nonlinear circuits almost as easily as linear ones.)
- 4. The concept of *operating point* and the topic of *small-signal analysis* are covered thoroughly.
- 5. Switching, triggering, and memory circuits as well as oscillators are illustrated with first-order and second-order examples.
- 6. Tableau analysis is used to greatly simplify the proof of many network theorems in linear and nonlinear circuits.
- 7. Modified node analysis is introduced in view of its complete generality and importance in the design of computer circuit simulators, such as SPICE.
- 8. Some numerical methods are introduced and implemented via equivalent circuits: in particular, solution of nonlinear algebraic equations (Newton-Raphson) and integration of the circuit differential equations (forward and backward Euler method).
- 9. Stability issues are met head on; in particular oscillators are analyzed and an elementary version of the Nyquist criterion (useful in the design of op amps circuits) is introduced.

CONTENTS OF THE BOOK

Chapter One treats Kirchhoff's Laws and Tellegen's theorem. The next four chapters introduce two-terminal and multiterminal resistive elements and resistive circuits; linear, nonlinear, passive, and active circuits; op amp circuits with linear and nonlinear models; operating points and small signal analysis; and network theorems and the Newton-Raphson procedure for solving nonlinear dc resistive circuits.

Chapters Six and Seven cover first- and second-order linear and nonlinear dynamic circuits: our goal is to exhibit their properties and illustrate them by numerous examples, including flip flops and oscillators. General dynamic circuits, analyzed by Tableau or modified node analysis, are covered in Chapter Eight.

The next three chapters build up the fundamentals of linear time-invariant

circuits: sinusoidal steady-state analysis; very brief treatment of Laplace transforms; properties of natural frequencies and network functions such as poles, zeros, stability, and convolution; Nyquist criterion; and stability of terminated one-ports.

A brief Chapter Twelve broadens the background on network topology and treats the usual general circuit analysis methods.

Chapter Thirteen covers two-ports, n-ports, and their properties—reciprocity in particular.

The last chapter brings out design issues such as the approximation problem, design of active Butterworth filters, and sensitivity analysis.

We believe that the topics covered in this text constitute an excellent background for further education in electronic circuits, computer-aided design, communications, control, and power.

ACKNOWLEDGMENT

Even though this book is a systematic introduction to circuit theory, it uses many concepts and techniques which were developed by people doing research in circuits, communications and control. In fact, without our own deep involvement in research, this book could not have been written. It is a pleasure to publicly acknowledge the research support of the University of California, the National Science Foundation, the Department of Defense, the State of California, and the support of industry.

We are also indebted to many people who have taught the course while the preliminary notes were written and revised; they have given us valuable suggestions. It is a pleasure to mention in particular F. Ayrom, G. Bernstein, C. C. Chang, A. C. Deng, A. Dervisoglu, N. A. Grundes, H. Haneda, K. Inan, P. Kennedy, R. W. Liu, H. Narayanan, M. Odyniec, T. S. Parker, A. Sangiovanni-Vincentelli, E. W. Szeto-Lee, L. Yang, and Quing-jian Yu.

We also benefited greatly from the judicious comments of our teaching assistants and the penetrating questions of our students.

We wish to record our gratitude for the high professional skills and care that B. Fuller, I. Stanczyk-Ng, and T. Sticpewich gave to the preparation of the manuscript.

Leon O. Chua Charles A. Desoer Ernest S. Kuh

CONTENTS

| Preface | xix |
|--|----------|
| 1 Kirchhoff's Laws | 1 |
| 1 The Discipline of Circuit Theory | 1 |
| 2 Lumped-Circuit Approximation | 2 |
| 3 Electric Circuits, Models, and Circuit Elements | 4 |
| 4 Kirchhoff's Laws | 6 |
| 4.1 Reference Directions | 6 |
| 4.2 Kirchhoff's Voltage Law (KVL) | 7 |
| 4.3 Kirchhoff's Current Law (KCL) | 10 |
| 4.4 Three Important Remarks | 12 |
| 5 From Circuits to Graphs | 12 |
| 5.1 The Element Graph: Branch Currents, Branch Voltages, and the | 10 |
| Associated Reference Directions | 13 |
| 5.2 The Circuit Graph: Digraph | 16 |
| 5.3 Two-Ports, Multiports, and Hinged Graphs | 18 21 |
| 5.4 Cut Sets and KCL | 21 |
| 6 Matrix Formulation of Kirchhoff's Laws | 23 |
| 6.1 Linear Independence | 23 |
| 6.2 Independent KCL Equations | 26 |
| 6.3 Independent KVL Equations | 27 |
| 7 Tellegen's Theorem | 27 |
| 7.1 Theorem, Proof, and Remarks | 29 |
| 7.2 Tellegen's Theorem and Conservation of Energy | 30 |
| 7.3 The Relation between Kirchhoff's Laws and Tellegen's Theorem | 31 |
| 7.4 Geometric Interpretation | 34 |
| Summary | . 34 |
| 2 Two-Terminal Resistors | 45 |
| 1 v-i Characteristic of Two-Terminal Resistors | 46 |
| | 46 |
| 1.1 From Linear Resistor to Resistor 1.2 The Nonlinear Resistor | 50 |

X CONTENTS

| 1.2 T1 | |
|--|-------|
| 1.3 Independent Sources | 56 |
| 1.4 Time-Invariant and Time-Varying Resistors | 59 |
| 2 Series and Parallel Connections | 61 |
| 2.1 Series Connection of Resistors | 62 |
| 2.2 Parallel Connection of Resistors | 67 |
| 2.3 Series-Parallel Connection of Resistors | 73 |
| 3 Piecewise-Linear Techniques | 76 |
| 3.1 The Concave and Convex Resistors | 78 |
| 3.2 Approximation and Synthesis | 80 |
| 4 dc Operating Points | 83 |
| 5 Small-Signal Analysis | 92 |
| 6 Transfer Characteristics | 96 |
| Summary | 100 |
| | |
| 3 Multiterminal Resistors | 116 |
| 1 Resistive Two-Ports | 117 |
| 1.1 A Linear Resistive Two-Port Example | 118 |
| 1.2 Six Representations | 119 |
| 1.3 Physical Interpretations | 121 |
| 2 Useful Resistive Two-Ports | 126 |
| 2.1 Linear Controlled Sources | 126 |
| 2.2 Ideal Transformer | 130 |
| 2.3 Gyrator | 132 |
| 3 Nonlinear Resistive Two-Ports | 133 |
| 4 The npn Bipolar Transistor | 137 |
| 4.1 v-i Characteristics and Modeling | 137 |
| 4.2 dc Operating Points and Double Load Lines | 142 |
| 4.3 Small-Signal Analysis | 145 |
| 5 The MOS Transistor and the Basic Inverter | 150 |
| 6 Multiport and Multiterminal Resistors | 155 |
| 6.1 The Three-Port Ideal Transformer | 155 |
| 6.2 The Three-Port Circulator | 156 |
| 6.3 Analog Multiplier | 158 |
| Summary | 159 |
| | |
| 4 Operational-Amplifier Circuits | 171 |
| 1 Device Description, Characteristics, and Model | 171 |
| 2 OP-AMP Circuits Operating in the Linear Region | 177 |
| 2.1 Virtual Short Circuits | 177 |
| 2.2 Inspection Method | . 178 |
| A Voltage Follower (Buffer) | 178 |
| B Inverting Amplifier | 179 |
| C Noninverting Amplifier | 180 |
| D Resistance Measurement without Surgery | 181 |
| E Nonlinear Feedback | 182 |
| 2.3 Systematic Method | 184 |

| 3 OP-AMP Circuits Operating in the Nonlinear Region | 187 |
|--|-------|
| 3.1 + Saturation and - Saturation Equivalent Circuits | 187 |
| 3.2 Inspection Method | 188 |
| A Comparator (Threshold Detector) | 188 |
| B Negative vs. Positive Feedback Circuit | 190 |
| C Negative-Resistive Converter | 192 |
| D Concave and Convex Resistors | 195 |
| 3.3 Systematic Method | 199 |
| 4 Comparison with Finite-Gain Model | 200 |
| Summary | 202 |
| 5 General Resistive Circuits | 213 |
| | 214 |
| 1 Node Analysis for Resistive Circuits | 215 |
| 1.1 Node Equation Formulation: Linear Resistive Circuits | |
| 1.2 Linear Circuits Containing Two-Terminal Resistors and | 221 |
| Independent Sources | 222 |
| 1.3 Existence and Uniqueness of Solution | 223 |
| 1.4 Node Equation Formulation: Nonlinear Resistive Circuits | 225 |
| 2 Tableau Analysis for Resistive Circuits | 226 |
| 2.1 Tableau Équation Formulation: Linear Resistive Circuits2.2 Tableau Equation Formulation: Nonlinear Resistive Circuits | 230 |
| 3 Computer-Aided Solution of Nonlinear Algebraic Equations | 232 |
| 3 Computer-Aided Solution of Normical Algebraic Equations | 233 |
| 3.1 The Newton-Raphson Algorithm | 236 |
| 3.2 Newton-Raphson Discrete Equivalent Circuit | 243 |
| 4 General Properties of Linear Resistive Circuits | 244 |
| 4.1 Superposition Theorem 4.2 The Thévenin-Norton Theorem | 251 |
| | 259 |
| 4.3 Two-Port Representation Theorem5 General Properties of Nonlinear Resistive Circuits | 266 |
| | 267 |
| 5.1 Substitution Theorem | 269 |
| 5.2 Loop-Cut-Set Exclusion Property | 271 |
| 5.3 Consequences of Strict Passivity | 276 |
| 5.4 Consequences of Strict Monotonicity Summary | 279 |
| | 295 |
| 6 First-Order Circuits | |
| 1 Two-Terminal Capacitors and Inductors | 295 |
| 1.1 q - v and ϕ - i Characteristics | 297 |
| 1.2 Time-Varying Capacitors and Inductors | 303 |
| 2 Basic Properties Exhibited by Time-Invariant Capacitors and Inductors | 306 |
| 2.1 Memory Property | 306 |
| 2.2 Continuity Property | 309 |
| 2.3 Lossless Property | 312 |
| 2.4 Energy Stored in a Linear Time-Invariant Capacitor or Inductor | 316 |
| 2.5 Energy Stored in a Nonlinear Time-Invariant Capacitor or Inductor | . 317 |

xii CONTENTS

| 0.70 | |
|--|-----|
| 3 First-Order Linear Circuits | 320 |
| 3.1 Circuits Driven by dc Sources | 321 |
| A Properties of Exponential Waveforms | 322 |
| B Elapsed Time Formula | 327 |
| C Inspection Method (First-Order Linear Time-Invariant Circuits | 321 |
| = · · · · · · · · · · · · · · · · · · · | 327 |
| 3.2 Circuits Driven by Piecewise-Constant Signals | 329 |
| 3.5 Linear Time-Invariant Circuits Driven by an Impulsi- | 334 |
| 5.4 Circuits Driven by Arbitrary Signals | 336 |
| 4 First-Order Linear Switching Circuits | 339 |
| 5 First-Order Piecewise-Linear Circuits | 340 |
| 5.1 The Dynamic Route | 341 |
| 5.2 Jump Phenomenon and Relaxation Oscillation | 344 |
| 3.3 ringgering a Bistable Circuit (Flip-Flop) | |
| Summary | 347 |
| | 350 |
| • | |
| 7 Second-Order Circuits | |
| 1 Equation Formulation: Linear Time-Invariant Circuits | 363 |
| 1.1 Two Standard Forms: $\ddot{x} + 2\alpha \dot{x} + \omega_0^2 x = u_s(t)$ and $\dot{x} = Ax + u(t)$ | 364 |
| 1.2 State Equation and State Variables | 364 |
| 1.3 Linear State Equation Formulation | 368 |
| A Two-Capacitor Configuration | 371 |
| B Two-Inductor Configuration | 371 |
| C Capacitor-Inductor Configuration | 373 |
| 2 Zero-Input Response | 373 |
| 2.1 Determining Zero Input Possesses for T. 2 | 374 |
| 2.1 Determining Zero-Input Response from $\ddot{x} + 2\alpha \dot{x} + \omega_0^2 x = 0$; $\alpha \ge 0$ and $\omega_0^2 > 0$ | |
| A Determination of Arbitrary Constants | 375 |
| B Physical Interpretation via Parallel RLC Circuits | 378 |
| C Quality Factor | 378 |
| 2.2 Determining Zero Input Domanus C | 381 |
| 2.2 Determining Zero-Input Response from $\dot{\mathbf{x}} = \mathbf{A}\mathbf{x}$; $\Delta \neq \frac{1}{4}T^2$ 3 Qualitative Behavior of $\dot{\mathbf{x}} = \mathbf{A}\mathbf{x}$ | 383 |
| 3.1 Two Distinct Peal Figurestrees A = 1.772 | 386 |
| 3.1 Two Distinct Real Eigenvalues: $\Delta < \frac{1}{4}T^2$, $\Delta \neq 0$ (Equivalently, $\alpha^2 > \omega_0^2$, $\omega_0^2 \neq 0$) | |
| A Stable Node: Two Pool Nove: \mathbf{F} | 388 |
| A Stable Node: Two Real Negative Eigenvalues $(s_2 < s_1 < 0; Equivalently, \alpha > \omega_0 > 0)$ | |
| B Unstable Node: Two Book Books | 389 |
| B Unstable Node: Two Real Positive Eigenvalues $(0 < s_2 < s_1)$; Equivalently, $\alpha < -\omega_0 < 0$ | |
| $\omega = \omega_0 > 0$ | 390 |
| C Saddle Point: One Negative and One Positive Eigenvalue | |
| $(s_2 < 0 < s_1; Equivalently, \omega_0^2 < 0)$ | 391 |
| 3.2 Two Complex-Conjugate Eigenvalues: $\Delta > \frac{1}{4}T^2$ (Equivalently, $\alpha^2 < \omega_0^2$) | 392 |
| A Center: Two Imaginary Eigenvalues (Equivalently, $\alpha = 0$, $\omega_d = \omega_0$) | 393 |
| B Stable Focus: Two Complex-Conjugate Eigenvalues with a | |
| Negative Real Part (Equivalently, $\alpha > 0$ and $\alpha^2 < \omega_0^2$) | 395 |
| C Unstable Focus: Two Complex-Conjugate Eigenvalues with a | |
| Positive Real Part (Equivalently, $\alpha < 0$ and $\alpha^2 < \omega_0^2$) | 396 |
| 3.3 Summary of Equilibrium State Classification | 397 |
| 4 Nonlinear State Equation Formulation | 399 |

| 4.1 Tunnel Diode and Josephson Junction Circuits | 400 |
|--|-----|
| 4.2 How to Write Nonlinear State Equations | 402 |
| 5 Qualitative Behavior of $\dot{\mathbf{x}} = \mathbf{f}(\mathbf{x})$ | 407 |
| 5.1 Phase Portrait | 409 |
| 5.2 Equilibrium States and Operating Points | 417 |
| 5.3 Qualitative Behavior Near Equilibrium States | 421 |
| 6 Nonlinear Oscillation | 425 |
| 6.1 Basic Negative-Resistance Oscillator | 426 |
| 6.2 Physical Mechanisms for Oscillation | 428 |
| 6.3 Phase Portrait of Typical Oscillators | 431 |
| A Linear Oscillator | 431 |
| B Van der Pol Oscillator | 432 |
| C Jump Phenomenon Revisited | 436 |
| Summary | 439 |
| | |
| 8 General Dynamic Circuits | 453 |
| • | 433 |
| 1 Coupled Inductors | 454 |
| 1.1 Linear Time-Invariant Coupled Inductors | 454 |
| A Characterization | 454 |
| B Stored Energy | 456 |
| C Sign of M | 458 |
| D More Than Two Inductors | 459 |
| E Relation with Ideal Transformers | 460 |
| 1.2 Nonlinear Time-Invariant Coupled Inductors | 461 |
| 2 Tableau Analysis | 462 |
| 2.1 Linear Dynamic Circuits | 462 |
| A Linear Time-Invariant Circuits | 462 |
| B Linear Time-Varying Circuit | 464 |
| 2.2 Nonlinear Dynamic Circuits | 466 |
| 3 Modified Node Analysis | 468 |
| 4 Small-Signal Analysis | 472 |
| 5 General Properties of Dynamic Circuits 5 1 Superposition Theorem for Linear Dynamic Circuits | 481 |
| 5.1 Superposition Theorem for Linear Dynamic Circuits | 482 |
| 5.2 Substitution Theorem for Dynamic Circuits 6 Numerical Solution of Circuit Equations | 486 |
| 6 Numerical Solution of Circuit Equations 6.1 The Forward Euler Method | 488 |
| 6.2 The Backward Euler Method | 489 |
| 6.3 The Backward Euler Method Applied to Circuit Equations | 491 |
| Summary | 493 |
| Summary | 496 |
| | |
| 9 Sinusoidal Steady-State Analysis | 505 |
| Introduction | 505 |
| 0 Review of Complex Numbers | 506 |
| Operations with Complex Numbers | 507 |
| 1 Phasors and Sinusoidal Solutions | 508 |
| 1.1 Sinusoids and Phasors | 508 |
| 1.2 Three Lemmas | 509 |
| 1.3 Example of Sinusoidal Steady-State Solution | 512 |

xiv CONTENTS

| 514 514 514 515 516 517 520 522 522 522 523 524 |
|--|
| 514 515 516 517 520 522 522 523 524 |
| 515 516 517 520 522 522 522 523 524 |
| 516 517 520 522 522 523 524 |
| \$17 520 522 522 522 523 524 |
| 520 522 522 523 524 |
| 522 522 523 524 |
| 522 523 524 |
| 523 524 |
| 524 |
| |
| 524 |
| J24. |
| 528 |
| 531 |
| 531 |
| 533 |
| 535 |
| 535 |
| 536 |
| 536 |
| 536 |
| 538 |
| 540 |
| 542 |
| 542 |
| 542 |
| 544 |
| 545 |
| 545 |
| 546 |
| 547 |
| 548 |
| 549 |
| 550 |
| ver 552 |
| 554 |
| 554 |
| 555 |
| 557 |
| 559 |
| 560 |
| 562 |
| |
| 575 |
| 575 |
| 576 |
| 576 |
| 578 |
| |

| 2 Four Basic Prop | perties of Laplace Transforms | | 581 |
|--------------------|--|------|-----|
| 2.1 Uniqueness | | | 581 |
| 2.2 Linearity Pr | | | 581 |
| 2.3 Differentiat | | | 583 |
| A Property | | | 583 |
| 2.4 Integration | | | 585 |
| | ansform Rules and Phasor Calculation Rules | | 585 |
| 3 Partial Fraction | | | 588 |
| 3.1 Reduction S | | | 588 |
| 3.2 Simple Pole | | | 589 |
| 3.3 Multiple Po | | | 590 |
| | Circuit Analysis Using Laplace Transform | | 591 |
| | inear Time-Invariant Circuits | | 593 |
| | justions in the Frequency Domain | | 593 |
| | Transform of Kirchhoff's Laws | | 593 |
| | Transform of the Branch Equations | | 594 |
| | leau Equations in the Frequency Domain | | 594 |
| D Two Day | operties of Linear Time-Invariant Circuits | | 595 |
| | | | 596 |
| | ode Analysis in the Frequency Domain | | 600 |
| | Response and Natural Frequencies | | 600 |
| | eristic Polynomial and Natural Frequencies | . 30 | 601 |
| | Interpretation of Natural Frequencies | | 605 |
| | o-Input Response | | 609 |
| | Response, Network Functions, and Impulse Response | | 610 |
| A Network | | | 612 |
| | on Cancellations | | 613 |
| | Response and Network Functions | | 615 |
| | tal Theorem of the Sinusoidal Steady State | | 619 |
| 6 Convolution | | | 619 |
| | g Interpretation of Convolution | | 619 |
| | nvolution Operation | | 620 |
| B The Co | nvolution Integral | | 62Ů |
| | f Eq. (6.5) Based on Linearity and Time-Invariance | | |
| D Graphic | cal Interpretation: Flip and Drag | | 623 |
| E Example | | | 624 |
| | -Time of a Circuit | | 626 |
| | olution Theorem | | 626 |
| 6.3 The Sinus | oidal Steady State Analyzed by Convolution | | 627 |
| Summary | | | 629 |
| | | | |
| 11 Network | Functions and Stability | | 644 |
| | | | 644 |
| Introduction | and Dela Zano | • | 644 |
| | ase and Pole Zeros | | 645 |
| 1.1 First-Orde | | | 645 |
| A Analysi | | | 646 |
| | ude and Phase Curves | | 648 |
| C Bode F | lots - | | 040 |

xvi CONTENTS

| 650 |
|-----|
| 651 |
| 652 |
| 655 |
| 658 |
| 660 |
| 661 |
| 665 |
| 667 |
| 671 |
| 673 |
| 675 |
| 677 |
| 678 |
| 679 |
| 682 |
| 683 |
| 683 |
| 684 |
| 686 |
| 000 |
| 695 |
| 696 |
| 697 |
| 698 |
| 700 |
| 700 |
| 702 |
| 702 |
| 705 |
| 705 |
| 707 |
| 710 |
| 714 |
| 715 |
| 719 |
| 719 |
| 719 |
| 722 |
| 724 |
| 726 |
| 727 |
| 731 |
| .51 |
| 740 |
| 742 |
| 742 |
| 744 |
| |

CONTENTS XVII

| 2 Linear Time-Invariant Two-Ports | |
|--|-------------|
| 2.1 The Impedance and Admittance Matrices | 747 |
| 2.2 Hybrid Matrices | 752 |
| 2.3 The Transmission Matrices | 754 |
| 3 Terminated and Interconnected Two-Ports | 756 |
| 3.1 Terminated Two-Ports | 756 |
| 3.2 Interconnected Two-Ports | 758 |
| 4 Multiports and Multiterminal Circuits | 762 |
| 4.1 n-Port Characterization | 762 |
| 4.2 The Indefinite Admittance Matrix | 765 |
| 5 The Soldering Iron Entry and Pliers Entry | 770 |
| 6 The Reciprocity Theorem | <i>7</i> 71 |
| Summary | 77 9 |
| 14 Design and Sensitivity | 790 |
| Introduction | 790 |
| | 790 |
| 1 Simple Low-Pass Filter Design | 791 |
| 1.1 The Butterworth Approximation | 795 |
| 1.2 Synthesis of All-Pole Transfer Functions | 801 |
| 1.3 Renormalization | 801 |
| A Magnitude Scaling | 801 |
| B Frequency Scaling | 802 |
| C Impedance Scaling | 803 |
| 2 Sensitivity Analysis | 805 |
| 2.1 Explicit Sensitivity Formulas | 805 |
| A In Terms of Network Function Components | 808 |
| B In Terms of the Node Admittance Matrix | 813 |
| 2.2 Calculating Sensitivity Via the Adjoint Equation | . 814 |
| A Physical Interpretation of the Adjoint Equation | 815 |
| B Calculating δE_n in Terms of E and E ^a | 818 |
| C LU Decomposition Method for Solving E and E ^a Summary | 819 |
| | |
| Glossaries | . 826 |
| A Symbols | = |
| B Notation | . 828 |
| Index | 83 |