# Neural and Concurrent Real-Time Systems

The Sixth Generation

**BRANKO SOUČEK** 

## Neural and Concurrent Real-Time Systems

## The Sixth Generation

### **BRANKO SOUČEK**

Department of Mathematics University of Zagreb





A Wiley-Interscience Publication

**JOHN WILEY & SONS** 

New York / Chichester / Brisbane / Toronto / Singapore

Copyright © 1989 by John Wiley & Sons, Inc.

All rights reserved. Published simultaneously in Canada.

Reproduction or translation of any part of this work beyond that permitted by Section 107 or 108 of the 1976 United States Copyright Act without the permission of the copyright owner is unlawful. Requests for permission or further information should be addressed to the Permissions Department, John Wiley & Sons, Inc.

#### Library of Congress Cataloging in Publication Data:

Souček, Branko.

Neural and concurrent real-time systems: the sixth generation/ Branko Souček.

p. cm.

"A Wiley-Interscience publication." Includes bibliographies and index.

ISBN 0-471-50889-6

1. Neural computers. 2. Real-time data processing.

I. Title.

QA76.5.S6572 1989 006.3—dc19

89-5682

CIP

Printed in the United States of America

10 9 8 7 6 5 4 3 2 1

### **PRFFACE**

Thanks to advances in artificial neural networks, expert systems, and concurrent processors, it is now possible to build *intelligent* real-time systems, which belong to the sixth-generation technology. Intelligent real-time systems described in this book cover the following applications: intelligent instruments, data acquisition, signal and speech processing, computer vision and graphics, process control and manufacturing inspection, intelligent robots, decision-making systems, on-line simulators, transputer-based superclusters, flexible factory cells, and compter integrated manufacturing.

The built-in neural and concurrent processors and knowledge bases provide these systems with a set of new features: learning and self-organization, fuzzy reasoning, very high-speed signal processing and process control, real-time recognition and classification of patterns in signals and images, high reliability due to the dispersed storage, and a very high performance/price ratio. As a result, intelligent real-time systems present a rapidly growing technology.

The book is divided into two parts:

Part I: INTELLIGENT PROCESSES (Chapters 1 to 5)
Part II: INTELLIGENT SYSTEMS (Chapters 6 to 11)

Chapter 1 gives an overview of signal processing. Histogram measurement principles are reviewed first; different methods for the correlation analysis are explained next. Computer programs are shown to calculate power spectra for periodic and nonperiodic signals. Chapter 2 reviews the processes needed to understand real-time systems: sampling and quantizing

theorems, analog-to-digital conversion, and servicing and queueing involving serial and parallel components.

Chapter 3 describes mapping, adaptation, and learning in neural networks. Neural networks of very fine and medium granularity are presented: field computers, back-propagation networks, high-order units, alternating projection networks, and stochastic cells. Chapter 4 gives concrete examples of neural networks in real-time applications. Examples cover signal processing, noise cancellation, EKG, sonar, speech, phonetic typewriter, image recognition, object classification, manufacturing inspection, feedback process control, and robot manipulators. Neural networks open the road toward new learning, control, and robots, avoiding analytical system design.

Chapter 5 deals with coarse grain intelligent processes: artificial genetic selection, forward-backward chaining of knowledge schemas, and frames. Examples include expert systems for fault diagnosis, factory cell simulation and knowledge-based robots and process control.

Chapter 6 gives an overview of computers in instrumentation and inprocess control. The basic techniques explained are: input/output transfer, interrupts, direct-memory access, interfacing, real-time clock, channels, pools, and semaphores. Real-time operating systems, schedulers and monitors are also explained and widely used real-time computers, buses, and networks are illustrated.

Chapter 7 describes newly developed neural chips and systems. Structures realizing 20 billion multiply/adds per second equivalent to 40,000 MIPS are shown. Neurocomputers with 10 million processing elements are presented. Digital and analog neural chips are described and compared. The chapter ends with the description of high-speed neural coprocessors. Chapter 8 defines the concurrent system as a set of relatively independent processes, connected to each other through synchronized messages. Functional features of the transputer family of chips and the OCCAM language are described. Real-time programming is explained, including timers, scheduling, interrupts, and polling.

Chapter 9 concentrates on concurrent system design and applications. Concrete examples of parallel system design and real-time programming, based on transputers and OCCAM, are presented. Examples range from simple units, speech, vision and graphics, to multi-user flight simulators and flexible manufacturing cells.

Chapter 10 deals with the upper level transputer-based concurrent systems: computing surface, cluster and superclusters. These systems achieve the power of supercomputers for a tenth of the cost of other systems. Superclusters can expand infinitely, with overall throughput going up in proportion to the number of processors. Application specific topologies can be configured with the network configuration unit.

Chapter 11 outlines the market for neural, concurrent, and intelligent realtime systems. A whole new industry and business has been born. Intelligent systems and services represent the infrastructure for a highly efficient and clean economy of the 1990s. The suggestions of Nobel Laureates, devoting time and energy in intelligence research, are discussed.

The book has been written as a textbook for students, as well as a reference for practicing engineers and scientists. A minimal undergraduate-level background is assumed, although many fundamentals are reviewed. The treatment is kept as straightforward as possible emphasizing functions, systems, and applications. A comprehensive list of manufacturers at the end of the book includes addresses, products, and services (neural, concurrent, intelligent, real-time systems). Readers interested in other related topics of neural and concurrent processing should combine this book with B. Souček and M. Souček, *Neural and Massively Parallel Computers: The Sixth Generation*, Wiley, New York, 1988. These two books are independent volumes, with only a slight overlap.

Zagreb, Yugoslavia September 1989 Branko Souček

### **ACKNOWLEDGMENTS**

I acknowledge the encouragement, stimulating atmosphere, discussions, and support received from my teachers, friends, and colleagues. Here, I can only name a few:

P. Brajak, I. Bratko, A. D. Carlson, F. Cellier, B. Furht, S. R. Hameroff, F. J. Hill, F. Jović, G. A. Korn, D. Koruga, H. Kraljević, R. H. Mattson, V. Milutinović, B. Ostojić, P. Papić, G. R. Peterson, M. Petrinović, S. Prešern, J. W. Rozenblit, A. Scott, P. Šuhel, T. Triffet, D. Vrsalović, B. P. Zeigler, A. Železnikar.

I thank the following institutions where I performed the experiments with neural, concurrent, and real time systems:

Department of Mathematics, University of Zagreb

Department of Electrical and Computer Engineering, University of Arizona

Department of Neurobiology and Behavior, State University of New York at Stony Brook

Brookhaven National Laboratory

Institute Rudjer Bošković

To write a book like this one, it was necessary to borrow some data from the manuals of computer/system manufacturers. I should like to thank the following companies for giving permission to adapt material from their publications:

#### xviii ACKNOWLEDGMENTS

AI Wave, Inc.
Global Holonetic Corporation
Hecht-Nielsen Neurocomputer Corporation
INMOS Group of Companies
Intellicorp
Nestor, Inc.
Oxford Computer
Paracom
Parsytec
Syntonic Systems, Inc.

TRW

Special thanks to John Wiley's editors and reviewers and to Mrs. B. Grdović and Mrs. V. Zlatić for assistance during preparation of the manuscript for the publisher. I am grateful to Mrs. L. Van Horn for an outstanding job in supervising the copyediting of the manuscript.

## **CONTENTS**

Preface Acknowledgments	xiii xvii
PART I INTELLIGENT PROCESSES	
1. Signal Processing	3
Introduction and Survey, 3 1.1. Amplitude and Latency Histograms, 4 1.2. Measuring Histograms, 8 1.3. Amplitude Correlation, 14 1.4. Interval Correlation, 23 1.5. Fourier Analysis and Power Spectra, 30 1.6. Some Typical Spectra, 34 1.7. Signal Processing versus Neural Mapping, 41 References, 41	
2. Sampling, Quantizing, Servicing, and Queueing	43
Introduction and Survey, 43 2.1. Sampling, 43 2.2. Quantizing, 47 2.3. Analog-to-Digital Conversion, 51 2.4. Choice of Sampling and Quantizing Equipment, 2.5. Response Time, 61 2.6. Dead-Time Losses, 64	59

References, 70

3.	Mapping, Adaptation, and Learning in Neural Networks	71
	<ul> <li>Introduction and Survey, 71</li> <li>3.1. Intelligence Granularity, 71</li> <li>3.2. Field Computers, 73</li> <li>3.3. Neural Computers, 74</li> <li>3.4. The Kolmogorov Mapping Neural Network Existence Theorem, 77</li> <li>3.5. Back-Propagation Network, 79</li> <li>3.6. Alternating Projection Neural Network, 83</li> <li>3.7. Stochastic Computing, 85</li> <li>3.8. Probabilistic Logic Neuron Based on RAM, 89</li> <li>3.9. High-Order Neural Units, 91</li> <li>3.10. Learning Networks of Neurons with Boolean Logic, 94 References, 96</li> </ul>	
4.	Neural Networks in Real-Time Applications	99
	<ul> <li>Introduction and Survey, 99</li> <li>4.1. Gorman-Sejnowski Sonar Signal Processing, 99</li> <li>4.2. Widrow-Winter Adaptive Noise Canceler, 104</li> <li>4.3. EKG Processing on an ANZA Neurocomputer, 107</li> <li>4.4. Speech Recognition, 109</li> <li>4.5. Kohonen Phonetic Typewriter, 110</li> <li>4.6. Image Analysis and Visual Recognition, 113</li> <li>4.7. Reber-Lyman Rotation and Scale-Invariant Pattern Recognition Experiments, 114</li> <li>4.8. Lightware-ANZA Manufacturing Inspection, 117</li> <li>4.9. Reading Handprinted Numbers, 119</li> <li>4.10. Nestor Learning System for Pattern Recognition, 119</li> <li>4.11. Carpenter-Grossberg Adaptive Resonance Theory, 123</li> <li>4.12. Process Control Applications, 125</li> <li>4.13. Pole Balancing, 127</li> <li>4.14. Process Control Based on ANZA and Graded Learning Network, 130</li> <li>4.15. Intelligent Robots, 133</li> <li>4.16. Miyamoto-Kawato-Setoyama-Suzuki Robotic Manipulator, 134 References, 137</li> </ul>	
5.	Knowledge Chaining in Real-Time Applications	139
	Introduction and Survey, 139 5.1. Feature Extractors and Genetic Selection Systems, 139	

2.7. Real-Time Servicing and Queueing System, 69

<ul> <li>5.2. Rule-Based Representation of Knowledge, 141</li> <li>5.3. Knowledge Frames, 143</li> <li>5.4. Schema, Frame, BAM and FCM, 147</li> <li>5.5. Learning in Rule-Based Systems, 150</li> <li>5.6. Real-Time Knowledge Systems, Laning Recommendations, 152</li> <li>5.7. A Control System Simulator, 155</li> <li>5.8. Computer-Integrated Manufacturing (CIM), 156</li> <li>5.9. Model-Based Reasoning, 158</li> <li>5.10. The Factory Model, 159</li> <li>5.11. The Factory Model Application, 160</li> <li>5.12. Expert Systems in Engineering, 162</li> <li>5.13. Železnikar Informational Logic, 166</li> <li>5.14. From Fine to Coarse Intelligence Granularity, 167</li> <li>References, 169</li> </ul>	
PART II INTELLIGENT SYSTEMS	
6. Computers in Instrumentation and Process Control	175
<ul> <li>Introduction and Survey, 175</li> <li>6.1. Minicomputers and Microcomputers, 176</li> <li>6.2. Programmed Input-Output Transfer, 178</li> <li>6.3. Party Line for Programmed Input-Output, 180</li> <li>6.4. Example of Programmed Input-Output Transfer, 185</li> <li>6.5. Direct Memory Access, 186</li> <li>6.6. Instrument and Control System Components, 187</li> <li>6.7. Real-Time Clock, 190</li> <li>6.8. Processes, Channels and Pools, 192</li> <li>6.9. Synchronization, 193</li> <li>6.10. Real-Time Operating Systems, Monitors and Schedulers, 194</li> <li>6.11. Typical Small Process Control System: DIPS 85, 196</li> <li>6.12. Typical Small Process Control Software: NIVO, 201</li> <li>6.13. HP Computers for Real-Time and for Networking, 204</li> <li>6.14. Micro VAX and Micro PDP-11, 215</li> <li>References, 221</li> </ul>	
7. High-Speed Neural Chips and Systems	223
Introduction and Survey, 223 7.1. Garth Simulation-Communication Neural Chips, 223 7.2. Morton Intelligent Memory Chips, 227 7.3. Intelligent Memory Chips for Matrices with Hundreds of Columns, 230	

	<ul> <li>7.4. DENDROS Neural Network Analog CMOS Circuits, 237</li> <li>7.5. Mead-Mahowald Silicon Model of Early Visual Processing, 241</li> <li>7.6. Delta Functional Architecture, 245</li> </ul>	
	References, 249	
	3. Concurrent Chips and Languages	251
	Introduction and Survey, 251 8.1. Transputer and OCCAM, 252 8.2. Real-Time Programming, 258 8.3. Generating Events at Regular Intervals, 261 8.4. Using Timers in ALTs, 263 8.5. Interrupts, 267 8.6. Polling, 268 References, 271	
,	9. Concurrent System Design and Applications	273
	<ul> <li>Introduction and Survey, 273</li> <li>9.1. Multiprocessor System Topology, 273</li> <li>9.2. Distributed Operating System, 279</li> <li>9.3. Transputers in Speech Processing, 280</li> <li>9.4. Concurrent System for Hough Transform in Vision, 282</li> <li>9.5. A Multiprocessor Architecture for Robot Arm Control, 283</li> <li>9.6. Computer Graphics, 283</li> <li>9.7. Three-Dimensional Transformation on the IMS T800, 289</li> <li>9.8. Multiuser Flight Simulator, 293</li> <li>9.9. Parallel Backtracking PROLOG Engine, 296</li> <li>9.10. Control System of the Flexible Manufacturing Cell, 296 References, 298</li> </ul>	
1	0. Transputer-Based Computing Surface, Clusters, and Hyperclusters	299
	Introduction and Survey, 299 10.1. Computing Surface, 300 10.2. The MEGAFRAME Module Family, 303 10.3. Computing Cluster, 312 10.4. Mühlenbein-Krämer-Peise-Rinn MEGAFRAME Hypercluster, 318	N <sub>j</sub>

<ul> <li>10.5. Operating System Kernel on the Chip, 331</li> <li>10.6. Helios Operating System, 331</li> <li>10.7. Problem-Adapted Transputer Structure, 336</li> <li>10.8. Easy and Efficient Scalability, 338</li> <li>10.9. Nuclear Magnetic Resonance Computer Tomograph, 344</li> <li>10.10. Industrial Application, 344</li> <li>References, 347</li> </ul>	
11. Market and Trends	349
<ul> <li>Introduction and Survey, 349</li> <li>11.1. Sixth-Generation Intelligent Systems, 350</li> <li>11.2. Trends in Massively Parallel Computers, 351</li> <li>11.3. Trends in RISC Processors, 356</li> <li>11.4. Trends in VME Bus, 358</li> <li>11.5. Neural Network Market, 362</li> <li>11.6. Market for Neural and Concurrent Real-Time Systems, 363</li> <li>11.7. Market for Intelligence and Sensation, 366</li> <li>11.8. Nobel Laureates in Intelligence Research, 367</li> <li>11.9. Noble Intelligence, 370</li> <li>References, 371</li> </ul>	
LIST OF MANUFACTURERS	373
AUTHOR INDEX	383
SUBJECT INDEX	385

## Intelligent Processes

Signal Processing
Sampling, Quantizing, Servicing, and Queueing
Mapping, Adaptation, and Learning in Neural Networks
Neural Networks in Real-Time Applications
Knowledge Chaining in Real-Time Applications

### CHAPTER 1

## Signal Processing

#### INTRODUCTION AND SURVEY

A substantial increase in data collection efficiency can be achieved if data can be sorted on the basis of some parameter. In this case, hundreds or thousands of pieces of data, instead of being individually recorded, can be accumulated into one counting location. Each value of the parameter should have its own counting location. A histogram accumulated in this way presents the probability distribution function of the measured parameter. This kind of data collection is used in numerous experiments and can be readily done with a laboratory computer. Data collecting and sorting systems are usually called *analyzers*.

This chapter describes basic data analysis techniques. Probability distribution measurement principles are reviewed first, and the most important analysis techniques are described next. Different kinds of data analyzers are widely used in physics, chemistry, biomedicine, and engineering (pulseheight analyzers, time-of-flight analyzers, Mossbauer-effect analyzers, neuron activity analysis, industrial product testing, communication signal analysis, etc.).

A simple yet efficient tool for analyzing random data through statistical averaging is the correlation function, which describes the time dependencies of a random data record. The correlation function can be measured with special instruments or a laboratory computer. In this chapter, we describe the basic definition and properties of the correlation function and explain two methods for calculating it. It is shown that the classical method, although good for analysis of random amplitudes, produces meaningless results if used to analyze random intervals. Both computer-simulated and

experimental data have been used to check the correlation algorithms. A modified method for interval analysis is explained, and computer programs for both methods are discussed.

The most obvious presentation of the signal is in the time domain: the amplitude of the signal is displayed as a function of time. Another important presentation of the signal is in the frequency domain. For this purpose a Fourier series is used. The signal waveform may be expanded into a series of sine waves. By finding the amplitudes of these sine waves, we form the power spectra. The power spectral density function describes the general frequency composition of the data. The frequency composition of the data, in turn, bears important relationships to the basic characteristics of the physical or biomedical system involved.

In this chapter the Fourier series expansion is explained. Computer programs are used to calculate Fourier coefficients for a periodic signal and the power spectra for a nonperiodic signal. Also, some typical, frequently occurring waveforms are analyzed, and their power spectra are displayed and discussed. The chapter concludes with a discussion of special machines for measuring the power spectra and their variation with time. Such sonograms are widely used in acoustic communication for speech study and for animal communication study.

#### 1.1. AMPLITUDE AND LATENCY HISTOGRAMS

In analyzing nature, one finds that many results indicate that the subjects of measurements belong to the class of random physical phenomena. *Randomness* means that the data are nonperiodic, exhibit no explicit time trend, bias, or regularity, and cannot be precisely defined for all time by any simple analytic function. Many random processes belong to the class of stationary processes. *Stationary* means that certain statistical properties of the data do not change with time.

Three main types of statistical analyses should be carried out for random and stationary data:

- 1. Amplitude probability density functions,
- 2. Correlation functions, and
- 3. Power spectral density functions.

These functions describe a random process, much like the amplitude, waveform, and Fourier frequency spectrum describe a deterministic process. This chapter deals with methods for measuring the amplitude probability density function (pdf).

Five important examples of processes that could occur in practice, singly or in combinations, will now be considered: