

Frank Pfenning (Ed.)

LNCS 4098

Term Rewriting and Applications

17th International Conference, RTA 2006
Seattle, WA, USA, August 2006
Proceedings

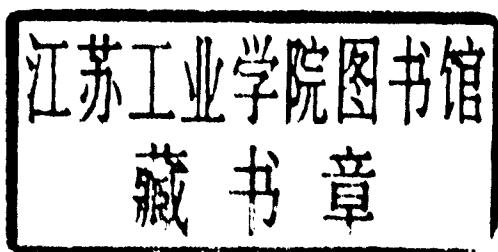


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Frank Pfenning (Ed.)

Term Rewriting and Applications

17th International Conference, RTA 2006
Seattle, WA, USA, August 12-14, 2006
Proceedings



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Library of Congress Control Number: 2006929604

CR Subject Classification (1998): F.4, F.3.2, D.3, I.2.2-3, I.1

LNCS Sublibrary: SL 1 – Theoretical Computer Science and General Issues

ISSN	0302-9743
ISBN-10	3-540-36834-5 Springer Berlin Heidelberg New York
ISBN-13	978-3-540-36834-2 Springer Berlin Heidelberg New York

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Printed in Germany

Typesetting: Camera-ready by author, data conversion by Scientific Publishing Services, Chennai, India
Printed on acid-free paper SPIN: 11805618 06/3142 5 4 3 2 1 0

Commenced Publication in 1973

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Preface

This volume contains the proceedings of the 17th International Conference on Rewriting Techniques and Applications, which was held on August 12–14, 2006 in Seattle, Washington, as part of the 4th Federated Logic Conference (FLoC). RTA is the major forum for the presentation of research on all aspects of rewriting. Previous RTA conferences took place in Dijon (1985), Bordeaux (1987), Chapel Hill (1989), Como (1991), Montreal (1993), Kaiserslautern (1995), New Brunswick (1996), Sitges (1997), Tsukuba (1998), Trento (1999), Norwich (2000), Utrecht (2001), Copenhagen (2002), Valencia (2003), Aachen (2004), and Nara (2005).

A total of 23 regular papers and 4 system descriptions were selected for presentation from 52 submissions. Each paper was reviewed by at least 4 members of the Program Committee, with the help of 115 external referees. The committee decided to give the Best Paper Award for RTA 2006 to the contribution “Termination of String Rewriting with Matrix Interpretations” by Dieter Hofbauer and Johannes Waldmann for their original and powerful application of SAT solving in proving termination.

I would like to thank all the members of the Program Committee for their diligent, careful, and timely work and thoughtful deliberation, and Andrei Voronkov for providing the EasyChair system which greatly facilitated the reviewing process, the electronic Program Committee meeting, and the preparation of the program and the proceedings.

In addition to contributed papers, the program contained a FLoC plenary talk by Randal Bryant and two invited talks by Javier Esparza and Jürgen Giesl. I would like to thank the invited speakers not only for their presentations, but also for contributing abstracts or full papers to the proceedings.

RTA also sponsored a number of workshops held during FLoC, on the topics of Higher-Order Rewriting (HOR), Rule-Based Programming (RULE), Unification (UNIF), Reduction Strategies in Rewriting and Programming (WRS), Termination (WST), and a meeting of the IFIP Working Group 1.6 on Term Rewriting.

Many people helped to make RTA 2006 a success. I am particularly grateful to Ashish Tiwari, who took on the dual role of Conference Chair and RTA Workshop Chair, Ralf Treinen, the Publicity Chair, and Thomas Ball, Gopal Gupta, Jakob Rehof, and Moshe Vardi from the FLoC Organizing Committee who did an incredible amount of work in the arrangements for RTA and FLoC.

May 2006

Frank Pfenning

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Formal Verification of Infinite State Systems Using Boolean Methods^{*}

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Most successful automated formal verification tools are based on a bit-level model of computation, where a set of Boolean state variables encodes the system state. Using powerful inference engines, such as Binary Decision Diagrams (BDDs) and Boolean satisfiability (SAT) checkers, symbolic model checkers and similar tools can analyze all possible behaviors of very large, finite-state systems.

For many hardware and software systems, we would like to go beyond bit-level models to handle systems that are truly infinite state, or that are better modeled as infinite-state systems. Examples include programs manipulating integer data, concurrency protocols involving arbitrary numbers of processes, and systems containing buffers where the sizes are described parametrically.

Historically, much of the effort in verifying such systems involved automated theorem provers, requiring considerable guidance and expertise on the part of the user. We would like to devise approaches for these more expressive system models that retain the desirable features of model checking, such as the high degree of automation and the ability to generate counterexamples.

We have developed UCLID [1], a prototype verifier for infinite-state systems. The UCLID modeling language extends that of SMV [9], a bit-level model checker, to include state variables that are integers, as well as functions mapping integers to integers and integers to Booleans. Functional state variables can be used to define array and memory structures, including arrays of identical processes, FIFO buffers, and content-addressable memories.

System operation is defined in UCLID in terms of the initial values and next-state functions of the state variables. Integer operations include linear arithmetic and relational operations. Functions can be defined using uninterpreted function symbols, as well as via a restricted form of lambda expression. The underlying logic is reasonably expressive, yet it still permits a decision procedure that translates the formula into propositional logic and then uses a SAT solver [7].

UCLID supports multiple forms of verification, requiring different levels of sophistication in the handling of quantifiers. All styles verify that a safety property of the form $\forall \mathcal{X}P(s)$ holds for some set of system states s , where $\mathcal{X}$ denotes a set of integer *index variables*. Index variables can be used to express universal properties for all elements in an array of identical processes, all entries in a FIFO buffer, etc.

The simplest form of *bounded property checking* allows the user to determine that property $\forall \mathcal{X}P(s)$ holds for all states reachable within a fixed number of steps k from an

^{*} This research was supported by the Semiconductor Research Corporation, Contract RID 1029.001.

initial state. Verifying such a property can be done by direct application of the decision procedure. In practice, the effort required to verify such a property grows exponentially in k , limiting the verification to around 10–20 steps. However, it provides a useful debugging tool. In our experience, most errors are detected by this approach.

Of course, it is important to verify that properties hold for all reachable states of the system. Unfortunately, the standard fixed-point methods for bit-level model checking do not work for infinite-state systems. In many cases, the system will not reach a fixed point within a bounded number of steps. Even for those that do, checking convergence is undecidable, and our efforts to implement incomplete methods for this task have had limited success [2].

To prove that property $\forall \mathcal{X}P(s)$ holds for all reachable states s , UCLID supports *inductive invariant* checking, where the user provides an invariant Q such that Q holds for all initial states, Q implies P , and any successor for a state satisfying Q must also satisfy Q . This latter condition requires proving the validity of a formula containing existentially quantified index variables. Although this problem is undecidable for our logic, we have successfully implemented an incomplete approach using quantifier instantiation [8].

A more automated technique is to derive an inductive invariant via *predicate abstraction* [4]. Predicate abstraction operates much like the fixed-point methods of symbolic model checking, but using the concretization and abstraction operations of abstract interpretation [3] on each step. We have generalized predicate abstraction to handle the indexed predicates supported by UCLID [6]. Each step requires quantifier elimination to eliminate the current state variables, much like the relational product step of symbolic model checking. We implement this step by performing SAT enumeration on the translated Boolean formula.

As a final level of automation, we can automatically discover a set of relevant predicates for predicate abstraction based on the property P and the next-state expressions for the state variables [5].

We have successfully verified a number of systems with UCLID, including out-of-order microprocessors, distributed cache protocols, and distributed synchronization protocols.

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