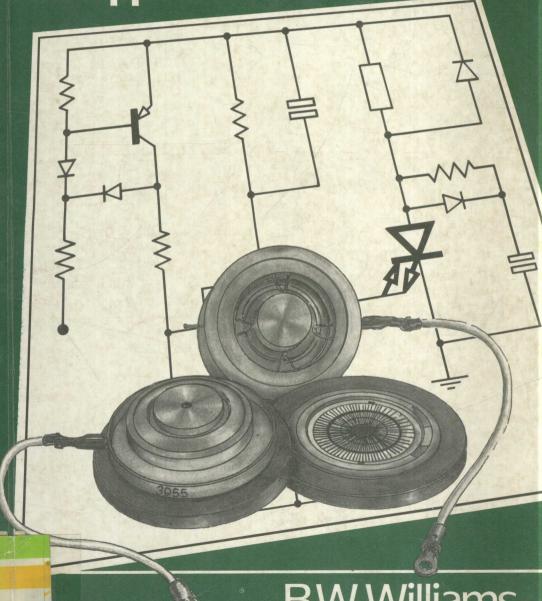
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Devices, Drivers and Applications



B.W. Williams

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Power Electronics

Devices, Drivers and Applications

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Preface

The objective of this book is to bridge the gap between semiconductor device theory and device application in the area of power electronics. The majority of existing books concentrate on one aspect at the expense of the other. They either analyse semiconductor physics and material aspects of devices without including any applications or, at the other end of the spectrum, they consider device applications with the device being treated as a 'black box'.

This book specifically considers high-power devices and in particular how a device's structure and construction are related to its terminal electrical and thermal characteristics. Only then, armed with an understanding of device characteristics, is it possible to select the correct device for a given application.

Emphasis is placed on the circuitry required to use power devices rather than treating them as ideal switches or considering just one device type. The structure and electrical characteristics of a device are related to its driving and protection requirements for a particular type of application.

The book is in three parts. Part 1 covers power device electrical and thermal characteristics and how they relate to a device's physical structure. Devices considered include the thyristor, gto, mosfet and bipolar transistor. Part 2 describes device driving and protection techniques and the final part gives a general selection of power electronic applications. Application areas other than those dominated by thyristors have been considered; in particular the growing area of switched-mode power supplies has been introduced.

The book gives a fundamental, yet detailed education in the art of power electronics. Since a basic knowledge of semiconductor physics and circuit techniques is assumed, it is a specialist subject book intended for university and polytechnic students, as well as for practising engineers wishing to broaden their understanding of power electronics. Non-trivial worked examples and problems have been included for the student, and each chapter has a specific reading list.

B. W. W.

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List of Symbols

device tolerance а Α anode b base b_{t} base transport factor saturation flux density $B_{\rm sat}$ c collector Ccapacitance C_{ds} drain-to-source capacitance C_{f} heat-sink correction factor $C_{\rm gd}$ gate-to-drain capacitance C_{gs} gate-to-source capacitance snubber transfer capacitance $C_{\rm o}$ $C_{\rm s}$ snubber capacitance d drain output voltage e_{0} $e_{\rm s}$ step input voltage Ecircuit voltage source E_{i} input voltage source **ESR** equivalent series resistance $f_{\mathbf{m}}$ maximum switching frequency switching frequency $f_{\mathbf{s}}$ mosfet gate g G thyristor gate

1.	la tata Company
h	heat transfer coefficient
$H_{\rm s}$	flux intensity at saturation
i	current
$i_{\rm b}$	base current
$i_{\rm i}$	input current
i_1	inductor current
i_0	
i_{rr}	output current
	reverse recovery current thyristor anode current
$I_{ m T}$	
$I_{\rm c}$	prospective fault current collector current
$I_{\rm cbo}$	collector leakage current
$I_{c(max)}$	maximum allowable collector current drain current
$I_{\rm d}$	rms drain current
$\frac{I_{d(rms)}}{I}$	mean diode current
I_{D}	forward current
$I_{ m F}$	
$I_{\mathrm{F(av)}}$	average forward current
$I_{\rm G}$	gate current
$I_{\rm H}$	holding current
$I_{\mathbf{i}}$	mean input current
I_1	leakage current
$I_{ m L}$	latching current
$\frac{I_0}{\bar{r}}$	reverse leakage current
$I_{\rm o}$	mean output current
I_{p}	peak let-through current
I_{rr}	peak reverse recovery current
$I_{\rm s/b}$	second breakdown current
I_{T}	total current
J	junction
J_{R}	reverse recovery energy
K	reverse recovery energy
k	transistor switching time ratio
K	cathode
K_{ID}	diode current form factor
$K_{\rm v}$	voltage ripple factor
l_{p}	primary inductance
$l_{\rm S}$	saturable inductance
$L_{\rm S}$	inductance
$L_{\rm S}$	linear snubber inductance
Ls	inical shubbel muuctance

bols

xii	List of Symb
m M	empirical transistor constant avalanche multiplication factor
n $n_{ m i}$ N $N_{ m A}$ $N_{ m C}$	ratio C_s/C_o intrinsic carrier concentration turns or turns ratio acceptor concentration concentration donor concentration
pd P P _d P _G P _p P _s	percentage derating power average power dissipation mean gate power peak power switching interval power loss
Q _G Q _o Q _R	mosfet gate charge diode total recovery charge diode reverse recovery charge
r_{b}' $r(t_{\mathrm{p}})$ R_{be} $R_{\mathrm{ds(on)}}$ R_{g} R_{L} $R_{\mathrm{\theta}}$ $R_{\mathrm{bj-c}}$	base lateral resistance thermal normalising factor base-to-emitter resistance mosfet on-state resistance gate resistance load resistance thermal resistance thermal resistance junction-to-case
s S SOA	source maximum dv/dt safe operating area
t t_{a} t_{c} t_{d} t_{D} t_{fi} t_{fr}	time fuse arcing time fuse clearing time turn-on delay time diode conduction time current fall time diode turn-on time voltage fall time
$t_{ m m}$ $t_{ m off}$	fuse melting time turn-off time

```
turn-on time
ton
          pulse width
t_{n}
          thyristor turn-off time
t_{a}
          current rise time
t_{ri}
          reverse recovery time
t_{rr}
          saturation delay time
te
          transistor on-time
t_{\rm T}
          time of no conduction
t_{\mathbf{x}}
T
          temperature
T_{\mathbf{a}}
          ambient temperature
T_{\rm c}
          case temperature
          junction temperature
T_{i}
          maximum allowable junction temperature
T_{\rm imax}
T_{\rm m}
          main thyristor
          voltage
```

```
v
v<sub>be(sat)</sub> base-to-emitter saturation voltage
v_{ce(sat)} collector-to-emitter saturation voltage
          diode on-state voltage
\nu_{\rm D}
          output voltage
\nu_{o}
V
          voltage
V_{\mathbf{a}}
          common emitter avalanche voltage
V_{ac}
          ac supply voltage
V_{\rm h}
          junction avalanche voltage
          base-to-emitter voltage
V_{\rm BF}
          scr forward breakover voltage
V_{\rm BR}
          scr reverse breakdown voltage
V_{c}
          collector voltage
          collector-to-emitter voltage
V_{ce}
V_{\rm D}
          device voltage rating
V_{\rm ds}
          drain-to-source voltage
V_{\rm fp}
          diode turn-on peak forward voltage
V_{gs}
          gate-to-source voltage
V_{\rm L}
          line-to-line rms voltage
V_{0}
          mean output voltage
V_{PT}
          punch-through voltage
V_{\mathbf{r}}
          rms output voltage
V_{\mathbf{R}}
          reverse applied voltage
V_{RI}
          ripple voltage
V_{\rm RM}
          reverse overshoot voltage
          dc supply voltage
V_{\rm c}
V_{\rm TH}
          mosfet threshold voltage
```

Zener voltage

 V_{Z}

 ω $\omega_{
m o}$

> peak minimum

of Symbols

xiv	List of
w	thickness
W	energy
W_{t}	total loss
Z	impedance
$Z_{ heta}$	thermal impedance
α	current transfer ratio
$\alpha_{ m o}$	α in the linear region
β	current amplification factor
β_{Q}	gto turn-off gain
γ	injection efficiency
δ	duty cycle
ΔT	change in temperature
λ	thermal conductance
ξ	damping factor
$\xi_{\rm m}$	peak electric field
σ	electrical conductivity
au	period
ϕ	$\tan^{-1} (\omega L/R)$
Φ	zero bias potential

oscillation frequency natural oscillation frequency

Contents

Ack	face knowledgements t of Symbols	viii ix x
PA	RT 1: DEVICES	1
1	Basic Semiconductor Physics and Technology	3
	1.1 Processes forming pn junctions1.2 The masking process	6
2	The pn Junction	8
	 2.1 The pn junction under forward bias (steady state) 2.2 The pn junction under reverse bias (steady state) 2.3 Thermal effects 2.4 Piecewise-linear junction diode model 	8 9 12 13
3	Power Switching Devices and their Static Electrical Characteristics	16
	3.1 The power diode3.2 Power switching semiconductor transistors3.3 The thyristor3.4 The field-controlled thyristor (fct)	16 23 36 50
4	Electrical Ratings and Characteristics of Power Semiconductor Switching Devices	53
	 4.1 General maximum ratings of power semiconductor devices 4.2 The fast-recovery diode 4.3 The bipolar, high-voltage, power switching npn transistor 4.4 The power mosfet 4.5 The thyristor 	53 55 59 70 78

vi Contents

5	Cooling of Power Switching Semiconductor Devices	90
	5.1 Thermal resistances	9
	5.2 Modes of power dissipation	93
	5.3 Average power dissipation	98
	5.4 Power losses from manufacturers' data sheets	98
	5.5 Heat-sinking design cases	100
PA	RT 2: DRIVERS	111
6	Load Considerations	113
	6.1 The resistive load	113
	6.2 The inductive load	116
7	Driving the Transistor, and its Protection	124
	7.1 Application of the power mosfet	124
	7.2 Application of the bipolar transistor	133
	7.3 Transistor combinations	151
	7.4 Appendix: Turn-off switching-aid circuit analysis	154
8	Driving the Thyristor, and its Protection	159
	8.1 Gate drive circuits	160
	8.2 Gate drive design	162
	8.3 Drive design for gto's	164
	8.4 Thyristor switching-aid circuits	164
	8.5 Switching-aid circuit for the gto thyristor	170
	8.6 Appendix: Turn-off snubber circuit analysis	170
9	Switching-aid Circuits with Energy Recovery	173
	9.1 Energy recovery for turn-on snubber circuits	173
	9.2 Energy recovery for turn-off snubber circuits	177
	9.3 Unified turn-on and turn-off snubber circuit energy recovery	184
10	Series and Parallel Device Operation and Protection	187
	10.1 Parallel and series operation of power devices	187
	10.2 Protection	198
PAI	RT 3: APPLICATIONS	213
11	Naturally Commutating Converters	215
	11.1 Single-phase uncontrolled converter circuits	216
	11.2 Single-phase half-controlled converter	224

		Contents	vii
	11.4	Single-phase controlled thyristor converter circuits Three-phase uncontrolled converter circuits	227 233
		Three-phase half-controlled converter	239
	11.6	Three-phase controlled thyristor converter circuits	239
12	AC V	oltage Regulators	249
		Single-phase ac regulator	249
		Three-phase ac regulator	251
		Integral cycle control	256
		Single-phase transformer tap changer	258
		Cycloconverter	259
	12.6	Load efficiency and supply current power factor	259
13	Thyr	istor Forced Commutated DC Choppers	264
		A thyristor reverse impressed voltage commutated dc chopper A thyristor current impulse displacement commutated dc	266
		chopper	270
	13.3	Other cid and riv commutation circuits	277
	13.4	Comparison between the cid and riv commutation processes	279
14	Stati	c Power Inverters	282
	14.1	Voltage-fed inverter bridge topologies	283
		Controlled current-sourced inverters	304
15	Swite	ched-mode Power Supplies	309
	15.1	The forward converter	309
	15.2	The flyback converter	314
	15.3	Comparison of basic converters	321
	15.4	Multiple-switch, balanced, isolated converters	324
Bib	liogra	phy	330
	Index		

PART 1:

DEVICES