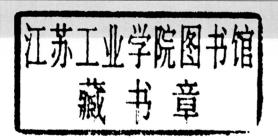
Operational Amplifiers and Linear Integrated Gircuits

6th Edition

Robert F. Coughlin Frederick F. Driscoll

SIXTH EDITION

Operational Amplifiers and Linear Integrated Circuits



Robert F. Coughlin Wentworth Institute of Technology

Frederick F. Driscoll

Wentworth Institute of Technology



Upper Saddle River, New Jersey Columbus, Ohio

Library of Congress Cataloging-in-Publication Data

Coughlin, Robert F.

Operational amplifiers and linear integrated circuits / Robert F. Coughlin, Frederick F. Driscoll. - 6th ed.

p.

Includes bibliographical references and index.

ISBN 0-13-014991-8

1. Operational amplifiers. 2. Linear integrated circuits.

I. Driscoll, Frederick F.,

II. Title.

TK7871.58.O6C68 2001

621.3815-dc21

00-040633

CIP

Vice President and Publisher: Dave Garza

Editor in Chief: Stephen Helba Acquisitions Editor: Scott J. Sambucci Production Editor: Rex Davidson

Design Coordinator: Karrie Converse-Jones

Cover Designer: Thomas Mack Cover art: Marjory Dressler Production Manager: Pat Tonneman Marketing Manager: Ben Leonard

This book was set in Times Roman by York Graphic Services, Inc. It was printed and bound by R. R. Donnelley & Sons Company. The cover was printed by Phoenix Color Corp.

Copyright © 2001, 1998, 1991, 1987, 1982, 1977 by Prentice-Hall, Inc., Upper Saddle River, New Jersey 07458. All rights reserved. Printed in the United States of America. This publication is protected by Copyright and permission should be obtained from the publisher prior to any prohibited reproduction, storage in a retrieval system, or transmission in any form or by any means, electronic, mechanical, photocopying, recording, or likewise. For information regarding permission(s), write to: Rights and Permissions Department.



10 9 8 7 6 5 4 3 2 ISBN: 0-13-014991-8

Preface

The authors' intention in all previous editions of *Operational Amplifiers and Linear Integrated Circuits* has been to show that operational amplifiers and other linear integrated circuits are easy to use and fun to work with. This sixth edition has kept that basic philosophy. For the fundamental circuits, we have continued to use devices that are readily available, easy to use, and forgiving if a wiring error is made. Newer devices are introduced where the application requires it. We have preserved our original objective of simplifying the process of learning about applications involving signal conditioning, signal generation, filters, instrumentation, timing, and control circuits. This edition continues to reflect the evolution of analog circuits into applications requiring transducer signals that must be conditioned for a microcontroller's analog-to-digital input. We have kept circuit simulation using OrCAD® PSpice®. A laboratory manual is now available to accompany

¹ A detailed procedure on how to design circuits that interface between the physical world and microcontrollers is presented in *Data Acquisition and Process Control with the M68HC11 Microcontroller, 2nd Edition*, by F. Driscoll, R. Coughlin, and R. Villanucci, published by Prentice Hall (2000).

XXVi Preface

this sixth edition.² It includes both detailed hardware and simulation exercises. Some exercises are step-by-step; others are design projects. The exercises follow the text material.

Chapters 1 through 6 provide the reader with a logical progression from op amp fundamentals to a variety of practical applications without having to worry about op amp limitations. Chapter 7 shows how op amps combined with diodes can be used to design ideal rectifier circuits as well as clamping and clipping circuits. PSpice models and simulations are included in these chapters.

Chapter 8 shows applications that require measuring a physical variable such as temperature, force, pressure, or weight and then having the signal conditioned by an instrumentation amplifier before being input into a microcontroller's A/D converter. Instrumentation amplifiers are required when a designer has to measure a differential signal, especially in the presence of a larger noise signal.

As previously mentioned, in order not to obscure the inherent simplicity and overwhelming advantages of using op amps, their limitations have been left for Chapters 9 and 10. Dc limitations are studied in Chapter 9 and ac limitations are covered in Chapter 10. An expanded discussion on common-mode rejection ratio has been included in this edition. Many limitations have been made negligible by the latest generations of op amps, as pointed out in these chapters.

Active filters, low-pass, high-pass, band-pass, and band-reject, are covered in Chapter 11. Butterworth-type filters were selected because they are easy to design and produce a maximally flat response in the pass band. Chapter 11 shows the reader how to design a variety of filters easily and quickly.

Chapter 12 introduces a linear integrated circuit known as the multiplier. The device makes analysis and design of AM communication circuits simpler than using discrete components. Modulators, demodulators, frequency shifters, a universal AM radio receiver, and analog divider circuits all use a multiplier IC as the system's basic building block. This chapter has been retained because instructors have written to say that the principles of single-side band suppressed carrier and standard amplitude-modulation transmission and detection are clearly explained and quite useful for their courses.

The inexpensive 555 IC timer is covered in Chapter 13. This chapter shows the basic operation of the device as well as many practical applications. The chapter also includes a timer/counter unit.

In previous editions, analog-to-digital and digital-to-analog converters have been covered in a single chapter. This edition separates these topics into two chapters so that more device specifications can be included as well as practical applications. Chapter 14 deals only with analog-to-digital converters, while the new Chapter 15 covers digital-to-analog converters. A serial ADC connected to a Motorola microprocessor is shown (with assembly language code) in Chapter 14.

Chapter 16 shows how to design a regulated linear power supply. This chapter begins with the fundamentals of unregulated supplies and proceeds to regulated supplies. It shows how IC regulators are used for building low-cost 5 V and \pm 15 V bench supplies.

² Laboratory Manual to Accompany Operational Amplifiers and Linear Integrated Circuits, 6th Edition, by R. Coughlin, F. Driscoll, and R. Villanucci published by Prentice Hall (2001).

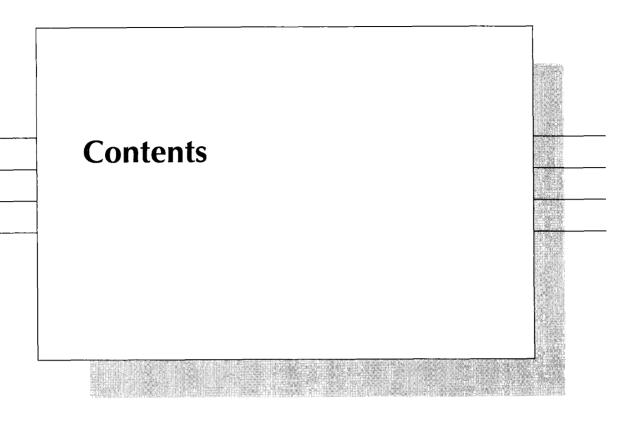
Preface XXVII

This edition has more than enough material for a single-semester course. After the first three chapters, instructors often take chapters out of sequence depending on the class interest, need to complement another course (such as a design course), or availability of lab equipment or class time. Therefore, Chapters 4 through 16 have been written as standalone chapters for this very reason. The circuits have been tested in the laboratory by the authors and the material is presented in a form useful to students or as a reference to practicing engineers and technologists. Each chapter includes learning objectives and problems, and most chapters have PSpice simulations. The reader should refer to the accompanying laboratory manual for lab exercises and additional simulation exercises.

ACKNOWLEDGMENTS

We acknowledge with gratitude the advice of Professor Robert Villanucci, who is also a co-author of the laboratory manual, and two highly respected engineers, Dan Sheingold of Analog Devices and Bob Pease of National Semiconductor. A special thanks goes to Libby Driscoll for assisting in the preparation of the manuscript. We thank the following reviewers of the manuscript: Warren Hioki, Community College of Southern Nevada; Gregory M. Rasmussen, St. Paul Technical School; Michael W. Rudisill, Northern Michigan University; Rod Schein, Edmonds Community College, ATTC; and Andrew C. Woodson.

Finally, we thank our students for their insistence on relevant instruction that is immediately useful and our readers for their enthusiastic reception of previous editions and their perceptive suggestions for this edition.



	PREI	FACE	XXV
1	INTF	RODUCTION TO OP AMPS	1
		Learning Objectives 1	
	1-0	Introduction 2	
	1-1	Is There Still a Need for Analog Circuitry? 2	
		 1-1.1 Analog and Digital Systems, 2 1-1.2 Op Amp Development, 3 1-1.3 Op Amps Become Specialized, 3 	
	1-2	741 General-Purpose Op Amp 4	
		1-2.1 Circuit Symbol and Terminals, 4 1-2.2 Simplified Internal Circuitry of a General-Purpose Op Amp, 5	

vi Contents

		1-2.3 Input Stage—Differential Amplifier, 6 1-2.4 Intermediate Stage—Level Shifter, 6 1-2.5 Output Stage—Push-Pull, 6
	1-3	Packaging and Pinouts 7
		1-3.1 Packaging, 7 1-3.2 Combining Symbol and Pinout, 8
	1-4	How to Identify or Order an Op Amp 9
		1-4.1 The Identification Code, 9 1-4.2 Order Number Example, 10
	1-5	Second Sources 10
	1-6	Breadboarding Op Amp Circuits 11
		1-6.1 The Power Supply, 11 1-6.2 Breadboarding Suggestions, 11
		Problems 12
2	FIRST	EXPERIENCES WITH AN OP AMP 13
		Learning Objectives 13
	2-0	Introduction 14
	2-1	Op Amp Terminals 14
		 2-1.1 Power Supply Terminals, 15 2-1.2 Output Terminal, 16 2-1.3 Input Terminals, 16 2-1.4 Input Bias Currents and Offset Voltage, 17
	2-2	Open-Loop Voltage Gain 18
		 2-2.1 Definition, 18 2-2.2 Differential Input Voltage, E_d, 18 2-2.3 Conclusions, 19
	2-3	Zero-Crossing Detectors 20
		2-3.1 Noninverting Zero-Crossing Detector, 20 2-3.2 Inverting Zero-Crossing Detector, 21
	2-4	Positive- and Negative-Voltage-Level Detectors 21
		2-4.1 Positive-Level Detectors, 21 2-4.2 Negative-Level Detectors, 21
	2-5	Typical Applications of Voltage-Level Detectors 21
		2-5.1 Adjustable Reference Voltage, 21

	2-5.2 Sound-Activated Switch, 22 2-5.3 Light Column Voltmeter, 24 2-5.4 Smoke Detector, 26	
2-6	Voltage Reference ICs 27	
	 2-6.1 Introduction, 27 2-6.2 Ref-02, 27 2-6.3 Ref-02/Voltage Level Detector Applications, 27 	
2-7	Signal Processing with Voltage-Level Detectors 29	
	 2-7.1 Introduction, 29 2-7.2 Sine-to-Square Wave Converter, 29 2-7.3 Sawtooth-to-Pulse Wave Converter, 29 2-7.4 Quad Voltage Comparator, LM339, 30 	
2-8	Computer Interfacing with Voltage-Level Detectors 32	
	 2-8.1 Introduction, 32 2-8.2 Pulse-Width Modulator, Noninverting, 33 2-8.3 Inverting and Noninverting Pulse-Width Modulators, 35 	
2-9	A Pulse-Width Modulator Interface to a Microcontroller 37	
2-10 Op Amp Comparator Circuit Simulation 38		
	2-10.1 Introduction, 382-10.2 Creating, Initializing, and Simulating a Circuit, 38	
	Problems 41	
INVE	RTING AND NONINVERTING AMPLIFIERS	44
	Learning Objectives 44	
3-0	Introduction 45	
3-1	The Inverting Amplifier 45	
	3-1.1 Introduction, 45 3-1.2 Positive Voltage Applied to the Inverting Input, 45 3-1.3 Load and Output Currents, 47 3-1.4 Negative Voltage Applied to the Inverting Input, 48 3-1.5 Voltage Applied to the Inverting Input, 49 3-1.6 Design Procedure, 51 3-1.7 Analysis Procedure, 51	
3-2	Inverting Adder and Audio Mixer 52	
	 3-2.1 Inverting Adder, 52 3-2.2 Audio Mixer, 53 3-2.3 DC Offsetting an AC Signal, 53 	

3

viii Contents

3-3	Multichannel Amplifier 55	
	3-3.1 The Need for a Multichannel Amplifier, 55 3-3.2 Circuit Analysis, 55 3-3.3 Design Procedure, 56	
3-4	Inverting Averaging Amplifier 56	
3-5	Noninverting Amplifier 57	
	3-5.1 Circuit Analysis, 57 3-5.2 Design Procedure, 59	
3-6	Voltage Follower 61	
	3-6.1 Introduction, 61 3-6.2 Using the Voltage Follower, 62	
3-7	The "Ideal" Voltage Source 64	
	 3-7.1 Definition and Awareness, 64 3-7.2 The Unrecognized Ideal Voltage Source, 64 3-7.3 The Practical Ideal Voltage Source, 65 3-7.4 Precise Voltage Sources, 66 	
3-8	Noninverting Adder 66	
3-9	Single-Supply Operation 67	
3-10	Difference Amplifiers 69	
	3-10.1 The Subtractor, 70 3-10.2 Inverting—Noninverting Amplifier, 71	
3-11	Designing a Signal Conditioning Circuit 71	
3-12	PSpice Simulation 76	
	3-12.1 Inverting Amplifier—DC Input, 76 3-12.2 Inverting Amplifier—AC Input, 77 3-12.3 Inverting Adder, 78 3-12.4 Noninverting Adder, 79	
	Problems 80	
COMF	PARATORS AND CONTROLS	84
	Learning Objectives 84	
4-0	Introduction 85	
4-1	Effect of Noise on Comparator Circuits 85	

4-2 Positive Feedback 87

	4-2.2	Introduction, 87 Upper-Threshold Voltage, 88 Lower-Threshold Voltage, 88		
4-3	Zero-	Crossing Detector with Hysteresis 90		
	4-3.1 4-3.2	Defining Hysteresis, 90 Zero-Crossing Detector with Hysteresis as a Memory Element, 91		
4-4	Volta	ge-Level Detectors with Hysteresis 91		
	4-4.1 4-4.2 4-4.3	Noninverting Voltage-Level Detector with Hysteresis, 92		
4-5		ge-Level Detector with Independent Adjustment esteresis and Center Voltage 96		
	4-5.1 4-5.2	Introduction, 96 Battery-Charger Control Circuit, 98		
4-6	On-Off Control Principles 99			
	4-6.1 4-6.2 4-6.3	F		
4-7	An In	dependently Adjustable Setpoint Controller 100		
	4-7.1 4-7.2 4-7.3 4-7.4 4-7.5	Principle of Operation, 100 Output—Input Characteristics of an Independently Adjustable Setpoint Controller, 100 Choice of Setpoint Voltages, 101 Circuit for Independently Adjustable Setpoint Voltage, 102 Precautions, 104		
4-8	IC Pr	ecision Comparator, 111/311 104		
	4-8.1 4-8.2 4-8.3	Introduction, 104 Output Terminal Operation, 104 Strobe Terminal Operation, 104		
4-9	Biom	edical Application 106		
4-10	Wind	low Detector 108		
	4-10.1 4-10.2			

X Contents

4-11	Propagation Delay 108	
	4-11.1 Definition, 108 4-11.2 Measurement of Propagation Delay, 110	
4-12	Using PSpice to Model and Simulate Comparator Circuits 111	
	4-12.1 Simulation of the Zero-Crossing Detector with Hysteresis, 111	
	4-12.2 Window Detector, 113	
	Problems 115	
SELE	CTED APPLICATIONS OF OP AMPS	118
	Learning Objectives 118	
5-0	Introduction 119	
5-1	High-Resistance DC Voltmeter 119	
	5-1.1 Basic Voltage-Measuring Circuit, 119 5-1.2 Voltmeter Scale Changing, 120	
5-2	Universal High-Resistance Voltmeter 121	
	5-2.1 Circuit Operation, 121 5-2.2 Design Procedure, 122	
5-3	Voltage-to-Current Converters: Floating Loads 123	
	 5-3.1 Voltage Control of Load Current, 123 5-3.2 Zener Diode Tester, 123 5-3.3 Diode Tester, 123 	
5-4	Light-Emitting-Diode Tester 125	
5-5	Furnishing a Constant Current to a Grounded Load 126	
	5-5.1 Differential Voltage-to-Current Converter, 126 5-5.2 Constant-High-Current Source, Grounded Load, 127 5-5.3 Interfacing a Microcontroller Output to a 4- to-20-mA	
	Transmitter, 128 5-5.4 Digitally Controlled 4- to 20-mA Current Source, 129	
5-6	Short-Circuit Current Measurement and Current-to- Voltage Conversion 130	
	5-6.1 Introduction, 130 5-6.2 Using the On Amp to Measure Short-Circuit Current, 130	

5

	5-7	Measuring Current from Photodetectors 132	
		5-7.1 Photoconductive Cell, 132 5-7.2 Photodiode, 133	
	5-8	Current Amplifier 133	
	5-9	Solar Cell Energy Measurements 134	
		5-9.1 Introduction to the Problems, 134 5-9.2 Converting Solar Cell Short-Circuit Current to a Voltage, 135	
		5-9.3 Current-Divider Circuit (Current-to-Current Converter), 136	
	5-10	Phase Shifter 137	
		5-10.1 Introduction, 137 5-10.2 Phase-Shifter Circuit, 138	
	5-11	Temperature-to-Voltage Converters 139	
		5-11.1 AD590 Temperature Transducer, 139 5-11.2 Celsius Thermometer, 140 5-11.3 Fahrenheit Thermometer, 140	
	5-12	Integrators and Differentiators 140	
		5-12.1 Integrators, 141 5-12.2 Servoamplifier, 142 5-12.3 Differentiators, 144	
	5-13	PSpice Simulation 146	
		Problems 148	
6	SIGN	AL GENERATORS	151
		Learning Objectives 151	
	6-0	Introduction 152	
	6-1	Free-Running Multivibrator 152	
		6-1.1 Multivibrator Action, 152 6-1.2 Frequency of Oscillation, 154	
	6-2	One-Shot Multivibrator 156	
		 6-2.1 Introduction, 156 6-2.2 Stable State, 156 6-2.3 Transition to the Timing State, 157 	

xii Contents

	6-2.4 Timing State, 157 6-2.5 Duration of Output Pulse, 159 6-2.6 Recovery Time, 159
6-3	Triangle-Wave Generators 160
	 6-3.1 Theory of Operation, 160 6-3.2 Frequency of Operation, 162 6-3.3 Unipolar Triangle-Wave Generator, 163
6-4	Sawtooth-Wave Generator 165
	6-4.1 Circuit Operation, 165 6-4.2 Sawtooth Waveshape Analysis, 165 6-4.3 Design Procedure, 165 6-4.4 Voltage-to-Frequency Converter, 167 6-4.5 Frequency Modulation and Frequency Shift Keying, 167 6-4.6 Disadvantages, 168
6-5	Balanced Modulator/Demodulator, the AD630 170
	6-5.1 Introduction, 170 6-5.2 Input and Output Terminals, 170 6-5.3 Input–Output Waveforms, 170
6-6	Precision Triangle/Square-Wave Generator 170
	6-6.1 Circuit Operation, 170 6-6.2 Frequency of Oscillation, 172
6-7	Sine-Wave Generation Survey 172
6-8	Universal Trigonometric Function Generator, the AD639 173
	6-8.1 Introduction, 173 6-8.2 Sine Function Operation, 173
6-9	Precision Sine-Wave Generator 175
	 6-9.1 Circuit Operation, 175 6-9.2 Frequency of Oscillation, 178 6-9.3 High Frequency Waveform Generator, 178
6-10	PSpice Simulation of Signal Generator Circuit 179
	 6-10.1 Free-Running Multivibrator, 179 6-10.2 One-Shot Multivibrator, 181 6-10.3 Bipolar Triangle-Wave Generator, 182 6-10.4 Unipolar Triangle-Wave Generator, 183
	Problems 185

	Conte	nts	xiii
7	OP AI	MPS WITH DIODES	187
		Learning Objectives 187	
	7-0	Introduction to Precision Rectifiers 188	
	7-1	Linear Half-Wave Rectifiers 189	
		 7-1.1 Introduction, 189 7-1.2 Inverting Linear Half-Wave Rectifier, Positive Output, 190 7-1.3 Inverting Linear Half-Wave Rectifier, Negative Output, 192 7-1.4 Signal Polarity Separator, 193 	
	7-2	Precision Rectifiers: The Absolute-Value Circuit 194	
		7-2.1 Introduction, 194 7-2.2 Types of Precision Full-Wave Rectifiers, 195	
	7-3	Peak Detectors 198	
		7-3.1 Positive Peak Follower and Hold, 1987-3.2 Negative Peak Follower and Hold, 200	
	7-4	AC-to-DC Converter 200	
		 7-4.1 AC-to-DC Conversion or MAV Circuit, 200 7-4.2 Precision Rectifier with Grounded Summing Inputs, 202 7-4.3 AC-to-DC Converter, 203 	
	7-5	Dead-Zone Circuits 203	
		 7-5.1 Introduction, 203 7-5.2 Dead-Zone Circuit with Negative Output, 203 7-5.3 Dead-Zone Circuit with Positive Output, 205 7-5.4 Bipolar-Output Dead-Zone Circuit, 208 	
	7-6	Precision Clipper 208	
	7-7	Triangular-to-Sine Wave Converter 208	
	7-8	PSpice Simulation of Op Amps with Diodes 209	
		 7-8.1 Linear Half-Wave Rectifier, 209 7-8.2 Precision Full-Wave Rectifier, 211 7-8.3 Mean-Absolute-Value Amplifier, 213 	
		Problems 215	

XIV Contents

3-0	Introduction 217
3-1	Basic Differential Amplifier 217
	8-1.1 Introduction, 217 8-1.2 Common-Mode Voltage, 219 8-1.3 Common-Mode Rejection, 220
3-2	Differential versus Single-Input Amplifiers 221
	8-2.1 Measurement with a Single-Input Amplifier, 2218-2.2 Measurement with a Differential Amplifier, 222
3-3	Improving the Basic Differential Amplifier 223
	8-3.1 Increasing Input Resistance, 223 8-3.2 Adjustable Gain, 223
3-4	Instrumentation Amplifier 226
	8-4.1 Circuit Operation, 2268-4.2 Referencing Output Voltage, 228
3-5	Sensing and Measuring with the Instrumentation Amplifier 229
	 8-5.1 Sense Terminal, 229 8-5.2 Differential Voltage Measurements, 230 8-5.3 Differential Voltage-to-Current Converter, 231
3-6	The Instrumentation Amplifier as a Signal Conditioning Circuit 233
	 8-6.1 Introduction to the Strain Gage, 233 8-6.2 Strain-Gage Material, 233 8-6.3 Using Strain-Gage Data, 234 8-6.4 Strain-Gage Mounting, 235 8-6.5 Strain-Gage Resistance Changes, 235
3-7	Measurement of Small Resistance Changes 235
	 8-7.1 Need for a Resistance Bridge, 235 8-7.2 Basic Resistance Bridge, 236 8-7.3 Thermal Effect on Bridge Balance, 237
3-8	Balancing a Strain-Gage Bridge 238
	8-8.1 The Obvious Technique, 238 8-8.2 The Better Technique, 238
3-9	Increasing Strain-Gage Bridge Output 239
3-10	Practical Strain-Gage Application 241
R-11	Measurement of Pressure Force and Weight 243

8-12 Basic Bridge Amplifier 243
8-12.1 Introduction, 243

		8-12.2 Basic Bridge Circuit Operations, 244 8-12.3 Temperature Measurement with a Bridge Circuit, 245 8-12.4 Bridge Amplifiers and Computers, 248	
	8-13	Adding Versatility to the Bridge Amplifier 248	
		8-13.1 Grounded Transducers, 248 8-13.2 High-Current Transducers, 248	
		Problems 249	
9	DC PE	ERFORMANCE: BIAS, OFFSETS, AND DRIFT	252
_		Learning Objectives 252	
	9-0	Introduction 253	
	9-1	Input Bias Currents 254	
	9-2	Input Offset Current 255	
	9-3	Effect of Bias Currents on Output Voltage 256	
		9-3.1 Simplification, 256 9-3.2 Effect of (-) Input Bias Current, 256 9-3.3 Effect of (+) Input Bias Current, 258	
	9-4	Effect of Offset Current on Output Voltage 259	
		 9-4.1 Current-Compensating the Voltage Follower, 259 9-4.2 Current-Compensating Other Amplifiers, 260 9-4.3 Summary on Bias-Current Compensation, 260 	
	9-5	Input Offset Voltage 261	
		 9-5.1 Definition and Model, 261 9-5.2 Effect of Input Offset Voltage on Output Voltage, 262 9-5.3 Measurement of Input Offset Voltage, 262 	
	9-6	Input Offset Voltage for the Adder Circuit 264	
		9-6.1 Comparison of Signal Gain and Offset Voltage Gain, 2649-6.2 How Not to Eliminate the Effects of Offset Voltage, 265	
	9-7	Nulling-Out Effect of Offset Voltage and Bias Currents 265	
		9-7.1 Design or Analysis Sequence, 265	