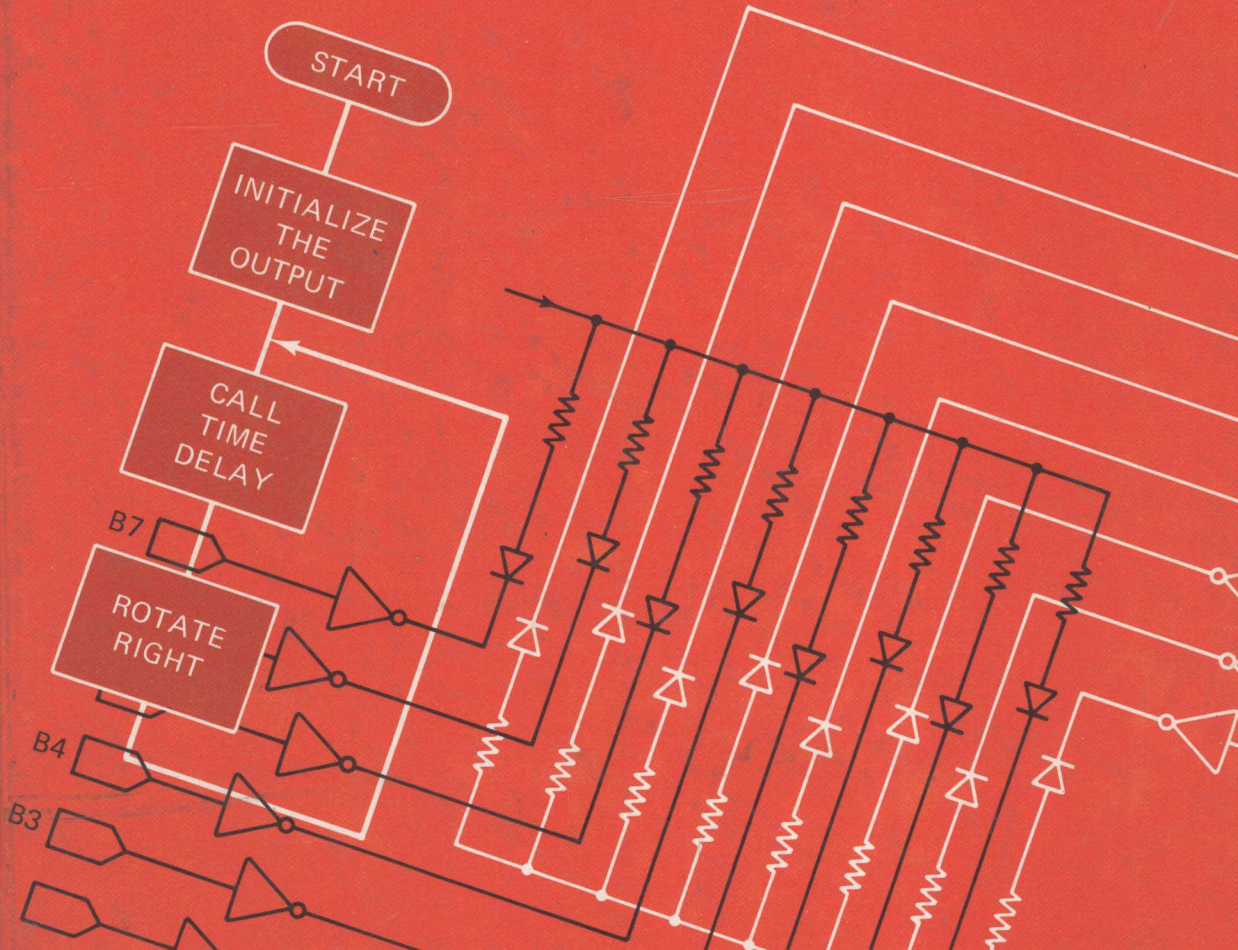


MICROPROCESSOR INSTRUCTION SETS AND SOFTWARE PRINCIPLES

David L. Heiserman

Provides a global view of microprocessor programming principles and summarizes four major classifications of microprocessor devices: Z-80, 8080A/8085, 6502, and 6800



Microprocessor Instruction Sets and Software Principles

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**Microprocessor
Instruction Sets
and
Software Principles**

Preface

No one can expect to work successfully with a microprocessor system without being fully acquainted with its instruction set and operating principles. A hardware engineer, for example, cannot expect to make intelligent decisions regarding the layout of the memory and I/O ports, a microprogrammer cannot hope to design the simplest software routines, and a technician cannot be expected to test, debug, and modify any part of the system.

Manufacturers of the various microprocessors have responded to the obvious need for describing their own programming principles and instruction sets. Indeed, through the first years of microprocessor technology, the manufacturers' own data books were the most popular references for teaching and learning the fundamentals of microprocessor programming.

But the maturing of microprocessor technology is making it inappropriate to define a well-prepared microprocessor engineer, programmer, or technician as one who has a thorough understanding of one manufacturer's system. It is becoming increasingly necessary to deal with diverse instruction sets and dissimilar programming operations. The rapid pace of new developments and, indeed, practical necessity are justifying the idea that it is time to cut across lines of traditional, one-manufacturer biases and adapt to new and different instruction sets and programming principles.

In an attempt to provide the broadest possible view of microprocessor programming techniques, this book deals with the instruction sets and fundamental programming procedures for four different devices: the Zilog Z-80, Intel 8080A/8085, MOS Technology 6502, and the Motorola 6800. These devices have been selected because they exemplify the similarities and differences one encounters when transferring some previous knowledge of one microprocessor system to another.

The reader, however, must not regard this book as a mere compilation of instructions sets for four different microprocessors. It is not simply four basic handbooks in a single volume. Rather, it is a tightly integrated presentation of microprocessor programming principles; and much would be lost in an attempt to treat the book as a source of information about a given microprocessor device.

The chapters are grouped according to categories of standard microprocessor instructions and operations: load immediate instructions, register transfer instructions, direct and indirect addressing operations, stack operations, arithmetic and logic functions, and so on. The individual chapters are generally organized in such a way that they present the most general principles first. Then one microprocessor is chosen as a model for demonstrating applications of those principles. Finally, the discussions show how these principles can be implemented with the three other types of microprocessor devices.

Relevant portions of the microprocessors' instruction sets are shown as needed in the text. Complete listings of all four instruction sets appear in the appendices as well.

Numerous examples throughout each chapter show specific applications of the principles being discussed. Those examples often demonstrate other important programming principles, such as the development of flowcharts and the organization of memory maps, and they encourage the reader to compare listings for several different microprocessors that are called upon to do the same overall task.

Where appropriate, exercises appear at the conclusion of sections within the chapters. The answers to most questions are included at the end of the book.

David L. Heiserman

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