

The Handbook of Data Communications and Computer Networks

Dimitris N. Chorafas



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Introduction

This handbook has been written as a graduate and professional text to help computer scientists and practitioners in the expanding field of data communications and computer networks. Prime importance has been given to interleaving theory with examples, thus allowing the reader to progress smoothly from concept to concept and one new term at a time.

We are living in an age when human knowledge doubles every 15 years. Every 10 years technology makes tremendous jumps, and the life cycle of 50 percent of our expertise is only 5 years. We must work and produce in this fast-changing environment, but our job is not to operate large machines or to build elegant systems. It is to solve business problems. Providing the most efficient software or hardware is only part of the problem. The *real* issue is how to make information work for the people who need it.

The starting point is understanding how information capabilities are received by and delivered to the end users. To do this requires a profound knowledge of the business—how it operates, and what the important issues are.

The implementation process of computers and communications includes:

Gaining management's recognition and acceptance.

Obtaining a commitment to the tangible and intangible costs of change.

Involving the end user in the design of change (he won't change unless he wants to).

Acquiring technologies that match needs and that people will accept.

Designing man-machine interfaces and limiting the impact on organizational structures and relationships.

Planning the gradual evolution of information technology within the organization.

Assuring that the benefit from computers and communications is tangible, factual and documented.

Keeping costs low so that benefits always exceed costs.

Implementing the change effectively (with the appropriate training and sales effort).

Training the user to the new system and demonstrating costs/benefits in a simple, efficient, understandable manner.

This handbook is divided into seven parts. Part One is devoted to microprocessor-based engines. It introduces the concept of distributed information systems; reviews the most advanced R&D efforts in semiconductor technology; shows how to gain cost/benefit; and leads the way to data processing and data communications.

Part Two is devoted to telecommunications. It explains the requirements; treats technologies such as space, frequency and time division multiplexing; examines switching technology and computer-based private branch exchanges (PBX); and includes such topics as transmission media (from coaxial cable to satellites), terminals, modems, multiplexers, concentrators, and frontends.

The theme of Part Three is protocols. What is a protocol; circuit switching and the polling/selecting option; bit-oriented protocols; the nesting of protocols; networking functions; X.25; session and presentation control; and the new standards for PLP.

Part Four concentrates on networks. Three generations of networks are presented; competitive offerings and international standards are explained; a discussion on backbone operations is followed by basic definitions for architectural design; functions and objectives in network architecture—including tradeoffs—are outlined; emphasis is placed on circuit, message, and packet switching.

Messages and transactions are the subjects to which Part Five addresses itself, including transaction-based systems, message theory, electronic mail, electronic funds transfer, videotex and its impact in the 1980s, and an American application of retail banking through Videotex.

Part Six underlines the software prerogatives: communications software, network operating systems, error detection and correction, and journaling.

Finally, Part Seven is devoted to network maintenance. The issues are life cycle maintainability, systems maintenance, the functions of a network control center, and network diagnostics and monitoring.

This choice of subjects has been made in the understanding that the rapid increase in the demand for data communications and computer networks has elicited a need for more efficient structures than those presently available.

Though we still learn on databases, we know beyond doubt that data communications, databasing, and data processing are converging. Before long, there will be no distinction between them at all. The common ground is distributed information systems. Its impact will transform our professional and personal interests.

Let me close by expressing my thanks to everyone who contributed to this book, from my colleagues for their advice, to the organizations I visited in my research for their insight; and to Eva-Maria Binder for the drawings and typing of the manuscript.

Valmer and Vitznau
Dimitris N. Chorafas

Contents

Introduction	xiii
PART ONE MICROPROCESSOR-BASED ENGINES	xvii
Chapter 1 Distributed Information Systems	1
One Microcomputer per Desk	1
Three Levels of Sophistication	5
Other Dimensions of DIS	6
Division of Labor	7
Alternatives: Vertical and Horizontal	9
Conclusion	12
Chapter 2 R&D and Semiconductor Technology	15
From Transistors to Biochips	16
The Pace of Microelectronics	19
An Inventory of New Images	21
Essence of Change	23
Market Characteristics	24
Chapter 3 Cost-Benefit with the New Technologies	29
Getting On-line	29
Decentralization and Centralization	32

	Facing the Constraints 33 Criteria for Choice 34	
	Cost Issues with Future Systems 37	
	Supporting Services and Management Decisions 40	
Chapter 4	From Data Processing to Data Communications	43
	User Levels in Data Communications 44	
	System Perspectives 46	
	The Three Main Classes of Communications 50	
	Electronic Message Systems 52 A Message Service: Ontyme 54	
PART TWO	TELECOMMUNICATIONS	57
Chapter 5	What is Telecommunications?	59
	The Investment 61 Communications Costs 63	
	Estimating Communications Requirements 64	
	Calculating Unit Charges 68 Expected Benefits 70	
	New Perspectives 71	
Chapter 6	Space, Frequency, and Time	73
	Space Division 74 Frequency and Time Division 75	
	Code Conversion 78 Time Slots and Alohanet 79	
	Time Assignment Speech Interpolation 81	
	Intelligent TDM 83 The Problem of Vocabulary 85	
Chapter 7	Switching Technology and the PBX	87
	Switching Functions 88	
	Quality Service and the Erlang Algorithm 90	
	Analog and Digital Private Branch Exchanges 93	
	Cost Control and Service Features with the PBX 95	
Chapter 8	Transmission Media: From Coaxial to Satellites	99
	Coaxial Cable Installations 100	
	Experiences with Cable Television 102	
	Optical Fibers Technology 105	
	Communications by Satellite 107	
	Coaxial vs. Satellite Transmission 109	
Chapter 9	Terminals	115
	The Use of Terminals 116 Building Blocks 119	
	Transmission Speed and Interfacing 122	

	The Line Controller 123	
	Computers, Microcomputers, Microprocessors, and Terminals 126	

Chapter 10	Modems	131
-------------------	---------------	-----

	The Use of Modems 132 Noise and Line Losses 135	
	Technical Issues with Modem Selection 136	
	Agile Modems 140 The User Site 141	
	Standard Interfaces: RS 232, RS 422, and RS 449 142	

Chapter 11	Multiplexers, Concentrators and Front-ends	151
-------------------	---	-----

	The Process of Multiplexing 152	
	Betting on Concentrators 154	
	Assisting the Central Resources 159	
	Breaking Down the Front-end Tasks 161	
	The Data Communications Machine 165	

	PART THREE PROTOCOLS	171
--	----------------------------------	-----

Chapter 12	What is a Protocol?	173
-------------------	----------------------------	-----

	Protocol Reliability 174 Access Methods 176	
	Procedural Requirements 178	
	Synchronous and Asynchronous Transmission 178	
	Synchronous Protocols 180	
	Terminals for Synchronous and Asynchronous Protocols 183	

Chapter 13	Circuit Switching and the Polling/Selecting Option	187
-------------------	---	-----

	Circuit Switching Principles 188	
	Control Procedures: Fixed, Switched, and Multipoint 189	
	The Coming Possibilities 194	

Chapter 14	Bit-Oriented Protocols	197
-------------------	-------------------------------	-----

	The Data Link 198	
	The Fields of xDLC 202 Frame Sequence and	
	Acknowledgment 207	
	Using a Bit-Oriented Protocol in a Loop 208 Objectives of xDLC 211	

Chapter 15	The Nesting of Protocols	215
-------------------	---------------------------------	-----

	Standards for the Physical Circuit 216	
	Second-Level Protocols 217	

	Framing and Link Management	218
	The Higher Levels	219 Applications Protocols 222
Chapter 16	Networking Functions	225
	Routing	226 Virtual Circuits and Datagrams 230
	Choosing Virtual Circuits or Datagram	232
	Connection and Transmission	233
	Node-to-Node Protocols	234
	The IMP-to-IMP Protocol	235
	The IMP-to-Host Protocol	236 Host-to-Host Protocols 236
Chapter 17	X.25	237
	What is X.25?	238
	The Communications Session	241
	Comparing Protocols	242 Flow Control 245
	Goals in Flow Control	247
	Congestion Control	247 Internetworking 248
Chapter 18	Session and Presentation Control	251
	The Purpose of Session Control	253
	ANSI and Session Control	255 Presentation Control 256
	A Process Level Protocol	258
Chapter 19	Standards for Presentation Level Protocol	261
	The Observance of Protocols	261
	Presentation Control	263
	The CEPT Standard	266 The PLP Standard 267
	Handling Graphics	273 International Coordination 276
PART FOUR	NETWORKS	281
Chapter 20	Three Generations of Networks	283
	The Early Periods	284 Point-to-Point 284
	The First Generation	285 The Second Generation 287
	Merging Technologies	288 Network Interfaces 292
	The Third Generation	294
Chapter 21	Competitive Offerings and International Standards	299
	The ISO/OSI Model	301
	A System Network Architecture?	305

	SNA Compatibility and User-Oriented Supports	307
	The Encryption Standard	308
	Using Value-Added Networks	309
Chapter 22	Toward Local Area Solutions	313
	Assuring Connectivity	314
	Benefits from a Packet Switching Network	316
	The Local Area Network Concept	318
	A Relative Standardization	320
	Cost/Effectiveness with LAN Solutions	321
Chapter 23	Backbone Operations	325
	Topological Description	326
	Topological Solutions	327
	Sender and Receiver	329
	Sharing Tasks	331
	Datapac—An Example of Network Service	332
	Gateways	334
Chapter 24	Basic Definitions for Architectural Design	337
	Systems and Network Architecture	337
	The Nodes of a Network	339
	Basic Definitions	341
	Projecting an Architecture	344
	Conclusion	345
Chapter 25	Functions and Objectives in Network Architecture	347
	Layered Solutions	347
	Duties of the Network Architect	350
	Protocols and Interfacing	351
	The Grand Design	352
	Impact at the Workplace	354
Chapter 26	The Tradeoffs	357
	Network Requirements	358
	The Seven Basic Steps	361
	Layered Communications Principles	365
	Implementing a Layered Solution	368
Chapter 27	Circuit, Message and Packet Switching	371
	Circuit Switching	373
	Message Switching	378
	Packet Switching	381
	Comparison of the Three Basic Methods	383
PART FIVE	MESSAGES AND TRANSACTIONS	385
Chapter 28	Transaction-Based Systems	387
	The Transaction Network Service	387
	The Choice of Facilities	389

	Types of Communications Links	391
	Change in Structure	393
	System Requirements	395
Chapter 29	Message Theory	397
	Message Technology	397
	Principles of Message Systems	398
	Conventions	401
	Performance Criteria	402
	The User's Requirements	403
	Examples with Message Systems	404
Chapter 30	From Electronic Mail to Funds Transfer	407
	A Telemail Application	408
	Operating Through Workstations	413
	Perspectives in Electronic Funds Transfer	414
	EFT in the Supermarket	416
	Foreign Payments Procedure	417
Chapter 31	Videotex in the 1980s	423
	Personal Computers and Videotex	423
	Developing Videotex Systems	425
	The Mechanics of a Videotex Facility	426
	Connections, Transmission, Disconnection	429
	Lessons from the Market Trial	435
Chapter 32	Retail Banking Through Videotex	437
	Subscriber Agreement at the First Bank System	438
	Placing Emphasis on Marketing	440
	The Database and the Network	442
	Videotex Payments by the Bank of America	445
PART SIX	SOFTWARE PREROGATIVES	447
Chapter 33	Communications Software	449
	Developing the Software	451
	Software Functions	452
	Downline Loading, Upline Dumping, and Loopback	456
	Database Support	457
	System Design Requirements	458
	The Implementation Schedule	460
Chapter 34	A Network Operating System	463
	Designing a Basic Operating System	464
	The Software "Constant"	466
	Operating Systems Background	468

Executive Functions	471	System Management	472
Data Maintenance	472	Terminal Handling	472
The Server System	474	Distributed Operating Systems	477

Chapter 35 Error Detection and Correction 479

Errors and the System View	480	Error Reduction	480
An Integrated Approach to EDC	482		
The Cyclic Redundancy Check	483	Bit Error Rate	483
Error Rate and the Dependability of Carriers	485		
Control of Errors on Voice-Grade Lines	486		
Testing the Network	488		

Chapter 36 Journaling 489

The Journal is the Means	491
Procedural Prerequisites	492
Distributed Recovery and Restart	493
Failure Isolation	496
Security, Privacy, and Auditability	498

PART SEVEN NETWORK MAINTENANCE 501

Chapter 37 Life Cycle Maintainability 503

Requirements for Life Cycle Performance	506
Availability and Reliability	508
Software Reliability	511
Portability	515

Chapter 38 Systems Maintenance 521

Time of Systems Interrupt	521
Preventive Maintenance	525
Repairability	527
Diagnostic Centers	528
Do-It-Yourself Maintenance	530

Chapter 39 The Network Control Center 533

Environmental Factors	535
A Maintenance Architecture	537
Implementing the Maintenance Architecture	540

Chapter 40 Network Diagnostics and Monitoring 543

Applications with Remote Diagnostics	545
A Case Study with the Danish Savings Banks	550

1

Distributed Information Systems

Technology has advanced to the point where computer power is economically available at all levels of company operations. Distributed information systems are tools which enhance the productivity of organizations with widespread operating facilities. Productivity gains can only be expected, however, if system tools are used easily, by authorized individuals, for a variety of purposes, and can be adapted readily as requirements change.

Distributed information systems, DIS, increasingly draw attention, yet have a meaning that is not totally understood. DIS is an evolution from centralized processing, which in turn evolved from "free-standing" modes. Although it combines certain strengths of its two predecessors, DIS is unique in its own right. It represents a break from the past.

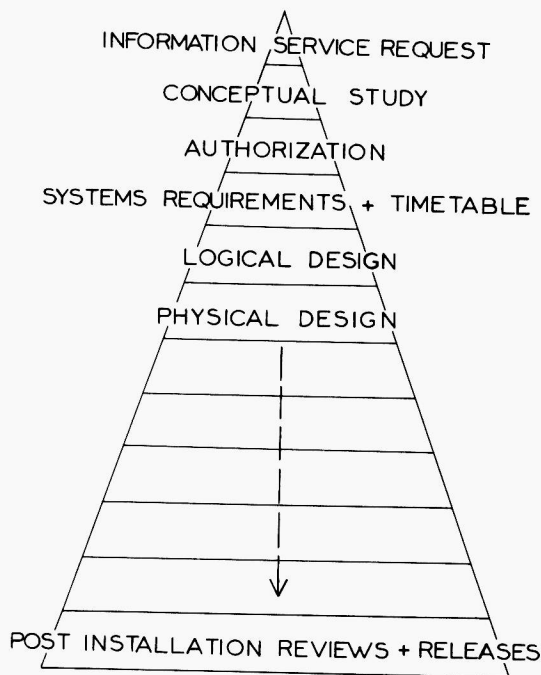
The implementation of a distributed information system rests both on Logical and on Physical premises. The logical functions center on the procedural design for channeling the flow of information, and controlling the physical faculty throughout the projected systems configuration. The object of the physical functions is the engineering of the hardware and hard software devices to provide a specific level of capability.

Figure 1.1 outlines the chronological order of development from the information service request to the logical and the physical design phases. The information service request can be motivated by the need for cost reduction in data processing, clerical operations or other sectors, and/or by requests for improving the current information handling service and bringing it nearer to the user.

We shall take factual and documented examples to back up these references.

One Microcomputer per Desk

Typically, in the 1950s and 1960s, information handling was centralized, batch-oriented and costly, and encountered an inordinate number of errors and delays.

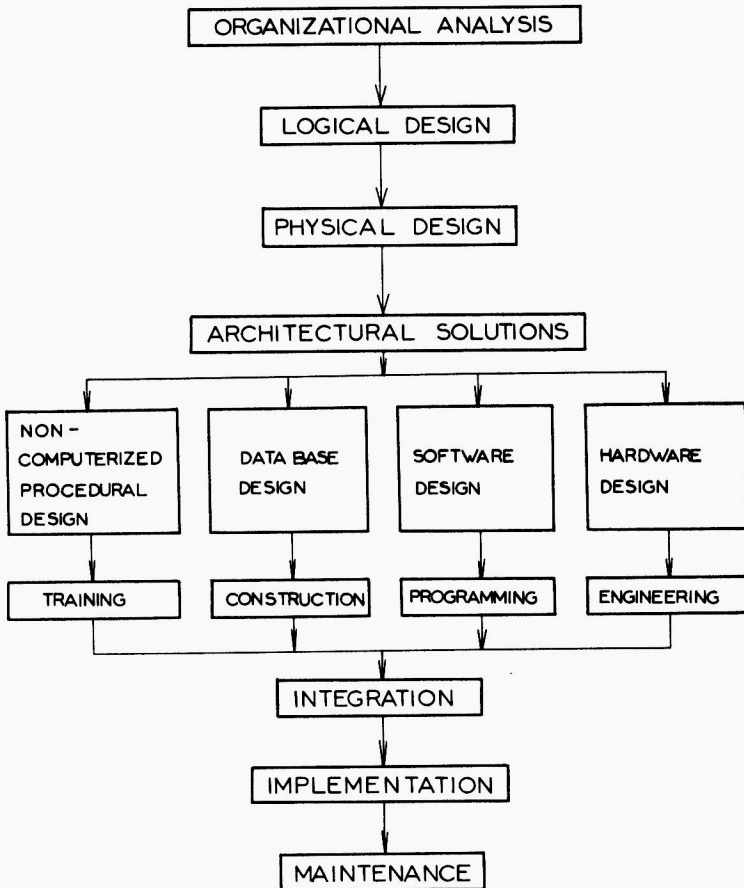
Figure 1.1

By the late 1960s there was plenty of evidence that this state of affairs was not satisfactory. Management needed to do something about this situation, and knew it. It was assisted in opening a new frontier by the system analysts' own realization that new ways were badly needed to master complex information systems.

In the late 1960s information system designers started to realize that software projects could not (and should not) always be behind schedule, taking more memory than planned, costing several times more than the original estimate, and performing less than projected. But it took another ten years to bring into perspective the fact that such results were the direct effect of underestimating complexity: there is no one-dimensional way from systems analysis to programming, but a four-dimensional approach past the architectural level involving noncomputerized procedural design, data base (DB) organization, software design, and hardware engineering. Figure 1.2 gives a snapshot of this organization. We shall return to it later at greater length.

The second vital element in opening the new frontier has been brought about by the cutting edge of technology which made it possible to bring computer power to the workplace at a lower cost than the previous mammoth solutions. Incorrectly called "minicomputers," this generation of information equipment made feasible both the division and the multiplication of computer power. Introduced in the mid-1960s, the minicomputer (based on semiconductors) exploded in the 1970s into three families each with its own market and price range: the maximini (or

Figure 1.2



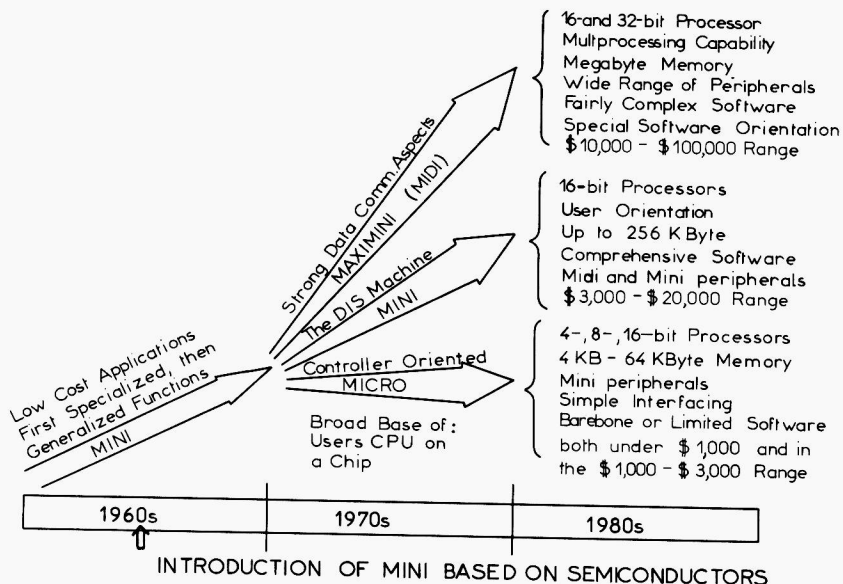
midi), the mini proper, the micromini. Figure 1.3 shows the perspectives for the 1980s, and the specific orientation of each of these markets.

Technologically, the evolution of the maxicomputer resembled the “big motor” approach with the main drive shaft which we had in factories at the beginning of the Industrial Revolution. The evolution of the mini can be considered as a data handling counterpart to that of small motors (fractional power): “one motor per tool,” “one mini per office.”

For the 1980s, the master of the marketplace is the personal computer (PC). Microprocessor-based, with capabilities which exceed those of 1960 mainframes (and, in some cases, of big computers of the 1970s), the PC has gone through an evolution characterized so far by two generations.

1. The first generation of PCs came onto the market between 1976 and 1979, but began to receive major attention only since 1980.

Figure 1.3
Introduction of minicomputers based on semiconductors



Typically, these are 8 bit-per-word (BPW) engines; have a central memory (CM, high speed memory, semiconductor memory) of 16 kilobytes (KB); use for external storage a cassette, eventually changing to floppy disc; have native support for a 40-column video, or 80 columns through a special printed circuit board (PCB); and have no capability for networking. (The latter can also be added through a PCB.)

2. The second generation of PCs entered the market after 1981 and attracted immediate market interest.

The central processing unit (CPU) of this generation is characterized by two types of commercially available microprocessors, though a few machines feature special LSI (large scale integration) semiconductor units or other than the two leading microprocessors:

the Intel 8086, with 16 BPW and a 16-bit bus (though some PCs have the Intel 8088 with 16 BPW and an 8-bit bus), and

the Motorola 68000, with 32 BPW and a 16-bit bus.

Though comparisons in terms of CPU power are quite abstract unless one knows and considers the machine's internal design, there is as an order of magnitude—a 30% rule—for comparison, with other things being equal. Benchmarks which I have done tend to indicate that, for currently available microprocessors,