

LOGICAL SYSTEMS FOR INDUSTRIAL APPLICATIONS

by

JERZY JACZEWSKI

Technical University of Gdańsk, Gdańsk, Poland

ELSEVIER SCIENTIFIC PUBLISHING COMPANY Amsterdam—Oxford—New York PWN—POLISH SCIENTIFIC PUBLISHERS Warszawa 1978 Revised and enlarged translation from the Polish original

Układy logiczne dla zastosowań przemysłowych

published in 1970 by Państwowe Wydawnictwo Naukowe, Warszawa

Translated by Tadeusz Przybylski Graphic design: Zygmunt Ziemka

Distribution of this book is being handled by the following publishers:

For the U.S.A. and Canada ELSEVIER/NORTH-HOLLAND, INC. 52 Vanderbilt Avenue New York, New York 10017

For Albania, Bulgaria, Chinese People's Republic, Czechoslovakia, Cuba, German Democratic Republic, Hungary, Korean People's Democratic Republic, Mongolia, Poland, Rumania, the U.S.S.R., Vietnam and Yugoslavia

ARS POLONA

Krakowskie Przedmieście 7, 00-068 Warszawa, Poland

For all remaining areas
ELSEVIER SCIENTIFIC PUBLISHING COMPANY
355 Jan van Galenstraat
P.O. Box 211, Amsterdam, The Netherlands

Library of Congress Cataloging in Publication Data Jaczewski, Jerzy.

Logical systems for industrial applications.

Translation of Układy logiczne dla zastosowań przemysłowych.

Includes bibliographical references and index.

1. Logic circuits. 2. Logic design. 3. Sequential

machine theory. 4. Digital control systems. I. Title. TK7868.L6J3213 629.8'91 77-11173

ISBN 0-444-99804-7

Copyright (C) 1978 by PWN—Polish Scientific Publishers—Warszawa

All rights reserved.

No part of this publication may be reproduced, stored in retrieval system or transmitted in any form or by any means, electronic, mechanical, photocopying, recording, or otherwise, without the prior written permission of the publisher.

Printed in Poland by D.R.P.

Preface to the Polish Edition

The automation of industry, transport, services and homes involves a large variety of mechanical, electrical, electronic, hydraulic, pneumatic, and other types of elements and components. No less varied are the methods of automation—ranging from signalling, relay protection, and constant value control, to remote control and adaptive and learning systems. However, among the multitude of control systems we can distinguish certain functional sub-systems which are common to all of them, namely:

setting devices (which set the required control system in operation)
measuring devices (transducers)

measuring devices (transducers)

decision devices (controllers—which generate sets of signals actuating

servo-systems)

actuators (servo-mechanisms—which execute the control algo-

rithm).

This book deals with a particular class of decision devices called *logical systems*. These are systems whose operating rule or algorithm may, in principle, although not exclusively, be described by means of declarative sentences which express propositions concerning the operation of the system using the *laws of binary logic*. The elements from which the system is built are usually, therefore, *two-state* elements. We are thus dealing here with discontinuous (discrete) control in contradistinction to continuous (analogue) control.

This book is intended for engineers and for students of technical universities. Its purpose is to give the reader a better idea of the new concepts in the theory of finite automata which are applied extensively in the design of more complicated logical systems, especially those with memories. Logical systems also form the basis for digital techniques, the use of which is steadily increasing. The theory of finite automata involves the use of branches of mathematics generally less familiar to industrial design engin-

eers. This book therefore endeavours to explain the fundamental concepts and methods of this theory and the principles of their application to the design of systems for industrial purposes. For this reason, problem-formulation principles based on the classical propositional calculus are given in Chapters 1, 2, and 3. These chapters are of a theoretical nature and thus, in them, the notation of a Boolean function, for example, is not related to its technical realization. Many binary combinational tables, matrices, and graphs are given to enable the reader to gain a thorough understanding of the methods described.

The problem of minimizing Boolean functions and graphs illustrating the operation of the logical system have deliberately been omitted from Chapters 1, 2, and 3. In my view, the essential problem is that of system optimization, this being understood as a compromise between minimization of the purchase cost of the equipment and maximization of the system's reliability. The design of a system which is optimal for a given performance factor (quality index) depends to a great extent on the choice of system of functors and logical operators and also on the structure of the system. For this reason, a wide variety of elements and two-state components, which are the subject of intensive research and development in technically advanced countries, is described in Chapters 4 and 5. At the present level of technological sophistication, semiconductor elements and systems of both multi-element and monolithic logical functors composed of these play a fundamental role. The development of monolithic and thin-film elements of high packing density and increased reliability puts a new slant on the fashionable problem of minimization.

Quite deliberately, I have devoted only limited space to such popular devices as electromechanical relays. These have been described in many papers and their use will decrease in the future. On the other hand, attention is drawn to the most recent development trends, such as optoelectronics, superconductivity and semiconductors with discontinuous characteristics. Considerable attention has also been devoted to pneumatic elements and to systems which underwent intensive development in the 'sixties; they may become important particularly in industrial or other special applications.

Chapter 6 is devoted to system design and optimization, which can be carried out only after proper formulation of the problem and choice of method for its solution.

The references listed at the end of the book are, for the most part, publications to which the author had recourse and cover the period up to 1968,

when the final manuscript was completed. A short reference list is appended at the end of each chapter.

The author wishes to express his thanks to all those who enabled this book to be written and in particular to J. C. Gille, J. Kuntzmann, J. Lagasse and P. Naslin of France for their comments and help, to Professor W. Szukszta for his detailed criticisms, and to Professor W. Findeisen for his highly valued remarks.

Gdańsk, 1968

Jerzy Jaczewski

Preface to the English Edition

The English edition of this book is being published nine years after the completion of work on the Polish text. During this period extremely rapid development of semiconductor integrated circuits (IC, MSI, LSI), and further development of the theory of automata and logical systems took place. The use of digital techniques, mini-computers, and calculators in industry has become widespread. The English edition is thus a revised, enlarged, and up-dated version of the Polish one. It also incorporates the teaching experience of the author and his colleagues, as the Polish edition served as a text-book for both degree and post-graduate courses.

The division of the book into three basic parts has been retained: I—The Abstract Theory of Logical Systems (Chapters 1, 2, 3); II—Realization of Logical Functors and Operators. Systems (Chapters 4 and 5); III—Design of Logical Systems (Structural Synthesis) (Chapters 6, 7, 8, 9). In Part I the author has made several additions to Chapter 2, such as Zhegalkin algebra, Boolean function derivatives, three-valued algebra; Chapter 3 has been rewritten and supplemented with language representations, non-deterministic automata, and composition of automata. In Part II, Chapter 5 on integrated circuits has been completely rewritten. Part III has undergone extensive changes. The previous Chapter 6 has been split up into four chapters and the sequence of the text has changed. In the author's opinion, it is better first to outline logical system design as a whole and then discuss the design of combinational logical systems or logical systems with memory. The two final Chapters 8 and 9 differ substantially from the corresponding chapters in the Polish edition and take into account the development of integrated circuits.

As before, it has been the intention of the author to present the subject while using only a small number of methods and to afford as similar a treatment as possible to logical systems with memory and combinational logi-

cal systems. Since the main purpose of the book is to facilitate self-education, an extensive list of references is given. The references reflect the sources used by the author; for the most part, the works and papers are from Central and Eastern Europe. The author believes that the presentation of these achievements to the English-speaking reader is one of the merits of the book.

In conclusion, the author wishes to express his thanks to all who helped to make this English edition possible: to the publishers for their initiative; to the translator, Dr T. Przybylski, for his hard work and kind co-operation; and to Mr A. Delacour of UNESCO, for his kind forbearance without which this text could not have been written.

Gdańsk 1977

Jerzy Jaczewski

Contents

Preface to the Polish Edition, v

Preface to the English Edition, viii

Part I. The Abstract Theory of Logical Systems

Chapter 1. Introduction, 3

- 1.1 Processing of information signals, 3
- 1.2 Symbolic notation for the control algorithm. Mathematical logic, 7
- 1.3 Multi-valued logic (non-chryssipian), 19

Chapter 2. Boolean Algebra. Logical Systems without Memory (Combinational — CLS, 23

- 2.1 Introductory remarks, 23
- 2.2 Boolean algebra, 23
- 2.3 Interpretation of Boolean algebra in the algebra of sets, 28
- 2.4 Synthesis of a Boolean function, 32
 - 2.4.1 Combination table. Combination tree, 32
 - 2.4.2 Synthesis of a Boolean function. Canonical forms, 35
 - 2.4.3 An example of synthesis. Proof of the equivalence of the two canonical forms, 37
 - 2.4.4 Reduction of any form of a function to the canonical form, 38
- 2.5 Complete sets of functions. Types and classes of functions, 44
- 2.6 Minimum systems of functors. Zhegalkin algebra, 50
- 2.7 Other ways of representing Boolean functions, 53
 - 2.7.1 Diagram of a logical system, 53
 - 2.7.2 Venn diagrams, 55

- 2.7.3 N-dimensional space (hypercube), 57
- 2.7.4 The Karnaugh map and other logical tables, 62
- 2.7.5 Graphs and matrices, 66
- 2.8 Boolean function systems. Operations on Boolean functions, 72
 - 2.8.1 Boolean function systems. Multi-output logical systems, 72
 - 2.8.2 Operations on Boolean functions, 73
 - 2.8.3 Superposition. Decomposition, 74
 - 2.8.4 Derivatives of Boolean functions, 76
- 2.9 Three-valued algebra, 79

Chapter 3. Digital Finite Automata. Logical Systems with Memory (LSM), 82

- 3.1 Discrete automatic process control, 82
- 3.2 The present state in automata theory, 85
- 3.3 Mealy and Moore models, 87
- 3.4 Methods of representing the properties of an automaton, 90
 - 3.4.1 Tabular representations, 90
 - 3.4.2 Geometrical representations, 93
 - 3.4.3 Linguistic representations, 97
- 3.5 Classification of digital automata. Special types of automata, 105
 - 3.5.1 Classification of deterministic digital automata, 105
 - 3.5.2 Non-deterministic automata, 109
- 3.6 Elementary automata. Elementary logical systems with memory (LSM), 111
 - 3.6.1 Elementary automata, 111
 - 3.6.2 Elementary LSM, 113
- 3.7 Composition of automata, 116
- 3.8 Examples of logical systems with memory and their transition graphs, 117
- 3.9 Summary, 124

Part II. Realization of Logical Functors and Operators. Systems

Chapter 4. Realization of Logical Functors and Operators, 127

- 4.1 General requirements, 127
- 4.2 Characteristics of functors, 129
 - 4.2.1 Principles of assigning a logical value to a state of a physical quantity, 129

- 4.2.2 Static characteristics, 132
- 4.2.3 Dynamic characteristics, 134
- 4.3 Characteristics of memory units (operators), 136
- 4.4 Survey of two-state units, 137
 - 4.4.1 Fundamentals, 137
 - 4.4.2 Units utilizing the flow of electrical energy, 138
 - 4.4.3 Fluidic elements (hydraulic and pneumatic), 172
 - 4.4.4 Miscellaneous, 181
- 4.5 Memory and delay units, 186
- 4.6 Specification and classification of the properties of bistable units, 187

Chapter 5. Industrial Systems of Logical Functors and Operators (SLFO's), 190

- 5.1 Definitions. Preliminary data, 190
- 5.2 Development of semiconductor SLFO's, 192
- 5.3 Choice of basic functors in semiconductor SLFO's, 201
- 5.4 Technical parameters, 203
 - 5.4.1 Parameters of logical modules, 204
 - 5.4.2 Noise immunity, 209
- 5.5 Survey of transistor systems realizing SLFO, 212
 - 5.5.1 Logical interpretation of the operation of transistor systems, 212
 - 5.5.2 Basic operating circuits of the bipolar transistor, 214
 - 5.5.3 Basic operating circuits of the unipolar fleld-effect transistor, 218
 - 5.5.4 Functor-realizing circuits, 223
 - 5.5.5 Circuits realizing MEMORY and DELAY operators, 231
- 5.6 Adaptive and auxiliary modules, 238
- 5.7 Examples of SLFO's, 239
- 5.8 Examples of pneumatic SLFO's, 241

Part III. Design of Logical Systems (Structural Synthesis)

Chapter 6. Economic Aspects of Logical System Design, 251

- 6.1 Introductory remarks, 251
- 6.2 Conventional cost of a logical system, 253
- 6.3 The form of the Boolean function and the system structure, 255

xiv CONTENTS

- 6.4 The annual costs of logical systems, 257
- 6.5 Decision models and design operation networks, 261

Chapter 7. The Reliability of Logical Systems, 267

- 7.1 Introductory remarks. The logical reliability of CLS's and LSM's, 267
 - 7.1.1 The logical reliability of CLS's, 268
 - 7.1.2 The logical reliability of LSM's, 275
- 7.2 The engineering reliability of electronic logical systems, 282
 - 7.2.1 Fundamental concepts, 282
 - 7.2.2 Operating life, 287
 - 7.2.3 The reliability of equipment, 288
 - 7.2.4 The effect of load and environmental factors on reliability, 292
 - 7.2.5 The principles of improving system reliability. Policies and redundancies, 295
- 7.3 Increasing the reliability of logical systems. Selected aspects, 304

Chapter 8. Design of Logical Systems Without Memory (CLS), 310

- 8.1 Introduction, 310
- 8.2 Synthesis of a Boolean function with a one-functor SLFO (NAND or NOR), 311
- 8.3 Minimization of the conventional cost of a single-output CLS (n > 1, m = 1), 322
 - 8.3.1 Introduction, 322
 - 8.3.2 Prime implicants: definitions and problems, 323
 - 8.3.3 The Quine-McCluskey algorithm, 327
 - 8.3.4 The iterated consensus method, 331
 - 8.3.5 Choice of optimum cover, 335
 - 8.3.6 Special problems, 338
- 8.4 Comments on design by the decomposition method, 343
- 8.5 Minimization of the conventional cost of a multi-output CLS (m > 1), 345

Chapter 9. Design of Logical Systems with Memory (LSM), 352

- 9.1 Introduction, 352
- 9.2 Elementary operators and their structural equations, 354

CONTENTS XV

9.3 Modifications of the properties of the elementary operators, 357

- 9.4 Structural synthesis of small timed (synchronous) systems, 364
 - 9.4.1 The number of indispensable operators and the coding problem, 364
 - 9.4.2 Structural equations, 367
 - 9.4.3 Optimization of LSM's, 372
 - 9.4.4 Realization of LSM's by means of the universal LSI-MOS structure, 377
- 9.5 Large timed systems and decomposition problems, 378
- 9.6 Structural synthesis of untimed (asynchronous) systems, 380
 - 9.6.1 Description of an untimed system, 380
 - 9.6.2 The signal race problem, 384
 - 9.6.3 Realization of LSM's by means of the universal LSI-MOS, 386
- 9.7 Special types of LSM's, 389
 - 9.7.1 Counters, 389
 - 9.7.2 Shift registers, 392
 - 9.7.3 Linear automata, 393
- 9.8 Comments on dynamic realizations, 396

Appendix A. Minimization of the Number of States, 398

Appendix B. Use of Dynamic Signals, 421

Appendix C. Intuitive Design of LSM's with a Single Feedback Loop, 424

References, 427 References Selected from the Polish Edition, 438 Subject Index, 447 I. The Abstract Theory of Logical Systems

1. Introduction

1.1 Processing of information signals

Automatic control of a machine or process consists in control signals affecting the flow or state of energy of the system being controlled. The rule according to which the signals exert this influence is called the *control algorithm*. As a prerequisite for automatic control, data concerning certain physical quantities which may change during the operation of the system must be available.

Information about physical processes comes from measuring transducers in the form of signals which represent the measured quantities of interest to us. A *signal* is here understood to be some physical means of transmitting information about the physical quantity. Every signal possesses the following three types of features: (a) qualitative—mechanical, electrical, thermal, etc., (b) quantitative—value in appropriate units, (c) morphological—form. The choice of signal quality and morphology is not arbitrary.

A signal must be transmitted and frequently has also to be converted. During this period it is subject to *internal* and *external interference* with the result that the information may be distorted. The susceptibility to interference varies with the form of signal. The choice of form of the signal also depends on the *time* allowed for information *transmission* or *conversion*. The *reliability* of transmission and conversion (reliability of equipment), as well as *investment* and *operating costs*, must also be taken into account. At the same time, one cannot overlook such factors as the possibility of data being *stored* for later use and the possibility of manual or automatic *modification of the control algorithm* (adaptive or learning systems). Consideration of all these factors implies that under given conditions certain forms of signal are more suitable than others.

The signals may be continuous (sometimes called *analogue*) or may be discontinuous, i.e., discrete. In this book we shall be concerned only with

discrete signals. An explanation is in order as to why data are frequently transmitted or processed in discrete form, although in macroscopic phenomena all temporal processes are, in principle, continuous. To begin with, the most important factor is the loss of part of the information in the change from continuous to discrete form.

Consider the course of information from source (transmitter) to receiver. The conversion of the signal from continuous to discrete form is based

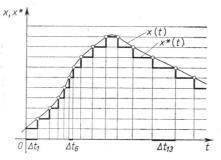


Fig. 1.1. Level quantization, that is, adaptive conversion of a continuous function x(t), yields a step curve $x^*(t)$. Here, $\Delta t_i = \text{var}$, for example $\Delta t_6 < \Delta t_{13}$.

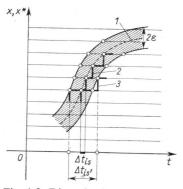


Fig. 1.2. Discretization errors due to measurement error band, 2ε in width. 1 - x(t) mean; $2 - x^*(t)$ obtained by quantization of mean curve; $3 - x^*(t)$ obtained on the basis of the error band as extreme case $(\Delta t_{ls'})$.

on quantization at the signal level (Fig. 1.1). The error with which the signal is measured is smaller than the quantum of measurement. Note that every measurement contains some random errors so that the measurement gives an interval inside which the true value lies instead of the exact value. Since the probability of positive and negative measuring errors is the same, the