Laurence T. Yang Xingshe Zhou Wei Zhao Zhaohui Wu Yian Zhu Man Lin (Eds.)

Embedded Softwar and Systems

Second International Conference, ICESS 2005 Xi'an, China, December 2005 Proceedings



Laurence T. Yang Xingshe Zhou Wei Zhao Zhaohui Wu Yian Zhu Man Lin (Eds.)

Embedded Software and Systems

Second International Conference, ICESS 2005 Xi'an, China, December 16-18, 2005 Proceedings





Volume Editors

Laurence T. Yang Man Lin

St. Francis Xavier University
Department of Computer Science
Antigonish, NS, B2G 2W5, Canada

E-mail: {lyang,mlin}@stfx.ca

Xingshe Zhou

Yian Zhu

Northwestern Polytechnical University

No. 127 West Youyi Road, P.O. Box 404

Xi'an City, Shaanxi Province, 710072, China

E-mail: {zhouxs,zhuya}@nwpu.edu.cn

Wei Zhao

Texas A&M University, Department of Computer Science College Station, TX 77843-1112, USA

and

National Science Foundation, Division of Computer and Network Systems

4201 Wilson Blvd, Arlington, VA 22230, USA

E-mail: w-zhao@tamu.edu

Zhaohui Wu

Zhejiang University

College of Computer Science

Hangzhou, Zhejiang Province, 310027, China

E-mail: wzh@zju.edu.cn

Library of Congress Control Number: 2005937056

CR Subject Classification (1998): C.3, C.2, C.5.3, D.2, D.4, H.4

ISSN 0302-9743

ISBN-10 3-540-30881-4 Springer Berlin Heidelberg New York ISBN-13 978-3-540-30881-2 Springer Berlin Heidelberg New York

This work is subject to copyright. All rights are reserved, whether the whole or part of the material is concerned, specifically the rights of translation, reprinting, re-use of illustrations, recitation, broadcasting, reproduction on microfilms or in any other way, and storage in data banks. Duplication of this publication or parts thereof is permitted only under the provisions of the German Copyright Law of September 9, 1965, in its current version, and permission for use must always be obtained from Springer. Violations are liable to prosecution under the German Copyright Law.

Springer is a part of Springer Science+Business Media

springeronline.com

© Springer-Verlag Berlin Heidelberg 2005 Printed in Germany

Typesetting: Camera-ready by author, data conversion by Scientific Publishing Services, Chennai, India Printed on acid-free paper SPIN: 11599555 06/3142 5 4 3 2 1 0

Commenced Publication in 1973
Founding and Former Series Editors:
Gerhard Goos, Juris Hartmanis, and Jan van Leeuwen

Editorial Board

David Hutchison

Lancaster University, UK

Takeo Kanade

Carnegie Mellon University, Pittsburgh, PA, USA

Josef Kittler

University of Surrey, Guildford, UK

Jon M. Kleinberg

Cornell University, Ithaca, NY, USA

Friedemann Mattern

ETH Zurich, Switzerland

John C. Mitchell

Stanford University, CA, USA

Moni Naor

Weizmann Institute of Science, Rehovot, Israel

Oscar Nierstrasz

University of Bern, Switzerland

C. Pandu Rangan

Indian Institute of Technology, Madras, India

Bernhard Steffen

University of Dortmund, Germany

Madhu Sudan

Massachusetts Institute of Technology, MA, USA

Demetri Terzopoulos

New York University, NY, USA

Doug Tygar

University of California, Berkeley, CA, USA

Moshe Y. Vardi

Rice University, Houston, TX, USA

Gerhard Weikum

Max-Planck Institute of Computer Science, Saarbruecken, Germany

Preface

Welcome to the proceedings of the 2005 International Conference on Embedded Software and Systems (ICESS 2005) held in Xian, China, December 16-18, 2005.

With the advent of VLSI system level integration and system-on-chip, the center of gravity of the computer industry is now moving from personal computing into embedded computing. Embedded software and systems are increasingly becoming a key technological component of all kinds of complex technical systems, ranging from vehicles, telephones, aircraft, toys, security systems, to medical diagnostics, weapons, pacemakers, climate control systems, etc.

The ICESS 2005 conference provided a premier international forum for researchers, developers and providers from academia and industry to address all resulting profound challenges; to present and discuss their new ideas, research results, applications and experience; to improve international communication and cooperation; and to promote embedded software and system industrialization and wide applications on all aspects of embedded software and systems.

Besides the main conference, we also featured the following four workshops to extend the spectrum of the main conference:

- Scheduling Techniques for Real-Time Systems
- IXA/IXP Application in Embedded Systems
- The Modeling and Security of Ubiquitous Systems
- Intelligent Storage System and Technology

There was a very large number of paper submissions (360) for the ICESS 2005 main conference, not only from Asia and the Pacific, but also from Europe, and North and South America. All submissions were reviewed by at least three program or technical committee members or external reviewers. It was extremely difficult to select the papers for the conference because there were so many excellent and interesting submissions. In order to allocate as many papers as possible and keep the high quality of the conference, we finally accepted 140 papers and 31 papers for the main conference and for the workshops, respectively. There were 63 main conference papers and 8 workshop papers selected in the LNCS proceedings. We believe that all of these papers and topics not only provided novel ideas, new results, work in progress and state-of-the-art techniques in this field, but also promoted cutting-edge research and future cooperation, and stimulated future research activities in the area of embedded software and systems.

The exciting conference program was the result of the hard and excellent work of program vice-chairs, external reviewers, and program and technical committee members under a very tight schedule. We were also grateful to the members of the local organizing committee for supporting us in handling so many organizational

tasks. Last but not least, we hoped you enjoyed the conference's technical and social program, and the natural and historic attractions of the ancient city of Xian.

October 2005

Laurence T. Yang, Xingshe Zhou, Wei Zhao, Zhaohui Wu, Yian Zhu and Man Lin

Organization

ICESS 2005 was organized by Northwestern Polytechnical University, China in collaboration with St. Francis Xavier University, Canada.

Sponsors

National Natural Science Foundation of China Important Software Committee of National 863 Program China Computer Federation Northwestern Polytechnical University, China Springer, Lecture Notes in Computer Science (LNCS)

Executive Committee

General Chairs:

Zhaohui Wu, Zhejiang University, China

Wei Zhao, Texas A&M University and NSF, USA

Program Chairs:

Laurence T. Yang, St. Francis Xavier University,

Canada

Xingshe Zhou, Northwestern Polytechnical University,

China

Program Vice-chairs:

Huiyang Zhou, University of Central Florida, USA

Walid Taha, Rice University, USA

Yann-Hang Lee, Arizona State University, USA Naehyuck Chang, Seoul National University, Korea Luis Gomes, Universidade Nova de Lisboa, Portugal Mohammed Y. Niamat, The University of Toledo,

USA

Susumu Horiguchi, Tohoku University, Japan Elhadi Shakshuki, Acadia University, Canada

Wenjing Lou, Worcester Polytechnic Institute, USA

Pin-Han Ho, University of Waterloo, Canada

Hong-Va Leong, Hong Kong Polytechnic University,

China.

Qun Jin, Waseda University, Japan

Arjan Durresi, Louisiana State University, USA Marios D. Dikaiakos, University of Cyprus, Cyprus

Workshop Chairs:

Yian Zhu, Northwestern Polytechnical University,

China

Man Lin, St. Francis Xavier University, Canada

Panel Chairs:

Joseph K. Ng, Hong Kong Baptist University, China

Xu Cheng, Peking University, China

VIII Organization

Conference Secretary: Yuying Wang, Northwestern Polytechnical University,

China

Publication Chair:

Tony Li Xu, St. Francis Xavier University, Canada

Local Executive

Committee: Zhanhuai Li (Chair)

Hong Tang, Yubo Wang, Mingxing Sun, Yumei Zhang

Program/Technical Committee

Raza Abidi Dalhousie University, Canada Esma Aimeur Université de Montréal, Canada

H. Amano Keio University, Japan

Leonard Barolli Fukuoka Institute of Technology, Japan

Darcy Benoit Acadia University, Canada

Marian Bubak Cyfronet University of Krakow, Poland

Jun Cai University of Waterloo, Canada

Jiannong Cao Hong Kong Polytechnic University, China Keith Chan Hong Kong Polytechnic University, China

Karam Chatha Arizona State University, USA
Xiangqun Chen Peking University, China
Phoebe Chen Deakin University, Australia

Jing Chen National Cheng Kung University, Taiwan

Yu Chen Tsinghua University, China Zhanglong Chen Fudan University, China

Xiuzhen Cheng George Washington University, USA

Xu Cheng Peking University, China

Jen-Yao Chung IBM, USA

Debatosh Debnath Oakland University, USA

Yunwei Dong Northwestern Polytechnical University, China

Stephen Edwards Columbia University, USA Tomoya Enokido Rissho University, Japan

Thomas Fahringer University of Innsbruck, Austria
Farzan Fallah Fujitsu Laboratory in America, USA
Ling Feng University of Twente, The Netherlands

Hakan Ferhatosmanoglu Ohio State University, USA
Joao Miguel Fernandes Universidade do Minho, Portugal

Antonio Ferrari Universidade de Aveiro, Portugal Jose Manuel Ferreira, Universidade do Porto, Portugal

Jose Manuel Ferreira, Universidade do Porto, Portugal Yue Gao Hopen Software Eng. Co. Ltd., China Mukul Goyal University of Wisconsin Milwaukee, USA

Rick Ha University of Waterloo, Canada

Naiping Han Chinasoft Network Technology Co. Ltd., China

Anwar Haque Bell Canada, Canada
Takahiro Hara Osaka University, Japan
Martin Hofmann University of Munich, Germany

Seongsoo Hong Seoul National University, Korea

Program/Technical Committee (continued)

Zhigang Hu

Michael C. Huang Xinming Huang Liviu Iftode

Clinton L. Jeffery

Hai Jiang

Xiaohong Jiang Roumen Kaiabachev Masashi Kastumata

Vlado Keselj

Ismail Khalil Ibrahim

Cheeha Kim

Jihong Kim Jung Hwan Kim Kwanho Kim Sung Won Kim Aris Kozyris

C.M. Krishna Morihiro Kuga Younggoo Kwon Anchow Lai Wai Lam Hsien-Hsin Lee

Chin-Laung Lei Qun Li

Tao Li Minghong Liao Xinhau Lin Yen-Chun Lin

Antonio Liotta Chunlei Liu Xiang Long Yung-Hsiang Lu

Jing Ma Wenchao Ma Zakaria Maamar Ricardo Machado Paulo Maciel

Evangelos Markatos

Grant Martin Janise McNair IBM T.J. Watson Research Center, USA

University of Rochester, USA University of New Orleans, USA

Rutgers University, USA

New Mexico State University, USA University of Waterloo, Canada

Tohoku University, Japan Rice University, USA

Nippon Institute of Technology, Japan

Dalhousie University, Canada

Johannes Kepler University of Linz, Austria Pohang University of Science and Technology, Korea

Seoul National University, Korea

University of Toledo, USA Samsung Electronics, Korea Yeungnam University, Korea

National Technical University of Athens,

Greece

University of Massachusetts, USA Kumamoto University, Japan Sejong University, Korea

Intel, USA

Chinese University of Hong Kong, China

Georgia Tech, USA

National Taiwan University, Taiwan College of William and Mary, USA

University of Florida, USA

Harbin Institute of Technology, China

University of Waterloo, Canada

Taiwan University of Science and Technology, Taiwan

University of Essex, UK Troy University, USA Bei Hang University, China Purdue University, USA

University of New Orleans, USA Microsoft Research Asia, China

Zayed University, UAE

Universidade do Minho, Portugal

Federal University of Pernambuco, Brazil ICS-FORTH and University of Crete, Greece

Tensilica, USA

University of Florida, USA

Program/Technical Committee (continued)

Teo Yong Meng Yan Meng

Tulita Mitra

S.M.F.D. Syed Mustapha Soraya K. Mostefaoui Tomasz Muldner Horacio Neto Naoki Nishi

WenSheng Niu

Sebnem Ozer Gordon Pace Jens Palsberg Seung-Jong Park

Ian Philp

Massimo Poncino Sunil Prabhakar Elliott Rachlin Omer Rana Minghui Shi Timothy K. Shih

Basem Shihada Youngsoo Shin

Dongkun Shin Kimura Shinnji Sandeep Shukla Valery Sklyarov

Prasanna Sundararajan

Wonyong Sung

Abd-Elhamid M. Taha Makoto Takizawa Jean-Pierre Talpin Kian-Lee Tan

Xinan Tang Zahir Tari P.S. Thiagarajan

Xuejun Tian

Hiroyuki Tomiyama Ali Saman Tosun Nur A. Touba Andre Trudel Lorna Uden National University of Singapore, Singapore

Stevens Institute of Technology, USA

National University of Singapore, Singapore

University of Malaysia, Malaysia University of Fribourg, Switzerland

Acadia University, Canada

Instituto Superior Tecnico, Portugal

NEC, Japan

Aeronautics Computing Research Institute,

China

Motorola Inc., USA

University of Malta, Malta

University of California at Los Angeles, USA

Louisiana State University, USA Los Alamos National Lab, USA University of Verona, Italy Purdue University, USA

Honeywell, USA

Cardiff University, UK

University of Waterloo, Canada Tamkang University, Taiwan University of Waterloo, Canada

KAIST, Korea

Samsung Electronics, Korea Waseda University, Japan Virginia Tech, USA

Universidade de Aveiro, Portugal

Xilinx Inc, USA

Seoul National University, Korea Queen's University, Canada Tokyo Denki University, Japan

INRIA, France

National University of Singapore, Singapore Intel Corp., Intel Compiler Lab., USA

RMIT, Australia

National University of Singapore, Singapore

Aichi Prefectural University, Japan

Nagoya University, Japan

University of Texas at San Antonio, USA University of Texas at Austin, USA

Acadia University, Canada

Staffordshire University, UK

Program/Technical Committee (continued)

Alexander P. Vazhenin

Jari Veijalainen

Salvatore Vitabile

Sarma Vrudhula

University of Aizu, Japan
University of Jyvaskyla, Finland
University of Palermo, Italy
Arizona State University, USA

Wenye Wang

North Carolina State University, USA

Xiaoge Wang Tsinghua University, China Ying-Hong Wang Tamkang University, Taiwan

Weng-Fai Wong National University of Singapore, Singapore

Eric Wong University of Texas at Dallas, USA

Jing Wu CRC, Canada

Dong Xie IBM China Research Lab, China Yuan Xie Pennsylvania State University, USA

Lin Xu National Natural Science Foundation, China

Dong Xuan Ohio State University, USA Ryuichi Yamaguchi Matsushita Co., Japan

Jie Yang Spirent Communications, Inc., USA Jun Yang University of California, Riverside, USA

Chi-Hsiang Yeh Queen's University, Canada
Y. Yokohira Okayama University, Japan
Muhammed Younas Oxford Brookes University, UK
Hsiang-Fu Yu National Center University, Taiwan

Demetrios Zeinalipour-Yazti University of California at Riverside, USA

Surong Zeng Motorola Inc., USA Guozhen Zhang Waseda University, Japan

Daging Zhang Agent for Science, Technology and Research,

Singapore

Shengbing Zhang Northwestern Polytechnical University, China

Zhao Zhang Iowa State University, USA
Wei Zhang Southern Illinois University, USA
Youtao Zhang University of Texas at Dallas, USA

Baihua Zheng Singapore Management University, Singapore

Jun ZhengUniversity of Ottawa, CanadaKougen ZhengZhejiang University, China

Dakai Zhu University of Texas at San Antonio, USA

Additional Reviewers

Iouliia Skliarova Universidade de Aveiro, Portugal

Mário Véstias INESC-ID, Portugal

Anikó Costa Universidade Nova de Lisboa, Portugal António Esteves Universidade do Minho, Portugal Universidade do Amazonas, Brazil

Workshop on Scheduling Techniques for Real-Time Systems

Introduction

Welcome to the proceedings of the 2005 International Workshop on Scheduling Techniques for Real-Time Systems (IWSRT 2005) held in conjunction with ICESS 2005 in Xi'an, China, December 16-18, 2005. Traditionally, scheduling has been an important aspect of real-time systems in ensuring soft/hard timing constraints. As real-time computing becomes complicated and has more limitations (e.g., power consumption), the demand for more sophisticated scheduling techniques becomes increasingly apparent.

The purpose of this workshop was to bring together researchers from both universities and industry to advance real-time scheduling techniques and its applications. IWSRT 2005 focused on the current technological challenges of developing scheduling algorithms:

- Power aware scheduling for real time systems
- Heuristic scheduling for real-time systems
- Parallel real-time scheduling
- Scheduling for distributed real-time systems
- Schedulability test, analysis and verification
- QoS scheduling for multimedia applications

From the many submissions, six papers were included in the workshop program. The workshop consisted of short presentations by the authors and encouraged discussion among the attendees. We hope that IWSRT 2005 provided a relaxed forum to present and discuss new ideas and new research directions, and to review current trends in this area. The success of the workshop was the result of the hard work of the authors and the program committee members. We were grateful for everyone's efforts in making the conference a success. Special thanks go to the members of the ICESS 2005 organizing committee for their support and help in many organizational tasks. We hoped you enjoyed the workshop program and the attractions of the ancient city of Xi'an.

Workshop Chairs

Man Lin, St. Francis Xavier University, Canada Fan Zhang, Hong Kong University of Science and Technology, China Dakai Zhu, University of Texas at San Antonio, USA

Program/Technical Committee

Samarjit Chakraborty

Deji Chen

Yuanshun Dai

Zonghua Gu

Hai Jin

Rodrigo de Mello

Xiao Qin

Gang Quan Chi-Sheng Shih Shengquan Wang National University of Singapore, Singapore

Emerson Process Management, USA

Indiana University-Purdue University, USA

Hong Kong University of Science and Technology, China Huazhong University of Science and Technology, China

University of Sao Paulo, Brazil

New Mexico Institute of Mining and Technology, USA

University of South Carolina, USA National Taiwan University, Taiwan

Texas A&M, USA

Workshop on IXA/IXP Application in Embedded Systems

Introduction

The 2005 International Workshop on IXA/IXP Application in Embedded Systems (IWIXA) was held in conjunction with the International Conference on Embedded Software and Systems (ICESS 2005), December 16-18, 2005, at Northwestern Polytechnical University, Xi'an, P.R. China. The workshop aimed to provide a stimulating environment for IXA/IXP researchers and developers to share their experience in order to promote the understanding of the latest trends in Network Processors and their application development in embedded systems. The workshop invited new and original submissions addressing theoretical and practical topics in the following fields (but not limited to these topics):

- Internet eXchange Architecture (IXA) in embedded systems
- Network Processors and IXP
- The IXA/IXP Network Processors-based applications
- New Network Technology
- IXA/IXP-related training and experiments

The workshop received 21 paper submissions. After careful review, 11 papers were accepted for the workshop program. The workshop committee was grateful to all authors for their interesting contributions.

Workshop Chair

Naiqi Liu, University of Electronic Science and Technology, China

Workshop Coordinator

Jeffrey Cao, Intel, China

Program/Technical Committee

Luo Lei University of Electronic Science and Technology,

China

Hang Lei University of Electronic Science and Technology,

China

Guangjun Li University of Electronic Science and Technology,

China

Workshop on the Modeling and Security of Ubiquitous Systems

Introduction

Rapid progress in computer hardware technology has made computers compact (e.g. laptop, palmtop), powerful, and more affordable. Furthermore, recent advances in wireless data communications technology have spawned an increasing demand for various types of services. As a result, we are witnessing an explosive growth for research and development efforts in the field of ubiquitous communication and computing systems.

The global growth of interest in the Internet and in high-quality audio, and video conferencing and VOD, coupled with a growing high-bandwidth structure, will lead to a rapidly expanding market for ubiquitous multimedia services. The popularity of mobile services should eventually affect the market for ubiquitous networks. For this reason, mobile based technologies, such as mobile synchronization, QoS assurance, mobile IP-based multimedia technologies and the security of mobile information systems, need to be studied and developed for future services offered to subscribers in future mobile information systems. This ubiquitous information technology will allow users to travel within an office building, from office to home, around the country and the world with a portable computer in their hands. Disconnection will no longer be a network fault, but a common event intentionally caused by the user in order to preserve a consequence of mobility.

The workshop on Modeling and Security in Ubiquitous Information Systems contained a collection of high-quality papers on this subject. In addition to this, we received a few more papers, as a result of the call-for-papers for this topic. Each paper went through a rigorous, peer review process as required by the conference. Based upon the review committee's decision, four papers were selected for their original contributions as well as their suitability to the topic of this workshop.

Many people have contributed to the creation of this workshop. Thanks are due to the members of Howon University's Mobile Networks Laboratory and the members of Kyonggi University's Security Laboratory for their support. Special thanks go to the members of the review committee for their excellent cooperation. Their hard work, comments and suggestions really helped to improve the quality of the papers. We would like to take this opportunity to thank everyone who made this workshop possible: the authors, the ICESS 2005 organizing committee and the publisher.

Workshop Chair

Dong Chun Lee, Howon University, Korea

Sang-Ho Kim

Masaru Kitsuregawa

Program/Technical Committee

HP Labs., USA Bernard Burg

Kijoon Chae Ewha Womans University, Korea Ying Chen IBM China Research Lab., China

Anthony Chung Depaul University, USA

Alex Delis New York Polytechnic University, USA Maggie Dunham Southern Methodist University, USA

Adrian Friday Lancaster University, UK ReX E. Gantenbein Wyoming University, USA Takahiro Hara Osaka University, Japan Yong-Sok Her Kyushu University, Japan Hang Dai Hoon Kyung Won University, Korea Queensland University, Australia Jadwiga Indulska Christian S. Jensen Aalborg University, Denmark

Hai Jin Huazhong University of Science and Technology,

China

Myuhang-Joo Kim Seoul Women's University, Korea

Korea Information Security Agency, Korea

Tokyo University, Japan Shonali Krishnaswamy Monash University, Australia

Agency for Defense Development, Korea Tae Won Kang

Taekyoung Kwon Sejong University, Korea Young Bin Kwon Chung-Ang University, Korea Alexandros Labrinidis Pittsburgh University, USA Jeong Bae Lee Sun Moon University, Korea

Wang-Chien Lee Pennsylvania State University, USA

Hui Lei IBM T. J. Watson Research Center, USA

Jong-In Lim Korea University, Korea Seng Wai Loke Monash University, Australia Chinese Academy of Science, China Hanqing Lu Sanjay Kumar Madria Missouri-Rolla University, USA Se Hyun Park Chung-Ang University, Korea

Oscar Pastor Valencia University, Spain Evaggelia Pitoura Ioannina University, Greece Andreas Pitsillides Cyprus University, Cyprus Indrajit Ray Colorado State University, USA

Peter Reiher University of California at Los Angeles, USA

Claudia Roncancio ENSIMAG/LSR, France Seref Sagiroglu Gazi University, Turkey

Ming-Chien Shan HP, USA

Theodore E. Simos Peloponnese University, Greece

SungWon Sohn Electronics and Telecommunications Research

Institute, Korea

Ki-Sung Yoo Korea Institute of Science and Technology

Information, Korea

Workshop on Intelligent Storage Systems and Technology

Introduction

With the present explosive growth in information, the demand for storage systems is increasing rapidly. To satisfy such mounting demand, storage systems are required to be more scalable, reliable, secure and manageable than they are currently. There is a clear and recent trend in which some intelligence is moved from host machines to storage devices and implemented in the embedded controller. The 2005 International Workshop on Intelligent Storage Systems and Technology (ISST 2005) brought together storage systems researchers and practitioners to explore new directions in the design, implementation, evaluation, and deployment of storage systems. ISST 2005 was one of the workshops held in conjunction with the 2nd International Conference on Embedded Software and Systems (ICESS 2005) held in Xian, China, December 16-18, 2005.

We were extremely grateful to the program committee members who worked under a very tight schedule to complete the rigorous review process for the large number of submissions received by ISST 2005. Their hard work lead to the selection of the 10 papers presented at the workshop.

Workshop Chairs

Dan Feng, Huazhong University of Science and Technology, China Hong Jiang, University of Nebraska-Lincoln, USA

Program/Technical Committee

Liang Fang National University of Defense Technology, China

Jizhong Han Chinese Academy of Sciences, China Ben Xubin He Tennessee Technological University, USA

Xiao Qin New Mexico Institute of Mining and Technology, USA Fang Wang Huazhong University of Science and Technology, China

Frank Zhigang Wang Cranfield University, UK

Song Wu Huazhong University of Science and Technology, China Changsheng Xie Huazhong University of Science and Technology, China

Lu Xu Chinese Academy of Science, China

Ke Zhou Huazhong University of Science and Technology, China

Yifeng Zhu University of Maine, USA