

COMPUTER AIDED DESIGN
OF DIGITAL SYSTEMS
A BIBLIOGRAPHY
VOLUME III 1976-77

W.M. VANCLEEMPOT

COMPUTER SCIENCE PRESS, INC.

7960472
5

COMPUTER AIDED DESIGN
OF DIGITAL SYSTEMS
A BIBLIOGRAPHY
VOLUME III 1976-77

W.M. VANCLEEMPOT

DIGITAL SYSTEMS LABORATORY

STANFORD UNIVERSITY



E7950472

COMPUTER SCIENCE PRESS, INC.

DIGITAL SYSTEM DESIGN SERIES

Arthur D. Friedman, Series Editor

THEORY & DESIGN OF SWITCHING CIRCUITS

Arthur D. Friedman and Premachandran R. Menon

LOGICAL DESIGN OF DIGITAL SYSTEMS

Arthur D. Friedman

DIAGNOSIS AND RELIABLE DESIGN OF DIGITAL SYSTEMS

Melvin A. Breuer and Arthur D. Friedman

DIGITAL SYSTEM DESIGN AUTOMATION: LANGUAGES, SIMULATION & DATA BASE

Edited by Melvin A. Breuer

COMPUTER AIDED DESIGN OF DIGITAL SYSTEMS - A BIBLIOGRAPHY VOLUME I

Compiled by William vanCleemput

COMPUTER AIDED DESIGN OF DIGITAL SYSTEMS - A BIBLIOGRAPHY VOLUME II 1975-76

Compiled by William vanCleemput

COMPUTER AIDED DESIGN OF DIGITAL SYSTEMS - A BIBLIOGRAPHY VOLUME III 1976-77

Compiled by William vanCleemput

© Copyright 1978, Computer Science Press, Inc.

Printed in the United States of America

All rights reserved. No part of this work may be reproduced, transmitted, or stored in any form or by any means, without the prior written consent of the publisher.



COMPUTER SCIENCE PRESS, Inc.
9125 Fall River Lane
Potomac, Md. 20854, USA

Preface

In the third volume of this bibliography, publications in the field of computer-aided design of digital systems collected during the period May 1976 - October 1977 are listed. Furthermore, coverage of the bibliography has been expanded backwards for certain journals. A list of complete coverage of the various journals and conference proceedings is included in the appendices.

An attempt has been made to cover all major publications (books, papers in journals and published conference proceedings, research reports) in the main areas of computer-aided design of digital systems (Automated logic design, fault diagnosis and test generation, simulation and circuit layout).

A number of references on the mathematical and computer science aspects of computer-aided design have been included in Section J. Furthermore, some references on computer-aided circuit analysis and computer-aided manufacturing of digital systems have been selected on the basis of their usefulness to designers of digital systems. In these fields the bibliography is not intended to be complete but it contains a number of citations that are representative of the area.

This work consists of four major sections, The first section contains the citations, classified under 10 major subject headings:

- A. General References on Design Automation
- B. Automated Logic Design
- C. Logic and Fault Simulation
- D. Fault Diagnosis and Fault Test Generation
- E. Layout of Printed and Integrated Circuits
- F. Computer-Aided Circuit Analysis
- G. Automated Documentation
- H. Computer-Aided Manufacturing of Digital Systems
- I. Software Design Automation
- J. Mathematical and Computer Science Aids for Digital Design Automation

Within every subheading, citations are classified alphabetically by author and chronologically for every author. Every citation is numbered and both the author and subject indices refer to the citations in this section.

The numbering scheme used in the first and second volume has been followed, albeit with a few modifications, in this third volume. Within every subsection the numbering of the first and second volumes has been continued, e.g. where B2.861 was the last citation in Volume 2, B2.862 will be the first citation of Volume 3 for Section B2.

The second section is author index. This section allows quick access to all publications in the bibliography by an individual author. This author index is cumulative over the three volumes of the bibliography.

The third section is the subject and keyword index, organized alphabetically by subject. This index is also cumulative over the first and second volumes of the bibliography.

The fourth section is a research report index, which allows access to citations by their source of origin. For the purpose of this index, dissertations are treated as research reports.

The appendices contain a list of frequently used abbreviations and a list of journals and conference proceedings that were covered systematically.

W.M.vanCleemput.

December, 1977.

How to Use this bibliography

The citations in this bibliography are classified under 10 major headings:

- A. General References on Design Automation
- B. Automated Logic Design
- C. Logic and Fault Simulation
- D. Fault Diagnosis and Fault Test Generation
- E. Layout of Printed and Integrated Circuits
- F. Computer-Aided Circuit Analysis
- G. Automated Documentation
- H. Computer-Aided Manufacturing of Digital Systems
- I. Software Design Automation
- J. Mathematical and Computer Science Aids for Digital Design Automation

Each of these subject headings is in turn broken up into several subheadings.

An important publication may be listed more than once if it clearly belongs to more than one subject area.

Multiple listings of the same citation have been avoided as much as possible and only important references belonging clearly under more than one heading have been listed more than once.

The subject and keyword index provides easy access to all citations that belong to more than one subject area.

For many research reports, the NTIS (National Technical Information Service) accession number is given if the report is available through NTIS. Most doctoral dissertations are available from Xerox University Microfilms and when known,

the order number for these dissertations is given.

If the user is interested in a particular subject area such as Printed Circuit Layout, he may look up the subject heading that seems most appropriate.

However, some of the subheadings such as B2, Logic Design Algorithms and D2, Fault Diagnosis Techniques, are likely to contain too many citations for efficiently locating the relevant references. The subject and keyword index in Section 3 is the key to the full resources of this bibliography. For every citation, this index contains entries to significant keyword and subject classifications.

The author index allows the user quick access to all the publications by the same author, whether or not he is the primary author.

The research report index lists research reports and theses by the source of origin. This may be helpful in locating a particular item if only the affiliation of the author(s) is known.

Appendix 1 contains a list of the most frequently used abbreviations. A list of major journals that were covered systematically is given in Appendix 2. A similar list is provided for conference proceedings in Appendix 3.

Table of Contents

Table of Contents.....	iii
Preface.....	v
How to Use This Bibliography.....	viii
Section 1 - Citations.....	1
A General References on Design Automation.....	3
A1 Books.....	3
A2 Surveys.....	4
A3 Bibliographies.....	6
A4 Other General References.....	7
B. Automated Logic Design.....	9
B1. Books, Surveys, Bibliographies.....	9
B2. Logic Design Algorithms.....	11
B3. Digital Design Languages.....	31
B4. Logic Design Systems.....	36
B5. Microprogramming Design Aids.....	40
B6. Other.....	44
C. Logic and Fault Simulation.....	47
C1. Books, Surveys, Bibliographies.....	47
C2. Logic Simulation Techniques.....	48
C3. Logic Simulation Programs and Systems.....	50
C4. Fault Simulation Techniques.....	52
C5. Fault Simulation Programs and Systems.....	53
C6. Other.....	54
D. Fault Diagnosis and Fault Test Generation.....	55
D1. Books, Surveys, Bibliographies.....	55
D2. Fault Diagnosis Techniques.....	56
D3. Fault Diagnosis Programs and Systems.....	70
D4. Design for Fault Diagnosis/Fault Tolerance...	71
D5. Other.....	77
E. Layout of Printed and Integrated Circuits.....	79
E1. Books, Surveys, Bibliographies.....	79
E2. Partitioning.....	80
E3. Module Pin and Gate Assignment.....	81
E4. Placement.....	82
E5. Routing.....	84
E6. Printed Circuit Layout.....	87
E7. Integrated Circuit Layout.....	89
E8. Topological Layout Methods.....	94
E9. Other.....	94
F. Computer-Aided Circuit Analysis.....	95
F1. Books, Surveys, Bibliographies.....	95
F2. Programs and Systems.....	95
G. Automated Documentation.....	99
G1. Books, Surveys, Bibliographies.....	99
G2. Automated Generation of Logic Diagrams.....	99
G3. Automated Drafting.....	99

G4.	Automated Generation of Flowcharts.....	100
G5.	Other.....	100
H.	Computer-Aided Manufacturing of Digital Systems...	101
H1.	Books, Surveys, Bibliographies.....	101
H2.	Computer-Aided Manufacturing and Testing....	101
I.	Software Design Automation.....	104
I1.	Books, Surveys, Bibliographies.....	104
I2.	Program Correctness and Program Analysis....	106
I3.	Automated Software Testing.....	113
I4.	Automated Program Synthesis.....	116
I5.	Automated Program Documentation.....	117
I6.	Software Engineering.....	118
I7.	Software Reliability.....	122
I8.	Structured Programming.....	125
J.	Mathematical and Computer Science Aids for Digital Design Automation.....	126
J1.	Graph Theory.....	126
J2.	Optimization.....	128
J3.	Computer Graphics.....	129
J4.	File Systems and Data Bases.....	131
J5.	Programming Languages, Compilers, Data Structures.....	132
Section 2 -	Cumulative Author Index.....	133
Section 3 -	Cumulative Subject and Keyword Index.....	231
Section 4 -	Research Report Index.....	283
Appendix 1:	Abbreviations Used.....	293
Appendix 2:	Cumulative List of Journals Covered.....	295
Appendix 3:	Cumulative List of Conferences Covered.....	299

SECTION 1

CITATIONS

A. General References on Design Automation

Al. Books

- Al.13 Agrawala,A.K.; Rauscher,T.G. "Foundations of Microprogramming - Architecture, Software and Applications," Academic Press, 1976, 416 pp..
- Al.14 Boon,C. "Microprogramming and Systems Architecture - International Computer State of the Art Report," Maidenhead, Berks, England: Infotech Information, 1975, 644 pp..
- Al.15 Boon,C.; Bunyan,C.J. "Data Base Management - International Computer State of the Art Report," Maidenhead, Berks, England: Infotech Information, 1973, 665 pp. .
- Al.16 Bunyan,C.J. "Computer Systems Reliability - International State of the Art Report," Maidenhead, Berks, England: Infotech Information, 1974, 829 pp..
- Al.17 Lewin,D.W. "Computer Aided Design of Digital Systems," New York: Crane-Russak Co., 1977.
- Al.18 Rine,D.C. "Computer Science and Multiple-Valued Logic," Amsterdam, Netherlands: North Holland, 1977, 548 pp..
- Al.19 Smith,R.J. "Physical Design Automation - System Study and Implementation Plans," Wichita, Kansas: V-R Information Systems, 1977.
- Al.20 Thelliez,S.F. "Introduction to the Study of Ternary Switching Structures," London, England: Gordon and Breach, 1975, 186 pp..
- Al.21 Wolfendale,E. "Computer Aided Design Techniques," London, England: Butterworth, 1970, 321 pp..
- Al.22 Zissos,D. "Problems and Solutions in Logic Design," London, England: Oxford, 1976, 146 pp..

A2. Surveys

- A2.21 Avizienis, A. "Computer Systems Reliability: An Overview," in: Computer Systems Reliability: International State of the Art Report, C.J. Bunyan (Ed.), Maidenhead, Berks, England: Infotech Information, 1974, pp. 215-233.
- A2.22 Boehm, B.W. "Software Engineering," IEEE Trans. on Computers, vol. C-25, no. 12, pp. 1226-1241, Dec. 1976. Also: IEEE Computer Society Repository, R77-215, 41 pp., 1977.
- A2.23 Brown, J.R.; Hoffman, R.H. "Automating Software Development: A Survey of Techniques and Automated Tools," IEEE Computer Society Repository, R77-191, 21 pp., June 1977.
- A2.24 Freeman, P. "Software Reliability and Design: A Survey," Proc. 13th Annual Design Automation Conf., San Francisco, California, June 1976, pp. 484-494.
- A2.25 Glushkov, V.M.; Derkach, V.P.; Kiyashko, G.F. "The State of Large-Scale Integrated Circuit Design Automation," Cybernetics, vol. 12, no. 6, pp. 857-861, Nov.-Dec. 1976.
- A2.26 Gulyanitskii, L.F.; Kaspshitskaya, M.F.; Sergienko, I.V. "Approaches and Algorithms for the Solution of Certain Optimisation Problems in Automated Design of Computer Systems and Processors," Cybernetics, vol. 12, no. 5, pp. 716-725, Sept.-Oct. 1976.
- A2.27 Hanan, M. "Layout, Interconnection and Placement," Networks, vol. 5, no. 1, pp. 85-88, 1975.
- A2.28 Hanan, M.; Wolff, P.K.; Agule, B.J. "A Comprehensive Study of Placement Techniques for Computer Logic Graphs," IBM Research Report RC 5080, Oct. 1974.
- A2.29 Hanan, M.; Wolff, P.K.; Agule, B.J. "A Study of Placement Techniques," J. of Design Automation and Fault-Tolerant Computing, vol. 1, no. 1, pp. 28-61, Oct. 1976.
- A2.30 Hantler, S.L.; King, J.C. "An Introduction to Proving the Correctness of Programs," ACM Computing Surveys, vol. 8, no. 3, pp. 331-353, Sept. 1976.
- A2.31 Ivaskiv, Y.L.; Pospelov, D.A.; Toshich, Z. "Representations in Many-Valued Logics," Cybernetics, vol. 5, no. 2, pp. 163-177, March-April 1969.

- A2.32 Magnuson, W.G. "Computer Aided Design and Design Automation in Europe," SIGDA Newsletter, vol. 6, no. 2, pp. 62-72, June 1976.
- A2.33 Magnuson, W.G. "Electronics Engineering Design Automation at ERDA Laboratories," Proc. AESOP-XV, Denver, Colorado, Sept. 1976, pp. 234-244.
- A2.34 McCluskey, E.J. "A Survey of Research at the Center for Reliable Computing, Stanford Univ.," J. of Design Automation and Fault-Tolerant Computing, vol. 1, no. 1, pp. 85-90, Oct. 1976.
- A2.35 Miller, E.F. "Modern Program Verification Techniques," Proc. Summer Simulation Conf., Chicago, Ill., July 1977, pp. 721-726.
- A2.36 Misuri, G. "Survey of Existing Programming Aids," SIGPLAN Not., vol. 11, no. 8, pp. 38-41, Aug. 1976.
- A2.37 Rine, D.C. "An Historical Survey of Logic Function Design for Digital Computing Systems: 1952-1976," Proc. Int. Symp. on Multiple-Valued Logic, Charlotte, N.C., May 1977, pp. 143-154.
- A2.38 Rine, D.C. "An Introduction to Multiple-Valued Logic," Chapter 1 of Rine, D.C.: Computer Science and Multiple-Valued Logic, North Holland, 1977, pp. 3-12.
- A2.39 Rosenbluth, W. "Design Automation Architecture and Applications," IEEE Computer, vol. 9, no. 2, pp. 12-16, Feb. 1976.
- A2.40 Su, S.Y.H. "A Survey of Digital Hardware Descriptive Languages," Proc. Workshop on Computer Hardware Description Languages, Darmstadt, Germany, July-Aug. 1974, pp. 143-158.
- A2.41 Su, S.Y.H.; Spillman, R.J. "An Overview of Fault-Tolerant Digital System Architecture," Proc. Nat. Computer Conf., Dallas, Texas, June 1977, pp. 19-26.
- A2.42 Szygenda, S.A. "The Next Step in Testing and Modeling," Digest 12th IEEE Computer Society Int. Conf., San Francisco, California, Feb. 1976, pp. 194-195.
- A2.43 Szygenda, S.A.; Thompson, E.W. "Modeling and Digital Simulation for Design Verification and Diagnosis," IEEE Trans. on Computers, vol. C-25, no. 12, pp. 1242-1253, Dec. 1976. Also: IEEE Computer Society Repository, R76-123, Sept. 1976.

A3. Bibliographies

- A3.45 Barnard, D.; Thompson, D. "An Annotated Bibliography on Computer Program Engineering," IEEE Computer Society Repository, R76-264, 100 pp., Nov. 1976.
- A3.46 Gutttag, J. "An Annotated Bibliography on Computer Program Engineering 'Third Edition'," IEEE Computer Society Repository, R77-43, 125 pp., Feb. 1977.
- A3.47 Oren, T.I. "Annotated Bibliography on Simulators," Simulation, vol. 27, no. 6, pp. 193-196, Dec. 1976.
- A3.48 Pierce, A.R. "Bibliography on Algorithms for Shortest Path, Shortest Spanning Tree, and Related Circuit Routing Problems (1956-1974)," Networks, vol. 5, no. 2, pp. 129-149, April 1975.
- A3.49 Pierce, A.R. "The Literature of Software Engineering: Description and Guide," Proc. 13th Annual Design Automation Conf., San Francisco, California, June 1976, pp. 451-461.
- A3.50 Pooch, U.W. "Computer Graphics Interactive Techniques, and Image Processing 1970-1975 : A Bibliography," IEEE Computer, vol. 9, no. 8, pp. 46-64, Aug. 1976.

A4. Other General References

- A4.85 Amkreutz, J.H. "Cybernetic Model of the Design Process," Computer Aided Design, vol. 8, no. 3, pp. 187-192, July 1976.
- A4.86 Breuer, M.A. "Incremental Processing in Design Automation," SIGDA Newsletter, vol. 6, no. 4, pp. 2-9, Dec. 1976.
- A4.87 Carey, B.J.; MacLachlan, G.F. "Automated Design Based upon Microprogrammable Bit Slice Microprocessors," Proc. of the Symp. on Design Automation and Microprocessors, Palo Alto, California, Feb. 1977, pp. 20-24.
- A4.88 Drongowski, P.J. "Capability Requirements in a Multimicro Processor, Hardware/ Software Simulation Environment," Proc. Symp. on Design Automation and Microprocessors, Palo Alto, California, Feb. 1977, pp. 2-5.
- A4.89 Drongowski, P.J.; Rose, C.W. "N.mPc: An Adaptable System to Support the Development of Microprocessor-Based Systems," Digest 13th IEEE Computer Society Int. Conf., Washington, D.C., Sept. 1976, pp. 180-184.
- A4.90 Fine, A.M. "Automated Design and Testing," Sperry Technology, vol. 2, no. 1, pp. 2-7, 1974.
- A4.91 Jaquart, R.; Regnier, P.; Valette, F.R. "GERMINAL, a Conversational and Integrated System for CAD (in French)," Automatisme, vol. 19, no. 3, pp. 163-168, March 1974.
- A4.92 Kaplan, G. "Computer-Aided Design," IEEE Spectrum, vol. 12, no. 10, pp. 40-47, Oct. 1975.
- A4.93 MacLachlan, G. "A Restructurable Microprocessor Design System," Univ. of Connecticut, Dept. of El. Eng., M.S. thesis, 1973.
- A4.94 Magnuson, W.G. "Computer Aided Design and Design Automation in Europe," Office of Naval Research, ONR London Rept. R-13-75, Oct. 1975, 54 pp..
- A4.95 Matelan, M.N. "Automating the Design of Dedicated Real-Time Control Systems," Lawrence Livermore Lab., Rept. UCRL-78651, 235 pp.. Also: IEEE Computer Society Repository, R77-89, April 1977, 222 pp..