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Howard Heys Carlisle Adams (Eds.)

Selected Areas in Cryptography

6th Annual International Workshop, SAC'99
Kingston, Ontario, Canada, August 1999
Proceedings



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Howard Heys
Faculty of Engineering and Applied Science
Memorial University of Newfoundland
St. John's, Newfoundland, Canada A1B 3X5
E-mail: howard@engr.mun.ca

Carlisle Adams
Entrust Technologies
750 Heron Road, Suite E08
Ottawa, Ontario, Canada K1V 1A7
E-mail: cadams@entrust.com

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Preface

SAC'99 was the sixth in a series of annual workshops on Selected Areas in Cryptography. Previous workshops were held at Carleton University in Ottawa (1995 and 1997) and at Queen's University in Kingston (1994, 1996, and 1998). The intent of the annual workshop is to provide a relaxed atmosphere in which researchers in cryptography can present and discuss new work on selected areas of current interest. The themes for the SAC'99 workshop were:

- Design and Analysis of Symmetric Key Cryptosystems
- Efficient Implementations of Cryptographic Systems
- Cryptographic Solutions for Web/Internet Security

The timing of the workshop was particularly fortuitous as the announcement by NIST of the five finalists for AES coincided with the first morning of the workshop, precipitating lively discussion on the merits of the selection!

A total of 29 papers were submitted to SAC'99 and, after a review process that had all papers reviewed by at least 3 referees, 17 were accepted and presented. As well, two invited presentations were given: one by Miles Smid from NIST entitled "From DES to AES: Twenty Years of Government Initiatives in Cryptography" and the other by Mike Reiter from Bell Labs entitled "Password Hardening with Applications to VPN Security".

The program committee for SAC'99 consisted of the following members: Carlisle Adams, Tom Cusick, Howard Heys, Lars Knudsen, Henk Meijer, Luke O'Connor, Doug Stinson, Stafford Tavares, and Serge Vaudenay. As well, additional reviewers were: Christian Cachin, Louis Granboulan, Helena Handschuh, Julio Lopez Hernandez, Mike Just, Alfred Menezes, Serge Mister, Guillaume Poupard, Victor Shoup, Michael Wiener, and Robert Zuccherato.

The organizers are very grateful for the financial support for the workshop received from Entrust Technologies, the Department of Electrical and Computer Engineering at Queen's University, and Communications and Information Technology Ontario (CITO). Special thanks to Stafford and Henk must be given for, once again, hosting SAC and being responsible for all the local arrangement details. The organizers would also like to thank Sheila Hutchison of the Department of Electrical and Computer Engineering at Queen's University for administrative and secretarial help and Yaser El-Sayed from the Faculty of Engineering at Memorial University of Newfoundland for help in preparing the workshop proceedings.

On behalf of the SAC'99 organizing committee, we thank all the workshop participants for making SAC'99 a success!

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Table of Contents

Cryptosystems and Pseudorandom Number Generators

A Universal Encryption Standard	1
<i>Helena Handschuh and Serge Vaudenay</i>	
Yarrow-160: Notes on the Design and Analysis of the Yarrow Cryptographic Pseudorandom Number Generator	13
<i>John Kelsey, Bruce Schneier, and Niels Ferguson</i>	
Elliptic Curve Pseudorandom Sequence Generators	34
<i>Guang Gong, Thomas A. Berson, and Douglas R. Stinson</i>	

Security Aspects of Block Ciphers

Adaptive-Attack Norm for Decorrelation and Super-Pseudorandomness ...	49
<i>Serge Vaudenay</i>	
Guesswork and Variation Distance as Measures of Cipher Security	62
<i>John O. Pliam</i>	
Modeling Linear Characteristics of Substitution-Permutation Networks ...	78
<i>Liam Keliher, Henk Meijer, and Stafford Tavares</i>	
Strong Linear Dependence and Unbiased Distribution of Non-propagative Vectors	92
<i>Yuliang Zheng and Xian-Mo Zhang</i>	

Cryptanalysis of Block Ciphers

Security of E2 against Truncated Differential Cryptanalysis	106
<i>Shiho Moriai, Makoto Sugita, Kazumaro Aoki, and Masayuki Kanda</i>	
Key-Schedule Cryptanalysis of DEAL	118
<i>John Kelsey and Bruce Schneier</i>	
Efficient Evaluation of Security against Generalized Interpolation Attack ..	135
<i>Kazumaro Aoki</i>	

Efficient Implementations of Cryptosystems

Efficient Implementation of Cryptosystems Based on Non-maximal Imaginary Quadratic Orders	147
<i>Detlef Hühnlein</i>	

Improving and Extending the Lim/Lee Exponentiation Algorithm 163
Biljana Cubaleska, Andreas Rieke, and Thomas Hermann

Software Optimization of Decorrelation Module 175
Fabrice Noilhan

Cryptography for Network Applications

Pseudonym Systems 184
Anna Lysyanskaya, Ronald L. Rivest, Amit Sahai, and Stefan Wolf

Unconditionally Secure Proactive Secret Sharing Scheme with
Combinatorial Structures 200
Douglas R. Stinson and R. Wei

Protecting a Mobile Agent’s Route against Collusions 215
Dirk Westhoff, Markus Schneider, Claus Unger, and Firoz Kaderali

Photuris: Design Criteria 226
William Allen Simpson

Author Index 243

A Universal Encryption Standard

Helena Handschuh¹ and Serge Vaudenay²

¹ Gemplus – ENST

handschuh@gemplus.com

² Ecole Normale Supérieure – CNRS

Serge.Vaudenay@dmi.ens.fr

Abstract. DES and triple-DES are two well-known and popular encryption algorithms, but they both have the same drawback: their block size is limited to 64 bits. While the cryptographic community is working hard to select and evaluate candidates and finalists for the AES (Advanced Encryption Standard) contest launched by NIST in 1997, it might be of interest to propose a secure and simple double block-length encryption algorithm. More than in terms of key length and block size, our Universal Encryption Standard is a new construction that remains totally compliant with DES and triple-DES specifications as well as with AES requirements.

1 Introduction

For many years, DES [9] has been used as a worldwide encryption standard. But as technology improved for specialized key-search machines [26, 8], its 56-bit key size became too short, and a replacement was needed. 2-key triple-DES has since become the traditional block cipher used both by the cryptographic community as well as industry. However, there is a second drawback to DES which is also the case for triple-DES: its 64-bit block size. Therefore NIST launched a contest to select and evaluate candidates for a new encryption standard, the AES, in late 1997 [1]. The basic requirements for this new algorithm were that it be at least as secure and fast as triple-DES, but that its block size be of 128 bits instead of 64, and that its key size take possible values of 128, 192 and 256 bits.

Meanwhile, people are still using DES and triple-DES, and may want to start developing applications where these two as well as the new AES may independently be used as the encryption components. In order to be compliant with DES and triple-DES, we propose a new construction which is based on these building blocks, but which can take AES specifications as a requirement for its key and block sizes. Therefore, when AES is finally selected, it will come as a natural plug-in replacement of the actual structure without anybody being forced to change input and output interfaces.

We notice that double block-length encryption primitives based on DES already exist: as an example, take DEAL, which uses DES as the round function in a traditional 6-round Feistel scheme [16]. One can also think of multiple modes with

two blocks, where DES is the underlying cipher [10], but except for two-key triple DES in outer CBC mode which is vulnerable to dictionary and matching ciphertext attacks, none of these constructions are backward compliant with DES and triple-DES, none do they make use of the full strength of a 128-bit block size (the second half of the plaintext never influences the first half of the ciphertext). Furthermore, multiple modes are either insecure [3–6] or require confidentiality or integrity protected initial values [25, 11]. We are also aware of the attacks by Lucks on 3-key triple DES [18] and DEAL [19].

The rest of the paper is organized as follows: section 2 presents our new encryption standard. Sections 3 and 4 provide details on collision attacks when some of the components of our UES are cut out. Section 5 provides additional security arguments on our construction and evaluates its strength based on the FX construction. Finally, we argue why we believe our construction is sound.

2 A Universal Encryption Standard

In this section we give the specifications of our new double block-length encryption algorithm. It basically runs two triple-DES encryptions in parallel and exchanges some of the bits of both halves inbetween each of the three encryption layers. Note that Outerbridge proposed a similar idea [21]. We investigated several related constructions and decided to add pre and post-whitening with extra keys, as well as an additional layer where bits of the left and the right half of the scheme are swapped under control of the extended secret key. Justification for these final choices will be given throughout this paper. The key schedule is considered to be the same as DEAL’s.

2.1 Notations

We use the following notations for our scheme as well as for the attacks presented in the next sections (all operations are on bitstrings):

$a|b$: concatenation of a and b

$a \oplus b$: bitwise “exclusive or” of a and b

$a \wedge b$: bitwise “and” of a and b

\bar{a} : bitwise 1-complement of a

001110100111_b : bitstring in binary notation

$3a7_x$: bitstring in hexadecimal notation with implicit length (multiple of four)

In addition we let $\text{DES}_k(x)$ denote the DES encryption of a 64-bit block x by using a 56-bit key k , and we let $\text{3DES}_{k_1, k_2}(x)$ denote the 2-key triple-DES encryption of x in EDE mode (Encryption followed by Decryption followed by Encryption), *i.e.*

$$\text{3DES}_{k_1, k_2}(x) = \text{DES}_{k_1}(\text{DES}_{k_2}^{-1}(\text{DES}_{k_1}(x))) .$$

2.2 Basic Building Blocks

We already mentioned that we use parallel 3DES as well as a kind of keyed swap. In order to further formalize our proposal, let us define the following three basic building blocks which refer to operations on 128-bit strings. For convenience, we split a 128-bit string x into two 64-bit halves x_h and x_l .

1. **Keyed Translation.** Let $k = k_h|k_l$ be a 128-bit string. We define

$$T_k(x) = x \oplus k.$$

2. **Keyed Swap.** Let k be a 64-bit string. We define

$$S_k(x) = (x_h \oplus u)|(x_l \oplus u)$$

where $u = (x_h \oplus x_l) \wedge k$. This actually consists of exchanging the bits which are masked by k in the two halves.

3. **Parallel Encryption.** Let $k = k_h|k_l$ be two concatenated keys for two keyed algorithms C and C' . We define

$$P_{k,C,C'}(x) = C_{k_h}(x_h)|C'_{k_l}(x_l).$$

Our algorithm is a combination of three rounds of products of these transformations with additional operations before the first and after the last encryption layer.

2.3 Our New DES and 3DES-Compliant Construction

Having defined the above components, let $m = 00000000\text{ffffffff}_x$, and let $k' = k_1|k_2|k_3|k_4$ and $m' = m_1|m_2|m_3|m_4$ be respectively two 256-bit extended keys derived from k by the key schedule.

Definition 1.

$$\text{UES}_k^* = P_{k_1|k_3,\text{DES},\text{DES}} \circ S_m \circ P_{k_2|k_4,\text{DES}^{-1},\text{DES}^{-1}} \circ S_m \circ P_{k_1|k_3,\text{DES},\text{DES}}$$

See figure 1. Then the precise formula to encrypt a plaintext under key k using UES reads as follows:

Definition 2.

$$\text{UES}_k = S_{m_4} \circ T_{m_3|m_3} \circ \text{UES}_k^* \circ T_{m_2|m_2} \circ S_{m_1}$$

See figure 2. This algorithm has two interesting properties. Namely if we set $m' = 0$ and $k' = k$, we have

Property 1.

$$\text{UES}_{k_1|k_2|k_1|k_2}(x_l|x_l) = \text{UES}_{k_1|k_2|k_1|k_2}^*(x_l|x_l) = 3\text{DES}_{k_1,k_2}(x_l)|3\text{DES}_{k_1,k_2}(x_l)$$

and

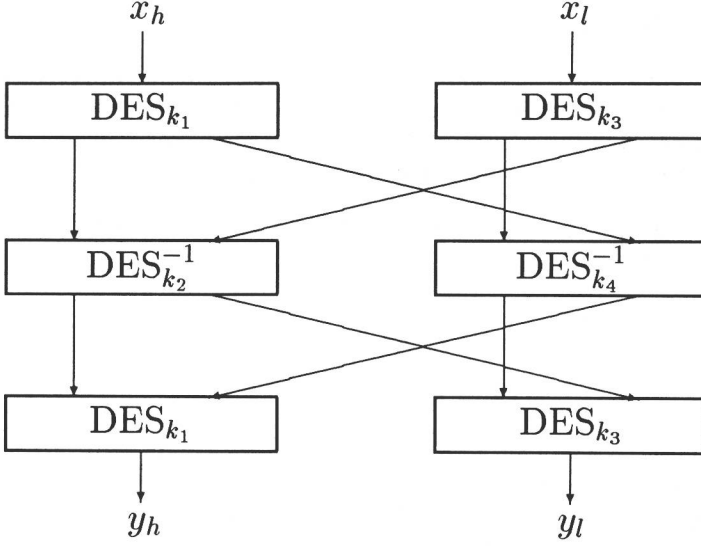


Fig. 1. UES*: Double-block length parallel triple DES

Property 2.

$$UES_{k_1|k_1|k_1|k_1}(x_l|x_l) = UES_{k_1|k_1|k_1|k_1}^*(x_l|x_l) = DES_{k_1}(x_l)|DES_{k_1}(x_l).$$

In addition it operates on 128-bit block messages. This makes the algorithm compatible with the forthcoming AES, and usable in DES or triple-DES mode. Finally, if we set $m = 0$, we can even run two full DES or 3DES encryptions in parallel, which doubles the encryption speed (two blocks are encrypted applying UES* only once).

Note that this scheme enables to construct double block-length encryption algorithms no matter what the underlying cipher is. For simplicity throughout this paper we will consider DES, but any other secure 64-bit block cipher could do the job. We will also focus on generic attacks that do not exploit the internal structure of the component encryption algorithm. Specific attacks such as differential [7] or linear cryptanalysis [20], truncated or higher order differentials [15] do not apply in this context as at least three layers of basic encryption are applied. We also believe that the best way to attack the scheme by a generic method is to try to create inner collisions.

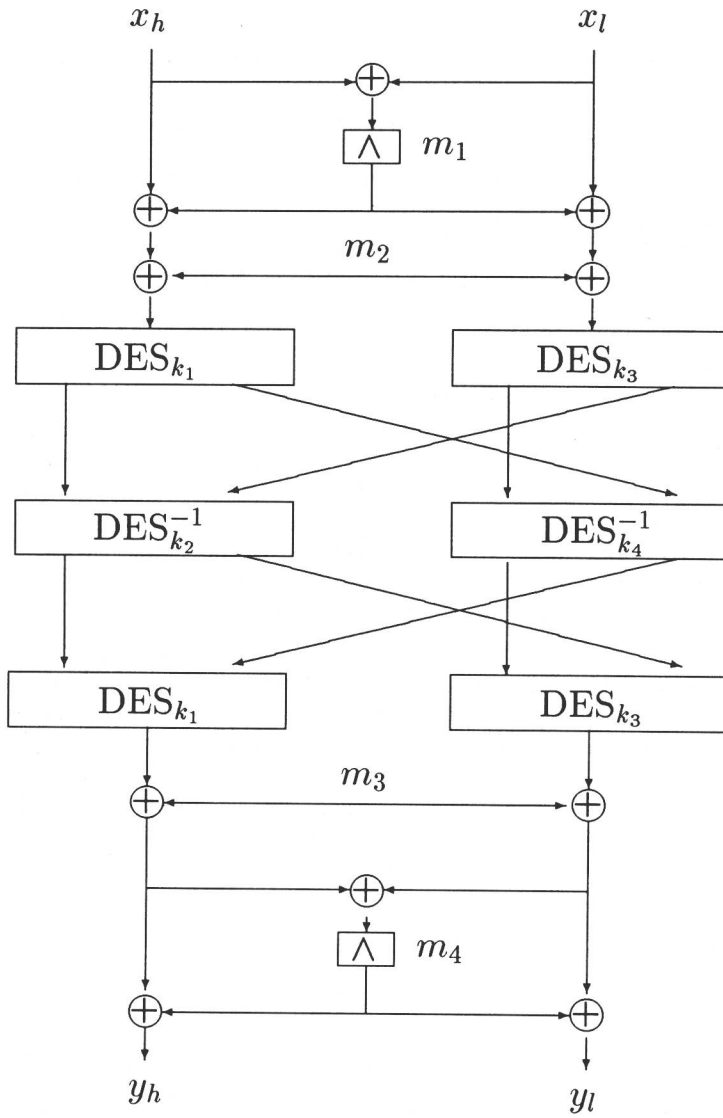


Fig. 2. Encryption with UES.

2.4 The Key-Schedule

In Table 1 below, we summarize in which different modes UES may be used.

Mode	DES	3DES	AES
Key size	56	112	128/192/256
Block size	$k' = k k k k$	$k' = k k$	-
64 bits	$m' = 0, m = 0$	$m' = 0, m = 0$	
Block size	$k' = k k k k$	$k' = k k$	$k' = k_1 k_2 k_3 k_4$
128 bits	$m' = 0, x_h = x_l$	$m' = 0, x_h = x_l$	$m' = m_1 m_2 m_3 m_4$

Table 1. Key-schedule for DES, 3DES and AES modes

The four subkeys and the four submasks used in AES-mode are derived from the user key using DEAL’s key-schedule (for a 256-bit key). The user key is first divided into s subkeys of 64 bits each for $s = 2, 3, 4$. Then expand these s keys to 8 keys by repetition and exor the keys with a new constant for every repetition. Encrypt the expanded list of keys using DES in CBC mode with a fixed key $K = 0123456789abcdef_x$ and with the initial value set to zero. In order to partially allow on the fly key generation, start by deriving m_1 and m_2 , next derive the four DES keys forming k' , and finally derive m_3 and m_4 .

We are aware of Kelsey and Schneier’s [13] key-schedule cryptanalysis of DEAL. It turns out UES may have a very small class of equivalent keys in the 192-bit key case, because of the use of 56-bit keys for the inner DES blocks, whereas 64 bit subkeys are generated by the key-schedule. We also worked out a similar related-key attack with John Kelsey, which recovers the keys in complexity 2^{64} using 2^{33} related keys. However, these attacks apply in a very limited number of practical settings. Developpers should still make sure an attacker is not allowed to choose the keys in such a way.

3 Collision Attacks on Parallel DES

In this section, we consider the variant of UES previously defined as:

$$UES_k^* = P_{k_1|k_3,DES,DES} \circ S_m \circ P_{k_2|k_4,DES^{-1},DES^{-1}} \circ S_m \circ P_{k_1|k_3,DES,DES}$$

We will show that this straightforward way of doubling the block size is not secure because a collision attack can be mounted against it (this phenomenon has been independently observed by Knudsen [17]). This is due to the fact that the construction is not a multipermutation. In other words, it may very well happen that if half of the input bits have a fixed value, half of the output bits also have, which would not be the case if the multipermutation property had been satisfied [22]. However, our intention is to prove that we can nevertheless use the structure if the input and output bits to this variant are unknown to the attacker. Therefore we begin by showing where the problem comes from, and justify our additional layers of swapping and masking in the final version of UES.