

**Microprocessors
and
Microcomputers
Hardware and Software**

RONALD J. TOCCI
LESTER P. LASKOWSKI

Microprocessors and Microcomputers Hardware and Software

RONALD J. TOCCI

Monroe Community College

LESTER P. LASKOWSKI

Monroe Community College

Library of Congress Cataloging in Publication Data

TOCCI, RONALD J

Microprocessors and microcomputers.

Includes index.

1. Microprocessors. 2. Microcomputers.

I. Laskowski, Lester, joint author. II. Title.

QA76.5.T556 001.6'4'04 78-26831

ISBN 0-13-581330-1

Editorial/production supervision and interior
design by Don Rosanelli and Gary Samartino
Cover design by Edsal Enterprises
Manufacturing buyer: Gordon Osbourne

© 1979 by Prentice-Hall, Inc., Englewood Cliffs, N.J. 07632

All rights reserved. No part of this book
may be reproduced in any form or
by any means without permission in writing
from the publisher.

Printed in the United States of America

10 9 8 7 6 5 4 3 2

PRENTICE-HALL INTERNATIONAL, INC., *London*
PRENTICE-HALL OF AUSTRALIA PTY. LIMITED, *Sydney*
PRENTICE-HALL OF CANADA, LTD., *Toronto*
PRENTICE-HALL OF INDIA PRIVATE LIMITED, *New Delhi*
PRENTICE-HALL OF JAPAN, INC., *Tokyo*
PRENTICE-HALL OF SOUTHEAST ASIA PTE. LTD., *Singapore*
WHITEHALL BOOKS LIMITED, *Wellington, New Zealand*

Microprocessors
and
Microcomputers
Hardware and Software

Preface

This book was written to present a broad spectrum of readers with a practical introduction to the relatively new world of microprocessors and microcomputers. It should prove to be useful to the computer novice, technical student, and practicing engineer alike. A comprehensive review of digital principles and circuits is provided for readers with a minimum background.

As the title suggests, this book concentrates on the fundamentals of microprocessor-based systems and is *not* intended to be a survey of the numerous microprocessors and microprocessor applications. The authors have chosen to emphasize the ideas and principles common to all microprocessor systems. The only deviation from this approach is the chapter on programming, where the principles are best described using an actual microprocessor's instruction set. Once the reader understands these principles, it is a relatively easy task to extend this understanding to any microprocessor.

The text is divided into three sections: review material, hardware, and programming. Chapters One and Two provide a thorough review of terminology, number systems, and digital circuits from basic gates to memory chips. Chapters Three through Six deal principally with computer structure and hardware, with some programming concepts introduced to show how the software and hardware work together. These chapters cover microcomputer structure, internal microprocessor organization, memory interfacing, and input/output interfacing. Chapter Seven is a detailed treatment of microcomputer programming on a machine language level, with some elements of assembly language.

The text includes several valuable learning aids to facilitate the understanding of important concepts: (1) numerous thoroughly explained illustrative examples; (2) clear, uncomplicated diagrams and flowcharts; and (3) extensive glossaries of new terms at the end of each chapter. In addition, the interrelationship of hardware and software is emphasized throughout the text so that the reader can develop a solid understanding of the complete microprocessor system which he/she can easily build on.

The authors wish to extend their thanks to several special people for their valuable assistance in preparing the original manuscripts. Through the efforts of Sally Altobello and our wives, Cathy and Sandy, the manuscript was completed almost as fast as we could write it and it was essentially error-free. We also would like to thank each other for performing this formidable task with virtually no areas of disagreement and with a spirit of cooperation that made the task a pleasant one.

RONALD J. TOCCI

LESTER P. LASKOWSKI

Contents

Preface

ix

PART 1: INTRODUCTORY TOPICS

1 Number Systems and Codes

3

- 1.1 Digital Number Systems 3**
- 1.2 Codes 12**
- 1.3 Binary Arithmetic 16**
- 1.4 Addition Using Signed Numbers 18**
- 1.5 Subtraction in 2's-Complement System 20**
- 1.6 Multiplication of Binary Numbers 21**
- 1.7 Binary Division 22**
- 1.8 BCD Arithmetic Operations 23**
- 1.9 Hexadecimal Arithmetic 24**
- Glossary 25**

2 Digital Circuits**26**

- 2.1 Parallel and Serial Transmission 27
- 2.2 Logic Gates 28
- 2.3 Logic Equivalences 30
- 2.4 Integrated-Circuit Logic Families 30
- 2.5 Tristate Logic (TSL) 32
- 2.6 Flip-flops 33
- 2.7 Clock Signals 34
- 2.8 Clocked Flip-flops 35
- 2.9 Synchronous and Asynchronous FF Inputs 38
- 2.10 Binary Counters 39
- 2.11 FF Registers 42
- 2.12 IC Registers 45
- 2.13 Data Bus 49
- 2.14 Decoders 53
- 2.15 Encoders 55
- 2.16 Multiplexers (Data Selectors) 55
- 2.17 Demultiplexers (Data Distributors) 57
- 2.18 Arithmetic Circuits 57
- 2.19 Memory Devices 58
- 2.20 Semiconductor Memories—Organization 59
- 2.21 Semiconductor Memories—Operating Speed 63
- 2.22 Semiconductor Memories—Types 64
- 2.23 RAMs—Static and Dynamic Modes 67
- 2.24 Expanding Memory Size 67
- Glossary 72

3 Introduction to Computers**75**

- 3.1 What Can Computers Do? 75
- 3.2 How Many Types of Computers Are There? 78
- 3.3 How Do Computers Think? 78
- 3.4 Basic Computer System Organization 81
- 3.5 Computer Words 83
- 3.6 Binary Data Words 84
- 3.7 Coded Data Words 85
- 3.8 Instruction Words 87
- 3.9 Simple Program Example 90
- 3.10 Computer Operating Cycles 93
- 3.11 Input/Output (I/O) 98
- 3.12 Hardware and Software 99
- Glossary 99

PART 2: MICROCOMPUTER HARDWARE

4	<i>Microcomputer Structure and Operation</i>	103
4.1	Basic μ C Elements	103
4.2	Why μ Ps and μ Cs?	105
4.3	Typical μ C Structure	105
4.4	READ and WRITE Operations	111
4.5	Address-Allocation Techniques	115
4.6	Address-Decoding Techniques	119
	Glossary	126
5	<i>The Microprocessor—Heart of the Microcomputer</i>	128
5.1	Timing and Control Section	129
5.2	Register Section	131
5.3	Arithmetic/Logic Unit (ALU)	142
5.4	Microprocessor Comparison	148
	Glossary	154
6	<i>Input/Output and Interfacing</i>	156
6.1	Some Basic Terms	156
6.2	Some Examples of I/O	157
6.3	Input/Output Alternatives	159
6.4	CPU Initiated—Unconditional I/O Transfer	160
6.5	CPU Initiated—Conditional (Polled) I/O Transfer	166
6.6	Device-Initiated I/O Transfer—Interrupts	172
6.7	Device-Initiated I/O Transfer—Direct Memory Access	182
6.8	Practical Interface Considerations	184
6.9	Asynchronous Serial Data Transmission	190
6.10	Parallel/Serial Interface—The UART	193
6.11	Motorola 6850 UART	197
6.12	Hex Keyboard Input Device	207
6.13	ASCII Keyboards	213
6.14	Teletypewriters	213
6.15	Magnetic-Tape Storage	215
6.16	Floppy Disks (or Diskettes)	220
6.17	TVT or Video Display	224
	Glossary	231

PART 3: MICROCOMPUTER SOFTWARE

7	<i>Microcomputer Programming</i>	237
7.1	Programming Languages	237
7.2	Microprocessor Instruction Sets	243
7.3	6502 Registers	244
7.4	6502 Instruction Set and Address Modes	249
7.5	Machine Status Control Instructions	254
7.6	Arithmetic Instructions	255
7.7	Logical Instructions	259
7.8	Shift and Rotate Instructions	265
7.9	Decrement/Increment Instructions	267
7.10	Compare Instructions	268
7.11	Data-Transfer Instructions	270
7.12	Unconditional Jump (JMP)	274
7.13	Conditional Branch Instructions	275
7.14	Subroutines	282
7.15	Indexed Addressing Modes	287
7.16	Indirect Addressing	293
7.17	Timing Loops	294
7.18	Multibyte Arithmetic Operations	301
7.19	6502 Address Vectors	302
7.20	Writing a Program	303
7.21	Final Comments	310
	Glossary	312
Appendix	<i>6502 Instruction Set and Op Codes</i>	314
Index		317

PART 1

Introductory Topics

1

Number Systems and Codes

Computers of all sizes have one thing in common—they handle *numbers*. In digital computers, these numbers are represented by binary digits. A *binary digit* is a digit that can only take on the values of 0 or 1, and no other value. The major reason why binary digits are used in computers is the simplicity with which electrical, magnetic, and mechanical devices can represent binary digits. Because the term “binary digit” is used so often in computer work, it is commonly abbreviated to *bit*. Henceforth, we shall use the latter form.

1.1 DIGITAL NUMBER SYSTEMS

Although actual computer operations use the binary number system, several other number systems are used to communicate with computers. The most common are the decimal, octal, and hexadecimal systems.

Decimal System

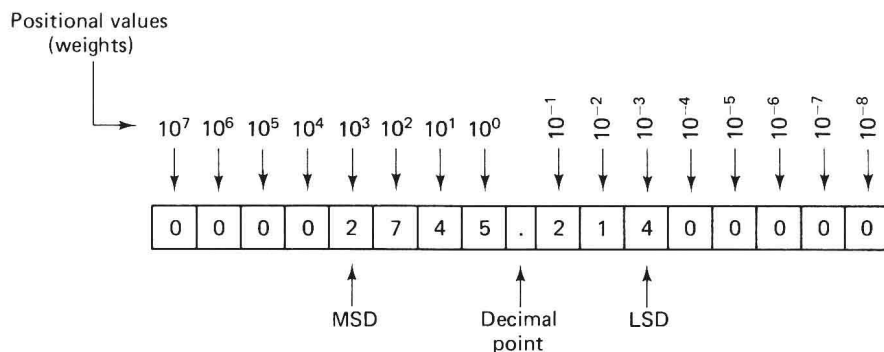
The *decimal system* is composed of the 10 symbols or digits: 0, 1, 2, 3, 4, 5, 6, 7, 8, and 9; using these symbols, we can express any quantity. The decimal system, also called the *base 10* system, because it has 10 digits, has evolved naturally as a result of the fact that man has 10 fingers. In fact, the word “digit” is the Latin word for “finger.”

The decimal system is a *positional-value system*, in which the value of a digit depends on its position. For example, consider the decimal number 453. We know that the digit 4 actually represents 4 *hundreds*, the 5 represents 5 *tens*, and the 3 represents 3 *units*. In essence, the 4 carries the most weight of the three digits; it is referred to as the *most significant digit* (MSD). The 3 carries the least weight and is called the *least significant digit* (LSD).

The various positions relative to the decimal point carry weights that can be expressed as powers of 10. This is illustrated below, where the number 2745.214 is represented. The decimal point separates the positive powers of 10 from the negative powers. The number 2745.214 is thus equal to

$$(2 \times 10^{+3}) + (7 \times 10^{+2}) + (4 \times 10^{+1}) + (5 \times 10^{+0}) \\ + (2 \times 10^{-1}) + (1 \times 10^{-2}) + (4 \times 10^{-3})$$

In general, any number is simply the sum of the products of each digit value times its positional value:



Decimal Counting

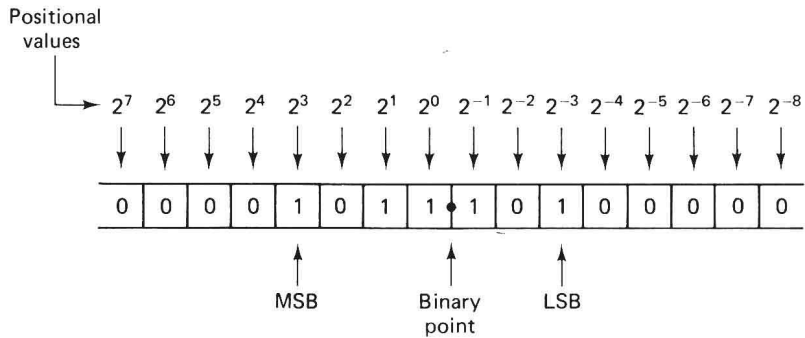
The number 9 is the largest digit value in the decimal system. Thus, as we are counting in decimal, a given digit will progress upward from 0 to 9. After 9, it goes back to 0 and the next higher digit position is incremented (goes up by 1). For example, note the digit changes in the following counting sequences: 25, 26, 27, 28, 29, 30; 196, 197, 198, 199, 200.

For a given number of digits, N , we can count decimal numbers from zero up to $10^N - 1$. In other words, with N digits we can have 10^N different numbers, including zero. To illustrate, with three decimal digits, we can count from 000 to 999, a total of 1000 different numbers.

Binary System

In the *binary system* there are only two symbols or possible digit values, 0 and 1. Even so, this *base 2 system* can be used to represent any quantity that can be represented in decimal or other number systems. In general, though, it will take a greater number of binary digits to express a given quantity.

All the statements made earlier concerning the decimal system are equally applicable to the binary system. The binary system is also a positional-value system, wherein each bit has its own value or weight expressed as powers of 2, as follows:



In the number expressed above, the positions to the left of the *binary point* (counterpart of the decimal point) are positive powers of 2 and the positions to the right of the binary point are negative powers of 2. The binary number 1011.101 is represented above, and its equivalent decimal value can be found by taking the sum of the products of each bit value (0 or 1) times its positional value.

$$\begin{aligned}
 1011.101_2 &= (1 \times 2^3) + (0 \times 2^2) + (1 \times 2^1) + (1 \times 2^0) \\
 &\quad + (1 \times 2^{-1}) + (0 \times 2^{-2}) + (1 \times 2^{-3}) \\
 &= 8 + 0 + 2 + 1 + 0.5 + 0 + 0.125 \\
 &= 11.625_{10}
 \end{aligned}$$

Notice in the preceding operation that subscripts (2 and 10) were used to indicate the base in which the particular number is expressed. This convention is used to avoid confusion whenever more than one number system is being employed.

Binary Counting

The largest digit value in the binary system is 1. Thus, when counting in binary, a given digit will progress from 0 to 1. After it reaches 1, it recycles back to 0 and

$$\begin{aligned}
 1 \ 0 \ 1 . 1 \ 0 \ 1 &= 2^2 + 2^0 + 2^{-1} + 2^{-3} \\
 &= 4 + 1 + .5 + .125 \\
 &= 5.625_{10}
 \end{aligned}$$

The following conversions should be performed and verified by the reader:

1. $1 \ 0 \ 0 \ 1 \ 1 \ 0_2 = 38_{10}$.

2. $0 . 1 \ 1 \ 0 \ 0 \ 0 \ 1_2 = 0.765625_{10}$.

3. $1 \ 1 \ 1 \ 1 \ 0 \ 0 \ 1 \ 1 . 0 \ 1 \ 0 \ 1_2 = 243.3125_{10}$.

There are several ways to convert a decimal number to its equivalent binary system representation. A method that is convenient for small numbers is just the reverse of the process described in the preceding section. The decimal number is simply expressed as a sum of powers of 2 and then 1s and 0s are written in the appropriate bit positions. To illustrate:

$$\begin{aligned}
 13_{10} &= 8 + 4 + 1 = 2^3 + 2^2 + 0 + 2^0 \\
 &= 1 \quad 1 \quad 0 \quad 1_2
 \end{aligned}$$

Another example:

$$\begin{aligned}
 25.375_{10} &= 16 + 8 + 1 + .25 + .125 \\
 &= 2^4 + 2^3 + 0 + 0 + 2^0 + 0 + 2^{-2} + 2^{-3} \\
 &= 1 \quad 1 \quad 0 \quad 0 \quad 1 \quad . \quad 0 \quad 1 \quad 1_2
 \end{aligned}$$

For larger decimal numbers, the method above is laborious. A more convenient method entails separate conversion of the integer and fractional parts. For example, take the decimal number 25.375, which was converted above. The first step is to convert the integer portion, 25. This conversion is accomplished by repeatedly *dividing* 25 by 2 and writing down the remainders after each division until a quotient of zero is obtained.

