

世界微处理器与微控制器 第四卷

WORLD MICROPROCESSOR & MICROCONTROLLER Volume IV

—— 电路数据手册



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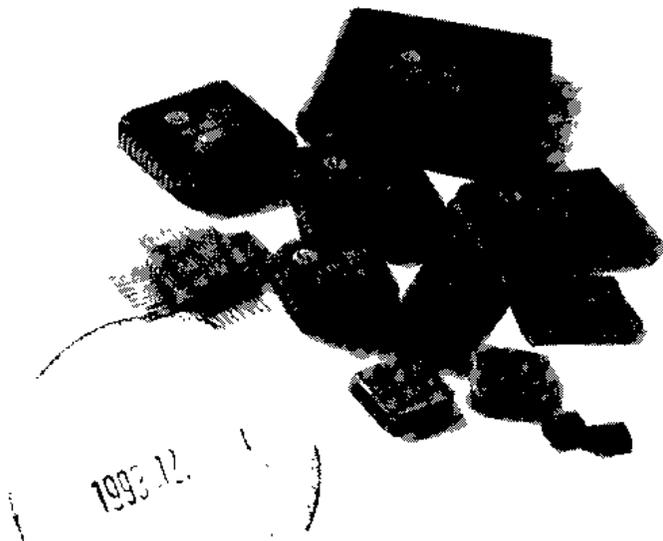
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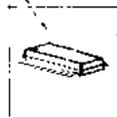
14 Pin DIP (DIP)



25 Pin Surface Mount (SOIC)



28 Pin Quad Flat Pack (QFP)



50 Pin Package (NIP)



50 Pin Package (NIP)



Ceramic Loaded Package (CIP)



28 Terminal Ceramic LCC (LCC)



Linear Chip Carrier Package (LCC)



Tape Automated Bonding (TAB)



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内 容 提 要

本书分为上、下二篇,上篇收集了 HMC、MITSUBISHI、NS、OKI、TOSHIBA、SAMSUNG 等六家世界知名厂商生产的四位微处理器及微控制器。下篇作为对前三卷书内容的补充和完善,收集了 INTEL、FUJITSU、MITSUBISHI、MOTOROLA、NEC、OKI、PHILIPS、SGS—THOMSON 等八家世界知名厂商生产的微控制器。全书资料均取材于各厂商的 OEM 手册,数据翔实准确,资料新颖全面,并直接采用原文,避免因翻译而引起的失真。为了便于读者快速阅读和浏览,在每章的开头,都有一个简单的中文简介,言简意赅地描述了本章所介绍的微处理器或微控制器的主要特性。

本书可供科研院所的科研人员、大专院校师生在科研学习过程中作为参考书使用,也可供系统维护及维修人员、硬件营销人员参考。

世界微处理器与微控制器

第四卷

四位机与微控制器

顺年选编

责任编辑 韩瑞宗

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编 者 的 话

随着计算机产业的蓬勃发展,作为计算机心脏的微处理器的应用越来越广泛,从家用电器到航空航天事业,无处不用到微处理器和微控制器。而国内的科研人员面对浩若繁星的国外各厂商的微处理器与微控制器,常有茫然不知所措的感觉。有鉴于此,为了帮助国内科研人员对世界各大厂商生产的微处理器与微控制器有一个全面的了解,北京瑞特电子技术公司集多年器件、信息资料服务之经验,凭借与国外各大厂商之密切关系,搜集国外最新资料,与电子工业出版社通力合作,编纂而成这套《电路数据手册》,以奉献给国内读者。本书编排思想以最新、最全、最实用为主旨,紧跟国际潮流,适应国内需求,力求能解决读者在工作中遇到的实际问题,在选型上力求全而新,以扩大读者的视野,在此基础上,侧重国内外流行的微处理器与微控制器,并注重当前流行的“绿色浪潮”,较多地选用了一些低功耗,高集成度,小型化的型号,另外,根据国内客观环境和市场调研,本书也注意选用一些军用、工业用抗恶劣环境的高性能微处理器与微控制器。

另外,在此书成书之前,我们作了大量的调查研究工作,广泛听取了用户和科研人员的意见和建议,吸取了国内其他单位编写同类书刊的经验,根据此书读者的知识结构和外语水平,在内容上大胆采用OEM手册中的原文,以避免因翻译而引起失真和笔误。这样作会给部分读者造成阅读上的困难,在此深表抱歉。

因时间仓促,且编者能力有限,本书必有不少不尽人意之处,望各界同仁给我们提出宝贵意见和建议,以期我们进一步改进。

编 者

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SAMSUNG(三星公司)	
TOSHIBA(东芝公司)	

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FUJITSU(富士通公司)	
MITSUBISHI(三菱公司)	
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NEC(日本电气公司)	
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第一章

华隆公司(HMC)

本章介绍 HMC 公司四位机,其应用领域很广泛,如家用环境、遥控器、消费产品和玩具等。
HMC 公司四位机的基本特点如下:

- 四位结构
- 片内有 ROM,容量从 2KB 到 8KB 不等
- 片内有 RAM,容量从 $52 \times 4\text{bits}$ 到 $372 \times 4\text{bits}$ 不等
- 带有输入口、输出口和双向输入/输出口,位数因不同的型号而各有所不同
- 片内有 12 位定时/计数器
- 具有丰富的指令集,指令数为 109 或 110

HMC 公司四位机的型号一览表如下:

型 号	ROM	RAM	I/P	O/P	I/O	TIMER	E. INT	I. INT
HM75200	2K	52	4	4	8	2	2	2
HM75220	2K	116	4	8	6	2	1	3
HM75261	2K	116	4	4	12	2	2	3
HM75290	2K	116	5	8	15	3	2	3
HM75400	4K	244	5	8	23	3	2	4
HM75462	4K	244	4	4	24	3	2	3
HM75491	4K	244	5	1	15	3	2	3
HM75492	4K	756	5	3	27	3	2	4
HM75495	4K	244	4	1	20	3	2	3
HM75496	4K	756	4	3	27	3	2	4
HM75863	8K	372	4	0	20	3	2	3

注: I/P——输入口, O/P——输出口, E. INT——外部中断, I. INT——内部中断

GENERAL DESCRIPTION

HM75200 is an advanced single chip CMOS 4-bit micro-controller. It contains 2K-byte ROM, 64-nibble RAM, 4-bit ALU, 13-level subroutine nesting, 22-stage time base, one independent 12-bit timer/counters and one data pointer (DP) for the kernel function, and the HM75200 also contains 5 interrupt sources, 10 I/O ports (including 1 output ports for LED driving, 1 input ports and 2 ports for bidirection I/O) and one high frequency clock output for modulating infrared signal.

Except low-power consumption and high speed, HM75200 also has sleep and hold mode operation for the power saving function.

HM75200 is suitable for application in family appliance, remote control, consumer products and toy controller.

FEATURES

- Operation voltage : 4.5V to 6V (clock frequency : 32 KHz to 8 MHz)
2.7V to 3.3V (clock frequency : 32 KHz to 4.19 MHz)
- Clock source : Single clock system for RC , Crystal or external clock source, decided by mask option.
- Instruction set : 109 powerful instructions.
- Instruction cycle time : Up to 1 μ s (for 8 MHz).
- ROM capacity : 2K x 8 bits.
- RAM capacity : 52 x 4 bits.
- Input port : 1 port (4 bits)(pull-up or pull-down resistor available decided by mask option).
- Output port : 1 port (4 bits) (open drain , high current for LED driving)
- Bidirection I/O port : 2 ports (8 bits) (push-pull or open-drain decided by mask option)
- 12 bits timer/counter : One 12 bits timer/counter is programmable for timer, event counter and pulse width measurement mode.
- One data pointer (DP) which can be the address pointer for the program ROM.
- Built-in time base counter : 22 stages.
- Subroutine nesting : Up to 13 levels.
- Interrupt : External 2 input interrupt sources.
Internal 1 Timer overflow interrupt.
1 Time base interrupt.
- High frequency clockout: Programmable high frequency clock output for modulating infrared signal.
- Power saving function . Sleep function, CPU hold internal state and stop oscillator working.
Hold function, CPU hold internal state and oscillator still working.
- Package type : HM75200H Chip form 22 pads.
 HM75200AP DIP 18 pins.
 HM75200BP DIP 20 pins.
 HM75200CP DIP 22 pins.

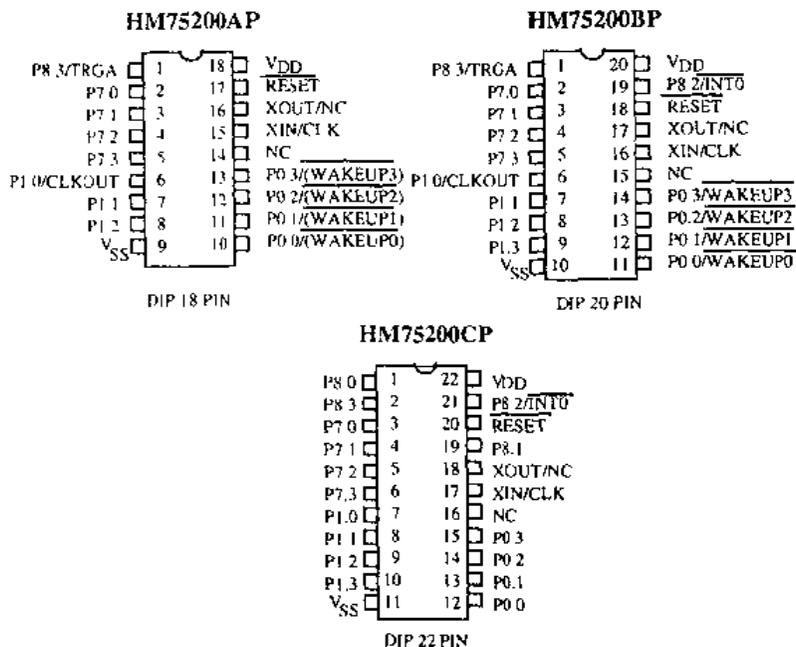


HM75200 4-BIT MICROCONTROLLER

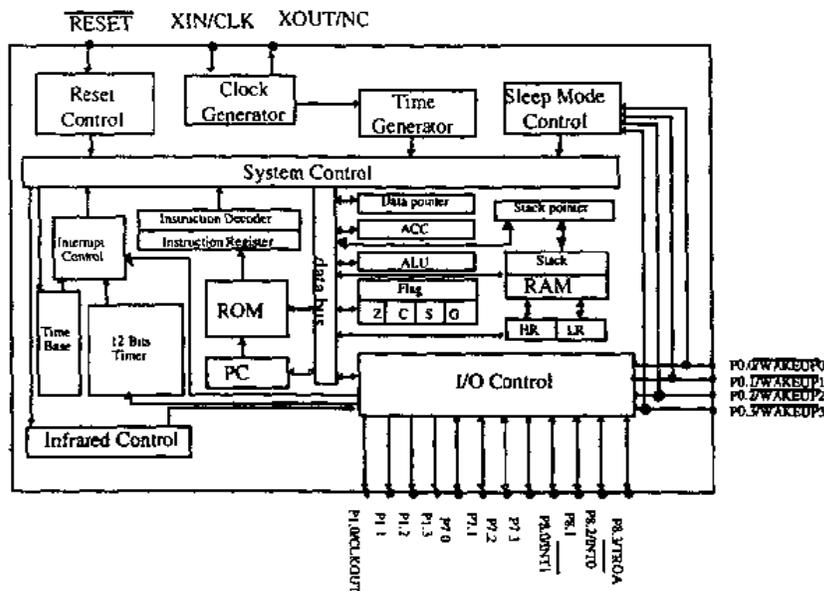
APPLICATION

HM75200 is suitable for application in family appliance, remote control, consumer product and toy controller.

PIN CONFIGURATIONS



FUNCTIONAL BLOCK DIAGRAM





PIN DESCRIPTIONS

Symbol	H	BP	AP	CP	Function
V _{DD}	21	20	18	22	Power supply (+)
V _{SS}	10	10	9	11	Power supply (-)
RESET	19	18	17	20	System reset input terminal
NC	15	15	14	16	Reserved
XIN/CLK	16	16	15	17	Resonator (crystal) or RC connecting pin Osc. type decided by mask option
XOUT/NC	17	17	16	18	NC for RC osc type OSC. output pin for resonator (crystal) type
P0.0/WAKEUP0	11	11	10	12	4 bits input port (pull-up, pull-down by mask option)
P0.1/WAKEUP1	12	12	11	13	
P0.2/WAKEUP2	13	13	12	14	
P0.3/WAKEUP3	14	14	13	15	
P1.0/CLKOUT	6	6	6	7	1 bit output pin (open-drain, high current out put pin); Clock output pin for infrared signal
P1.1	7	7	7	8	3 bits output pin (open-drain, high current output port)
P1.2	8	8	8	9	
P1.3	9	9	-	10	
P7.0	2	2	2	3	4 bits bidirection I/O port (push-pull, open-drain by mask option)
P7.1	3	3	3	4	
P7.2	4	4	4	5	
P7.3	5	5	5	6	
P8.0/INT1	22	-	-	1	1 bit bidirection I/O pin with external interrupt input pin for INT1. (push-pull, open-drain by mask option)
P8.1	18	-	-	19	1 bit bidirection I/O pin (push-pull, open-drain by mask option)
P8.2/INT0	20	19	-	21	1 bit bidirection I/O pin with external interrupt input pin for INT0. (push-pull, open-drain by mask option)
P8.3/TRGA	1	1	1	2	1 bit bidirection I/O pin with external timer/counter input pin for timer A (push-pull, open-drain by mask option)

ABSOLUTE MAXIMUM RATINGS

Items	Sym.	Ratings	Conditions
Supply Voltage	V_{DD}	-0.5V to 7V	
Input Voltage	V_{IN}	-0.5V to $V_{DD}+0.5V$	
Output Voltage	V_O	-0.5V to $V_{DD}+0.5V$	Except open-drain
		-0.5V to 7V	Open-drain
Output Current	I_O	3.2mA	Port P7,P8
Power Dissipation	P_D	300mW	$T_{opr}=70^{\circ}C$
Operating Temperature	T_{opr}	-30°C to 70°C	
Storage Temperature	T_{stg}	-55°C to 125°C	

RECOMMENDED OPERATING CONDITIONS

Items	Sym.	Ratings	Condition
Supply Voltage	V_{DD}	4.5V to 6V 2.4V to 3.3V	32 KHz < F_c < 8 MHz crystal 32 KHz < F_c < 4.19MHz crystal
		2.4V to 6V	32KHz < F_c < 4.19 MHz by RC osc
Input Voltage	V_{IH}	$0.75 * V_{DD}$ to V_{DD}	Hysteresis input, \overline{RESET} , P8, P0; $V_{DD} \geq 4.5V$
		$0.70 * V_{DD}$ to V_{DD}	Except hysteresis input; $V_{DD} \geq 4.5V$
		$0.90 * V_{DD}$ to V_{DD}	$V_{DD} < 4.5V$
Input Voltage	V_{IL}	$0.25 * V_{DD}$ to V_{DD}	Hysteresis input, \overline{RESET} , P8, P0; $V_{DD} \geq 4.5V$
		$0.3 * V_{DD}$ to V_{DD}	Except hysteresis input; $V_{DD} \geq 4.5V$
		$0.1 * V_{DD}$ to V_{DD}	$V_{DD} < 4.5V$
Operating Frequency	F_c	32K to 4.19 MHz 32K to 8 MHz	CLK (RC OSC) XIN, XOUT (crystal)

DC ELECTRICAL CHARACTERISTICS ($V_{DD}=3\pm 0.5V$, $V_{SS}=0V$, $T_{opr}=-30^{\circ}C$ to $60^{\circ}C$)

Parameters	Sym.	Min.	Typ.	Max.	Unit	Conditions
Supply current	I_{DD}	-	0.2	1	mA	$V_{DD}=3.3V, F_c=960KHz$
		-	8	15	μA	$V_{DD}=3.3V, F_c=32KHz$
		-	0.5	10	μA	$V_{DD}=3.3V$, Sleep mode
		-	50	-	μA	$V_{DD}=3V, F_c=250KHz$
Hysteresis voltage	V_{HYS}	-	0.5	-	V	\overline{RESET} , P8
Input current	I_{IN}	-	-	± 2	μA	Port P0, \overline{RESET} : $V_{DD}=3.3V$, $V_{IN}=3.3/0V$
		-	-	± 2	μA	Open-drain: $V_{DD}=3.3V$, $V_{IN}=3.3/0V$
	I_{IL}	-	-	-1	mA	Push-pull: $V_{DD}=3.3V$, $V_{IN}=0.4V$



HM75200 4-BIT MICROCONTROLLER

Parameters	Sym.	Min.	Typ.	Max.	Unit	Conditions
Output voltage	V_{OH}	2.0	-	-	V	Push-pull; $V_{DD}=2.7V, I_{OH}=-60\mu A$
	V_{OL}	-	-	0.3	V	Except port P1, $V_{DD}=2.7V, I_{OL}=0.9mA$
Output current	I_{OL}	9	-	-	mA	Port P1; $V_{DD}=2.7V, V_{OL}=0.9V$
Leakage current	I_{LO}	-	-	2	μA	Open-drain; $V_{DD}=3.3V, V_{OL}=3.3V$
Input resistor	R_{IN}	100	200	300	Kohm	Port P0
		300	600	900	Kohm	RESET

$V_{DD}=5\pm 0.5V, V_{SS}=0V, T_{opr}=-30^{\circ}C$ to $60^{\circ}C$)

Parameters	Sym.	Min.	Typ.	Max.	Unit	Conditions
Supply current	I_{DD}	-	1.0	5	mA	$V_{DD}=5.5V, F_c=4.19MHz$
		-	0.5	2	mA	$V_{DD}=5.5V, F_c=960KHz$
		-	0.5	10	μA	$V_{DD}=5.5V, \text{Sleep mode}$
		-	0.25	-	mA	$V_{DD}=4.5V, F_c=500KHz$
Hysteresis voltage	V_{HYS}	-	0.5	-	V	RESET, P8
Input current	I_{IN}	-	-	± 2	μA	Port P0, RESET; $V_{DD}=5.5V, V_{IN}=5.5/0V$
		-	-	± 2	μA	Open-drain : $V_{DD}=5.5V, V_{IN}=5.5/0V$
		-	-	-2	mA	Push-pull : $V_{DD}=5.5V, V_{IN}=0.4V$
Output voltage	V_{OH}	2.4	-	-	V	Push-pull : $V_{DD}=4.5V, I_{OH}=-250\mu A$
	V_{OL}	-	-	0.4	V	Except port P1, $V_{DD}=4.5V, I_{OL}=2.0mA$
Output current	I_{OL}	20	-	-	mA	Port P1, $V_{DD}=4.5V, V_{OL}=1.0V$
Leakage current	I_{LO}	-	-	2	μA	Open-drain : $V_{DD}=5.5V, V_o=5.5V$
Input resistor	R_{IN}	30	90	150	Kohm	Port P0
		100	300	450	Kohm	RESET

APPLICATION CIRCUIT

